

### 3 System address map

#### 3.2 Address map

The address maps define which address ranges are valid or invalid for the respective segment. The access type column describes the error status location if the access is not permitted.

For the detailed address space of a specific functional block (module), please to the specific user manual chapter describing that block.

##### 3.2.1 Segment 0

**Table 7 Address map of segment 0**

Address range	Size	Description	Access type	
			Read	Write
0000 0000 <sub>H</sub> - 0FFF FFFF <sub>H</sub>	-	Reserved	SRIBE <sup>1)</sup>	SRIBE <sup>1)</sup>

<sup>1)</sup> Any CPU load or store operation accessing 0000 0000<sub>H</sub> will trap

##### 3.2.2 Segment 1

**Table 8 Address map of segment 1**

Address range	Size	Description	Access type	
			Read	Write
1000 0000 <sub>H</sub> - 1003 BFFF <sub>H</sub>	240 Kbyte	CPUcs.DSPR	Access	Access
1003 C000 <sub>H</sub> - 1003 FFFF <sub>H</sub>	16 Kbyte	CPUcs.DSPR (extension) or CPUcs.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1004 0000 <sub>H</sub> - 100F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1010 0000 <sub>H</sub> - 1010 FFFF <sub>H</sub>	64 Kbyte	CPUcs.PSPR	Access	Access
1011 0000 <sub>H</sub> - 1011 7FFF <sub>H</sub>	32 Kbyte	CPUcs.PSPR (extension) or CPUcs.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1011 8000 <sub>H</sub> - 1011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1012 0000 <sub>H</sub> - 1012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1013 0000 <sub>H</sub> - 1013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1013 8000 <sub>H</sub> - 1013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1014 0000 <sub>H</sub> - 1014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1015 0000 <sub>H</sub> - 1015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1015 8000 <sub>H</sub> - 1015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1016 0000 <sub>H</sub> - 1016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1017 0000 <sub>H</sub> - 1017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1017 8000 <sub>H</sub> - 1017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1018 0000 <sub>H</sub> - 1018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1019 0000 <sub>H</sub> - 1019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access

(table continues...)

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**Table 8** (continued) Address map of segment 1

Address range	Size	Description	Access type	
			Read	Write
1019 8000 <sub>H</sub> - 1019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
101A 0000 <sub>H</sub> - 101A FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101B 0000 <sub>H</sub> - 101B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101B 8000 <sub>H</sub> - 101B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
101C 0000 <sub>H</sub> - 101C FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101D 0000 <sub>H</sub> - 101D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101D 8000 <sub>H</sub> - 101D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
101E 0000 <sub>H</sub> - 101E FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101F 0000 <sub>H</sub> - 101F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101F 8000 <sub>H</sub> - 101F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1020 0000 <sub>H</sub> - 1020 17FF <sub>H</sub>	-	CPUcs.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1020 1800 <sub>H</sub> - 102F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1030 0000 <sub>H</sub> - 1030 2FFF <sub>H</sub>	-	CPUcs.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1030 3000 <sub>H</sub> - 1FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.3 Segment 2

**Table 9 Address map of segment 2**

Address range	Size	Description	Access type	
			Read	Write
2000 0000 <sub>H</sub> - 2003 BFFF <sub>H</sub>	240 Kbyte	CPU5.DSPR	Access	Access
2003 C000 <sub>H</sub> - 2003 FFFF <sub>H</sub>	16 Kbyte	CPU5.DSPR (extension) or CPU5.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
2004 0000 <sub>H</sub> - 200F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
2010 0000 <sub>H</sub> - 2010 FFFF <sub>H</sub>	64 Kbyte	CPU5.PSPR	Access	Access
2011 0000 <sub>H</sub> - 2011 7FFF <sub>H</sub>	32 Kbyte	CPU5.PSPR (extension) or CPU5.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
2011 8000 <sub>H</sub> - 2011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
2012 0000 <sub>H</sub> - 2012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
2013 0000 <sub>H</sub> - 2013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
2013 8000 <sub>H</sub> - 2013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
2014 0000 <sub>H</sub> - 2014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
2015 0000 <sub>H</sub> - 2015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
2015 8000 <sub>H</sub> - 2015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
2016 0000 <sub>H</sub> - 2016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
2017 0000 <sub>H</sub> - 2017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
2017 8000 <sub>H</sub> - 2017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
2018 0000 <sub>H</sub> - 2018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
2019 0000 <sub>H</sub> - 2019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
2019 8000 <sub>H</sub> - 2019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
201A 0000 <sub>H</sub> - 201A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
201B 0000 <sub>H</sub> - 201B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
201B 8000 <sub>H</sub> - 201B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
201C 0000 <sub>H</sub> - 201C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
201D 0000 <sub>H</sub> - 201D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
201D 8000 <sub>H</sub> - 201D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
201E 0000 <sub>H</sub> - 201E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU5.PSPR	Access	Access
201F 0000 <sub>H</sub> - 201F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
201F 8000 <sub>H</sub> - 201F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
2020 0000 <sub>H</sub> - 2020 17FF <sub>H</sub>	-	CPU5.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
2020 1800 <sub>H</sub> - 202F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 9** (continued) Address map of segment 2

Address range	Size	Description	Access type	
			Read	Write
2030 0000 <sub>H</sub> - 2030 2FFF <sub>H</sub>	-	CPU5.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
2030 3000 <sub>H</sub> - 2FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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#### 3.2.4 Segment 3

**Table 10** Address map of segment 3

Address range	Size	Description	Access type	
			Read	Write
3000 0000 <sub>H</sub> - 3003 BFFF <sub>H</sub>	240 Kbyte	CPU4.DSPR	Access	Access
3003 C000 <sub>H</sub> - 3003 FFFF <sub>H</sub>	16 Kbyte	CPU4.DSPR (extension) or CPU4.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
3004 0000 <sub>H</sub> - 300F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
3010 0000 <sub>H</sub> - 3010 FFFF <sub>H</sub>	64 Kbyte	CPU4.PSPR	Access	Access
3011 0000 <sub>H</sub> - 3011 7FFF <sub>H</sub>	32 Kbyte	CPU4.PSPR (extension) or CPU4.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
3011 8000 <sub>H</sub> - 3011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
3012 0000 <sub>H</sub> - 3012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
3013 0000 <sub>H</sub> - 3013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
3013 8000 <sub>H</sub> - 3013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
3014 0000 <sub>H</sub> - 3014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
3015 0000 <sub>H</sub> - 3015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
3015 8000 <sub>H</sub> - 3015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
3016 0000 <sub>H</sub> - 3016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
3017 0000 <sub>H</sub> - 3017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
3017 8000 <sub>H</sub> - 3017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
3018 0000 <sub>H</sub> - 3018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
3019 0000 <sub>H</sub> - 3019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
3019 8000 <sub>H</sub> - 3019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
301A 0000 <sub>H</sub> - 301A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
301B 0000 <sub>H</sub> - 301B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
301B 8000 <sub>H</sub> - 301B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
301C 0000 <sub>H</sub> - 301C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
301D 0000 <sub>H</sub> - 301D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
301D 8000 <sub>H</sub> - 301D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
301E 0000 <sub>H</sub> - 301E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU4.PSPR	Access	Access
301F 0000 <sub>H</sub> - 301F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
301F 8000 <sub>H</sub> - 301F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
3020 0000 <sub>H</sub> - 3020 17FF <sub>H</sub>	-	CPU4.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
3020 1800 <sub>H</sub> - 302F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 10** (continued) Address map of segment 3

Address range	Size	Description	Access type	
			Read	Write
3030 0000 <sub>H</sub> - 3030 2FFF <sub>H</sub>	-	CPU4.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
3030 3000 <sub>H</sub> - 3FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.5 Segment 4

**Table 11** Address map of segment 4

Address range	Size	Description	Access type	
			Read	Write
4000 0000 <sub>H</sub> - 4003 BFFF <sub>H</sub>	240 Kbyte	CPU3.DSPR	Access	Access
4003 C000 <sub>H</sub> - 4003 FFFF <sub>H</sub>	16 Kbyte	CPU3.DSPR (extension) or CPU3.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4004 0000 <sub>H</sub> - 400F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4010 0000 <sub>H</sub> - 4010 FFFF <sub>H</sub>	64 Kbyte	CPU3.PSPR	Access	Access
4011 0000 <sub>H</sub> - 4011 7FFF <sub>H</sub>	32 Kbyte	CPU3.PSPR (extension) or CPU3.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4011 8000 <sub>H</sub> - 4011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4012 0000 <sub>H</sub> - 4012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4013 0000 <sub>H</sub> - 4013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4013 8000 <sub>H</sub> - 4013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4014 0000 <sub>H</sub> - 4014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4015 0000 <sub>H</sub> - 4015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4015 8000 <sub>H</sub> - 4015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4016 0000 <sub>H</sub> - 4016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4017 0000 <sub>H</sub> - 4017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4017 8000 <sub>H</sub> - 4017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4018 0000 <sub>H</sub> - 4018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4019 0000 <sub>H</sub> - 4019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4019 8000 <sub>H</sub> - 4019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
401A 0000 <sub>H</sub> - 401A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
401B 0000 <sub>H</sub> - 401B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
401B 8000 <sub>H</sub> - 401B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
401C 0000 <sub>H</sub> - 401C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
401D 0000 <sub>H</sub> - 401D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
401D 8000 <sub>H</sub> - 401D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
401E 0000 <sub>H</sub> - 401E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
401F 0000 <sub>H</sub> - 401F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
401F 8000 <sub>H</sub> - 401F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4020 0000 <sub>H</sub> - 4020 17FF <sub>H</sub>	-	CPU3.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4020 1800 <sub>H</sub> - 402F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 11** (continued) Address map of segment 4

Address range	Size	Description	Access type	
			Read	Write
4030 0000 <sub>H</sub> - 4030 2FFF <sub>H</sub>	-	CPU3.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4030 3000 <sub>H</sub> - 4FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space



### 3 System address map

#### 3.2.6 Segment 5

**Table 12** Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5000 0000 <sub>H</sub> - 5003 BFFF <sub>H</sub>	240 Kbyte	CPU2.DSPR	Access	Access
5003 C000 <sub>H</sub> - 5003 FFFF <sub>H</sub>	16 Kbyte	CPU2.DSPR (extension) or CPU2.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5004 0000 <sub>H</sub> - 500F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5010 0000 <sub>H</sub> - 5010 FFFF <sub>H</sub>	64 Kbyte	CPU2.PSPR	Access	Access
5011 0000 <sub>H</sub> - 5011 7FFF <sub>H</sub>	32 Kbyte	CPU2.PSPR (extension) or CPU2.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5011 8000 <sub>H</sub> - 5011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5012 0000 <sub>H</sub> - 5012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5013 0000 <sub>H</sub> - 5013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5013 8000 <sub>H</sub> - 5013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5014 0000 <sub>H</sub> - 5014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5015 0000 <sub>H</sub> - 5015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5015 8000 <sub>H</sub> - 5015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5016 0000 <sub>H</sub> - 5016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5017 0000 <sub>H</sub> - 5017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5017 8000 <sub>H</sub> - 5017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5018 0000 <sub>H</sub> - 5018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5019 0000 <sub>H</sub> - 5019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5019 8000 <sub>H</sub> - 5019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
501A 0000 <sub>H</sub> - 501A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501B 0000 <sub>H</sub> - 501B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501B 8000 <sub>H</sub> - 501B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
501C 0000 <sub>H</sub> - 501C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501D 0000 <sub>H</sub> - 501D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501D 8000 <sub>H</sub> - 501D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
501E 0000 <sub>H</sub> - 501E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501F 0000 <sub>H</sub> - 501F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501F 8000 <sub>H</sub> - 501F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5020 0000 <sub>H</sub> - 5020 17FF <sub>H</sub>	-	CPU2.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5020 1800 <sub>H</sub> - 502F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 12** (continued) Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5030 0000 <sub>H</sub> - 5030 2FFF <sub>H</sub>	-	CPU2.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5030 3000 <sub>H</sub> - 5FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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#### 3.2.7 Segment 6

**Table 13** Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6000 0000 <sub>H</sub> - 6003 BFFF <sub>H</sub>	240 Kbyte	CPU1.DSPR	Access	Access
6003 C000 <sub>H</sub> - 6003 FFFF <sub>H</sub>	16 Kbyte	CPU1.DSPR (extension) or CPU1.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6004 0000 <sub>H</sub> - 600F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6010 0000 <sub>H</sub> - 6010 FFFF <sub>H</sub>	64 Kbyte	CPU1.PSPR	Access	Access
6011 0000 <sub>H</sub> - 6011 7FFF <sub>H</sub>	32 Kbyte	CPU1.PSPR (extension) or CPU1.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6011 8000 <sub>H</sub> - 6011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6012 0000 <sub>H</sub> - 6012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6013 0000 <sub>H</sub> - 6013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6013 8000 <sub>H</sub> - 6013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6014 0000 <sub>H</sub> - 6014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6015 0000 <sub>H</sub> - 6015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6015 8000 <sub>H</sub> - 6015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6016 0000 <sub>H</sub> - 6016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6017 0000 <sub>H</sub> - 6017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6017 8000 <sub>H</sub> - 6017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6018 0000 <sub>H</sub> - 6018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6019 0000 <sub>H</sub> - 6019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6019 8000 <sub>H</sub> - 6019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
601A 0000 <sub>H</sub> - 601A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601B 0000 <sub>H</sub> - 601B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601B 8000 <sub>H</sub> - 601B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
601C 0000 <sub>H</sub> - 601C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601D 0000 <sub>H</sub> - 601D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601D 8000 <sub>H</sub> - 601D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
601E 0000 <sub>H</sub> - 601E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601F 0000 <sub>H</sub> - 601F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601F 8000 <sub>H</sub> - 601F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6020 0000 <sub>H</sub> - 6020 17FF <sub>H</sub>	-	CPU1.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6020 1800 <sub>H</sub> - 602F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 13** (continued) Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6030 0000 <sub>H</sub> - 6030 2FFF <sub>H</sub>	-	CPU1.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6030 3000 <sub>H</sub> - 6FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.8 Segment 7

**Table 14** Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7000 0000 <sub>H</sub> - 7003 BFFF <sub>H</sub>	240 Kbyte	CPU0.DSPR	Access	Access
7003 C000 <sub>H</sub> - 7003 FFFF <sub>H</sub>	16 Kbyte	CPU0.DSPR (extension) or CPU0.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7004 0000 <sub>H</sub> - 700F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7010 0000 <sub>H</sub> - 7010 FFFF <sub>H</sub>	64 Kbyte	CPU0.PSPR	Access	Access
7011 0000 <sub>H</sub> - 7011 7FFF <sub>H</sub>	32 Kbyte	CPU0.PSPR (extension) or CPU0.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7011 8000 <sub>H</sub> - 7011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7012 0000 <sub>H</sub> - 7012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7013 0000 <sub>H</sub> - 7013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7013 8000 <sub>H</sub> - 7013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7014 0000 <sub>H</sub> - 7014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7015 0000 <sub>H</sub> - 7015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7015 8000 <sub>H</sub> - 7015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7016 0000 <sub>H</sub> - 7016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7017 0000 <sub>H</sub> - 7017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7017 8000 <sub>H</sub> - 7017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7018 0000 <sub>H</sub> - 7018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7019 0000 <sub>H</sub> - 7019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7019 8000 <sub>H</sub> - 7019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
701A 0000 <sub>H</sub> - 701A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701B 0000 <sub>H</sub> - 701B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701B 8000 <sub>H</sub> - 701B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
701C 0000 <sub>H</sub> - 701C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701D 0000 <sub>H</sub> - 701D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701D 8000 <sub>H</sub> - 701D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
701E 0000 <sub>H</sub> - 701E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701F 0000 <sub>H</sub> - 701F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701F 8000 <sub>H</sub> - 701F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7020 0000 <sub>H</sub> - 7020 17FF <sub>H</sub>	-	CPU0.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7020 1800 <sub>H</sub> - 702F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 14** (continued) Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7030 0000 <sub>H</sub> - 7030 2FFF <sub>H</sub>	-	CPU0.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7030 3000 <sub>H</sub> - 7FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.9 Segments 8 and 10

The read address of the program flash and erase counters varies for the following modes:

- Linear address map
- SOTA address map A
- SOTA address map B

##### 3.2.9.1 Segment 8 linear address map

**Table 15** Linear address map of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 <sub>H</sub> - 801F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
8020 0000 <sub>H</sub> - 803F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
8040 0000 <sub>H</sub> - 805F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
8060 0000 <sub>H</sub> - 807F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
8080 0000 <sub>H</sub> - 808F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
8090 0000 <sub>H</sub> - 809F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
80A0 0000 <sub>H</sub> - 80BF FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
80C0 0000 <sub>H</sub> - 80DF FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
80E0 0000 <sub>H</sub> - 80FF FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
8100 0000 <sub>H</sub> - 811F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
8120 0000 <sub>H</sub> - 812F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
8130 0000 <sub>H</sub> - 813F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
8140 0000 <sub>H</sub> - 83FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8400 0000 <sub>H</sub> - 840F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
8410 0000 <sub>H</sub> - 8FDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FE0 0000 <sub>H</sub> - 8FE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 <sub>H</sub> - 8FFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FFE 0000 <sub>H</sub> - 8FFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 <sub>H</sub> - 8FFF FFFF <sub>H</sub>	64 Kbyte	NVM.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.2 Segment 10 linear address map

**Table 16** Linear address map of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 <sub>H</sub> - A01F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
A020 0000 <sub>H</sub> - A03F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
A040 0000 <sub>H</sub> - A05F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
A060 0000 <sub>H</sub> - A07F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
A080 0000 <sub>H</sub> - A08F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
A090 0000 <sub>H</sub> - A09F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
A0A0 0000 <sub>H</sub> - A0BF FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
A0C0 0000 <sub>H</sub> - A0DF FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
A0E0 0000 <sub>H</sub> - A0FF FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
A100 0000 <sub>H</sub> - A11F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
A120 0000 <sub>H</sub> - A12F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
A130 0000 <sub>H</sub> - A13F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
A140 0000 <sub>H</sub> - A3FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A400 0000 <sub>H</sub> - A40F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
A410 0000 <sub>H</sub> - A7FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A800 0000 <sub>H</sub> - A800 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH00 (Erase Counter)	Access	SRIBE
A800 4000 <sub>H</sub> - A81F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A820 0000 <sub>H</sub> - A820 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH01 (Erase Counter)	Access	SRIBE
A820 4000 <sub>H</sub> - A83F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A840 0000 <sub>H</sub> - A840 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH10 (Erase Counter)	Access	SRIBE
A840 4000 <sub>H</sub> - A85F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A860 0000 <sub>H</sub> - A860 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH11 (Erase Counter)	Access	SRIBE
A860 4000 <sub>H</sub> - A87F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A880 0000 <sub>H</sub> - A880 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH20 (Erase Counter)	Access	SRIBE
A880 4000 <sub>H</sub> - A88F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A890 0000 <sub>H</sub> - A890 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH21 (Erase Counter)	Access	SRIBE
A890 4000 <sub>H</sub> - A89F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A8A0 0000 <sub>H</sub> - A8A0 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH30 (Erase Counter)	Access	SRIBE
A8A0 4000 <sub>H</sub> - A8BF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A8C0 0000 <sub>H</sub> - A8C0 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH31 (Erase Counter)	Access	SRIBE
A8C0 4000 <sub>H</sub> - A8DF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)



### 3 System address map

**Table 16** (continued) Linear address map of segment 10

Address range	Size	Description	Access type	
			Read	Write
A8E0 0000 <sub>H</sub> - A8E0 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH40 (Erase Counter)	Access	SRIBE
A8E0 4000 <sub>H</sub> - A8FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A900 0000 <sub>H</sub> - A900 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH41 (Erase Counter)	Access	SRIBE
A900 4000 <sub>H</sub> - A91F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A920 0000 <sub>H</sub> - A920 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH50 (Erase Counter)	Access	SRIBE
A920 4000 <sub>H</sub> - A92F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A930 0000 <sub>H</sub> - A930 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH51 (Erase Counter)	Access	SRIBE
A930 4000 <sub>H</sub> - ABFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AC00 0000 <sub>H</sub> - AC00 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASHcs (Erase Counter)	Access	SRIBE
AC00 4000 <sub>H</sub> - ADFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE00 0000 <sub>H</sub> - AE0F FFFF <sub>H</sub>	1024 Kbyte	NVM.EEPROM0	Access	SRIBE
AE10 0000 <sub>H</sub> - AE3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE40 0000 <sub>H</sub> - AE41 3FFF <sub>H</sub>	80 Kbyte	NVM.UCB0	Access	SRIBE
AE41 4000 <sub>H</sub> - AE7F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE80 0000 <sub>H</sub> - AE81 FFFF <sub>H</sub>	128 Kbyte	NVM.EEPROM1	Access	SRIBE
AE82 0000 <sub>H</sub> - AEBF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AEC0 0000 <sub>H</sub> - AEC0 CFFF <sub>H</sub>	52 Kbyte	NVM.UCB1	Access	SRIBE
AEC0 D000 <sub>H</sub> - AFDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFE0 0000 <sub>H</sub> - AFE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 <sub>H</sub> - AFFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFFE 0000 <sub>H</sub> - AFFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 <sub>H</sub> - AFFF FFFF <sub>H</sub>	64 Kbyte	NVM.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.3 Segment 8 SOTA address map A

**Table 17** SOTA address map A of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 <sub>H</sub> - 801F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
8020 0000 <sub>H</sub> - 803F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
8040 0000 <sub>H</sub> - 804F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
8050 0000 <sub>H</sub> - 806F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
8070 0000 <sub>H</sub> - 808F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
8090 0000 <sub>H</sub> - 809F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
80A0 0000 <sub>H</sub> - 81FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8200 0000 <sub>H</sub> - 821F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
8220 0000 <sub>H</sub> - 823F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
8240 0000 <sub>H</sub> - 824F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
8250 0000 <sub>H</sub> - 826F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
8270 0000 <sub>H</sub> - 828F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
8290 0000 <sub>H</sub> - 829F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
82A0 0000 <sub>H</sub> - 83FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8400 0000 <sub>H</sub> - 840F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
8410 0000 <sub>H</sub> - 8FDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FE0 0000 <sub>H</sub> - 8FE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 <sub>H</sub> - 8FFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FFE 0000 <sub>H</sub> - 8FFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 <sub>H</sub> - 8FFF FFFF <sub>H</sub>	64 Kbyte	NVM.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.4 Segment 10 SOTA address map A

**Table 18 SOTA address map A of segment 10**

Address range	Size	Description	Access type	
			Read	Write
A000 0000 <sub>H</sub> - A01F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
A020 0000 <sub>H</sub> - A03F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
A040 0000 <sub>H</sub> - A04F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
A050 0000 <sub>H</sub> - A06F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
A070 0000 <sub>H</sub> - A08F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
A090 0000 <sub>H</sub> - A09F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
A0A0 0000 <sub>H</sub> - A1FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A200 0000 <sub>H</sub> - A21F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
A220 0000 <sub>H</sub> - A23F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
A240 0000 <sub>H</sub> - A24F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
A250 0000 <sub>H</sub> - A26F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
A270 0000 <sub>H</sub> - A28F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
A290 0000 <sub>H</sub> - A29F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
A2A0 0000 <sub>H</sub> - A3FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A400 0000 <sub>H</sub> - A40F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
A410 0000 <sub>H</sub> - A7FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A800 0000 <sub>H</sub> - A800 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH00 (Erase Counter)	Access	SRIBE
A800 4000 <sub>H</sub> - A81F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A820 0000 <sub>H</sub> - A820 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH10 (Erase Counter)	Access	SRIBE
A820 4000 <sub>H</sub> - A83F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A840 0000 <sub>H</sub> - A840 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH20 (Erase Counter)	Access	SRIBE
A840 4000 <sub>H</sub> - A84F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A850 0000 <sub>H</sub> - A850 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH30 (Erase Counter)	Access	SRIBE
A850 4000 <sub>H</sub> - A86F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A870 0000 <sub>H</sub> - A870 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH40 (Erase Counter)	Access	SRIBE
A870 4000 <sub>H</sub> - A88F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A890 0000 <sub>H</sub> - A890 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH50 (Erase Counter)	Access	SRIBE
A890 4000 <sub>H</sub> - A9FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA00 0000 <sub>H</sub> - AA00 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH01 (Erase Counter)	Access	SRIBE
AA00 4000 <sub>H</sub> - AA1F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA20 0000 <sub>H</sub> - AA20 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH11 (Erase Counter)	Access	SRIBE

(table continues...)

### 3 System address map

**Table 18** (continued) SOTA address map A of segment 10

Address range	Size	Description	Access type	
			Read	Write
AA20 4000 <sub>H</sub> - AA3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA40 0000 <sub>H</sub> - AA40 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH21 (Erase Counter)	Access	SRIBE
AA40 4000 <sub>H</sub> - AA4F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA50 0000 <sub>H</sub> - AA50 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH31 (Erase Counter)	Access	SRIBE
AA50 4000 <sub>H</sub> - AA6F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA70 0000 <sub>H</sub> - AA70 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH41 (Erase Counter)	Access	SRIBE
AA70 4000 <sub>H</sub> - AA8F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA90 0000 <sub>H</sub> - AA90 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH51 (Erase Counter)	Access	SRIBE
AA90 4000 <sub>H</sub> - ABFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AC00 0000 <sub>H</sub> - AC00 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASHcs (Erase Counter)	Access	SRIBE
AC00 4000 <sub>H</sub> - ADFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE00 0000 <sub>H</sub> - AE0F FFFF <sub>H</sub>	1024 Kbyte	NVM.EEPROM0	Access	SRIBE
AE10 0000 <sub>H</sub> - AE3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE40 0000 <sub>H</sub> - AE41 3FFF <sub>H</sub>	80 Kbyte	NVM.UCB0	Access	SRIBE
AE41 4000 <sub>H</sub> - AE7F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE80 0000 <sub>H</sub> - AE81 FFFF <sub>H</sub>	128 Kbyte	NVM.EEPROM1	Access	SRIBE
AE82 0000 <sub>H</sub> - AEBF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AEC0 0000 <sub>H</sub> - AEC0 CFFF <sub>H</sub>	52 Kbyte	NVM.UCB1	Access	SRIBE
AEC0 D000 <sub>H</sub> - AFDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFE0 0000 <sub>H</sub> - AFE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 <sub>H</sub> - AFFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFFE 0000 <sub>H</sub> - AFFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 <sub>H</sub> - AFFF FFFF <sub>H</sub>	64 Kbyte	NVM.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.5 Segment 8 SOTA address map B

**Table 19 SOTA address map B of segment 8**

Address range	Size	Description	Access type	
			Read	Write
8000 0000 <sub>H</sub> - 801F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
8020 0000 <sub>H</sub> - 803F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
8040 0000 <sub>H</sub> - 804F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
8050 0000 <sub>H</sub> - 806F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
8070 0000 <sub>H</sub> - 808F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
8090 0000 <sub>H</sub> - 809F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
80A0 0000 <sub>H</sub> - 81FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8200 0000 <sub>H</sub> - 821F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
8220 0000 <sub>H</sub> - 823F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
8240 0000 <sub>H</sub> - 824F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
8250 0000 <sub>H</sub> - 826F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
8270 0000 <sub>H</sub> - 828F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
8290 0000 <sub>H</sub> - 829F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
82A0 0000 <sub>H</sub> - 83FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8400 0000 <sub>H</sub> - 840F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
8410 0000 <sub>H</sub> - 8FDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FE0 0000 <sub>H</sub> - 8FE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 <sub>H</sub> - 8FFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FFE 0000 <sub>H</sub> - 8FFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 <sub>H</sub> - 8FFF FFFF <sub>H</sub>	64 Kbyte	NVM.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.6 Segment 10 SOTA address map B

**Table 20** SOTA address map B of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 <sub>H</sub> - A01F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
A020 0000 <sub>H</sub> - A03F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
A040 0000 <sub>H</sub> - A04F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
A050 0000 <sub>H</sub> - A06F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
A070 0000 <sub>H</sub> - A08F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
A090 0000 <sub>H</sub> - A09F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
A0A0 0000 <sub>H</sub> - A1FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A200 0000 <sub>H</sub> - A21F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
A220 0000 <sub>H</sub> - A23F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
A240 0000 <sub>H</sub> - A24F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
A250 0000 <sub>H</sub> - A26F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
A270 0000 <sub>H</sub> - A28F FFFF <sub>H</sub>	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
A290 0000 <sub>H</sub> - A29F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
A2A0 0000 <sub>H</sub> - A3FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A400 0000 <sub>H</sub> - A40F FFFF <sub>H</sub>	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
A410 0000 <sub>H</sub> - A7FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A800 0000 <sub>H</sub> - A800 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH01 (Erase Counter)	Access	SRIBE
A800 4000 <sub>H</sub> - A81F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A820 0000 <sub>H</sub> - A820 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH11 (Erase Counter)	Access	SRIBE
A820 4000 <sub>H</sub> - A83F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A840 0000 <sub>H</sub> - A840 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH21 (Erase Counter)	Access	SRIBE
A840 4000 <sub>H</sub> - A84F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A850 0000 <sub>H</sub> - A850 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH31 (Erase Counter)	Access	SRIBE
A850 4000 <sub>H</sub> - A86F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A870 0000 <sub>H</sub> - A870 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH41 (Erase Counter)	Access	SRIBE
A870 4000 <sub>H</sub> - A88F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A890 0000 <sub>H</sub> - A890 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH51 (Erase Counter)	Access	SRIBE
A890 4000 <sub>H</sub> - A9FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA00 0000 <sub>H</sub> - AA00 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH00 (Erase Counter)	Access	SRIBE
AA00 4000 <sub>H</sub> - AA1F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA20 0000 <sub>H</sub> - AA20 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH10 (Erase Counter)	Access	SRIBE

(table continues...)

### 3 System address map

**Table 20** (continued) SOTA address map B of segment 10

Address range	Size	Description	Access type	
			Read	Write
AA20 4000 <sub>H</sub> - AA3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA40 0000 <sub>H</sub> - AA40 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH20 (Erase Counter)	Access	SRIBE
AA40 4000 <sub>H</sub> - AA4F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA50 0000 <sub>H</sub> - AA50 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH30 (Erase Counter)	Access	SRIBE
AA50 4000 <sub>H</sub> - AA6F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA70 0000 <sub>H</sub> - AA70 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH40 (Erase Counter)	Access	SRIBE
AA70 4000 <sub>H</sub> - AA8F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AA90 0000 <sub>H</sub> - AA90 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASH50 (Erase Counter)	Access	SRIBE
AA90 4000 <sub>H</sub> - ABFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AC00 0000 <sub>H</sub> - AC00 3FFF <sub>H</sub>	16 Kbyte	NVM.PFLASHcs (Erase Counter)	Access	SRIBE
AC00 4000 <sub>H</sub> - ADFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE00 0000 <sub>H</sub> - AE0F FFFF <sub>H</sub>	1024 Kbyte	NVM.EEPROM0	Access	SRIBE
AE10 0000 <sub>H</sub> - AE3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE40 0000 <sub>H</sub> - AE41 3FFF <sub>H</sub>	80 Kbyte	NVM.UCB0	Access	SRIBE
AE41 4000 <sub>H</sub> - AE7F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE80 0000 <sub>H</sub> - AE81 FFFF <sub>H</sub>	128 Kbyte	NVM.EEPROM1	Access	SRIBE
AE82 0000 <sub>H</sub> - AEBF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AEC0 0000 <sub>H</sub> - AEC0 CFFF <sub>H</sub>	52 Kbyte	NVM.UCB1	Access	SRIBE
AEC0 D000 <sub>H</sub> - AFDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFE0 0000 <sub>H</sub> - AFE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 <sub>H</sub> - AFFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFFE 0000 <sub>H</sub> - AFFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 <sub>H</sub> - AFFF FFFF <sub>H</sub>	64 Kbyte	NVM.BROM	Access	SRIBE

### 3 System address map

#### 3.2.10 Segments 9 and 11

##### 3.2.10.1 Segment 9

**Table 21** Address map of segment 9

Address range	Size	Description	Access type	
			Read	Write
9000 0000 <sub>H</sub> - 9007 FFFF <sub>H</sub>	512 Kbyte	CPU0.DLMU	Access	Access
9008 0000 <sub>H</sub> - 900F FFFF <sub>H</sub>	512 Kbyte	CPU1.DLMU	Access	Access
9010 0000 <sub>H</sub> - 9017 FFFF <sub>H</sub>	512 Kbyte	CPU2.DLMU	Access	Access
9018 0000 <sub>H</sub> - 901F FFFF <sub>H</sub>	512 Kbyte	CPU3.DLMU	Access	Access
9020 0000 <sub>H</sub> - 9027 FFFF <sub>H</sub>	512 Kbyte	CPU4.DLMU	Access	Access
9028 0000 <sub>H</sub> - 902F FFFF <sub>H</sub>	512 Kbyte	CPU5.DLMU	Access	Access
9030 0000 <sub>H</sub> - 903F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
9040 0000 <sub>H</sub> - 9047 FFFF <sub>H</sub>	512 Kbyte	LMU0	Access	Access
9048 0000 <sub>H</sub> - 904F FFFF <sub>H</sub>	512 Kbyte	LMU1	Access	Access
9050 0000 <sub>H</sub> - 9057 FFFF <sub>H</sub>	512 Kbyte	LMU2	Access	Access
9058 0000 <sub>H</sub> - 905F FFFF <sub>H</sub>	512 Kbyte	LMU3	Access	Access
9060 0000 <sub>H</sub> - 9067 FFFF <sub>H</sub>	512 Kbyte	LMU4	Access	Access
9068 0000 <sub>H</sub> - 906F FFFF <sub>H</sub>	512 Kbyte	LMU5	Access	Access
9070 0000 <sub>H</sub> - 9077 FFFF <sub>H</sub>	512 Kbyte	LMU6	Access	Access
9078 0000 <sub>H</sub> - 907F FFFF <sub>H</sub>	512 Kbyte	LMU7	Access	Access
9080 0000 <sub>H</sub> - 9087 FFFF <sub>H</sub>	512 Kbyte	LMU8	Access	Access
9088 0000 <sub>H</sub> - 908F FFFF <sub>H</sub>	512 Kbyte	LMU9	Access	Access
9090 0000 <sub>H</sub> - 9207 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
9208 0000 <sub>H</sub> - 920F FFFF <sub>H</sub>	512 Kbyte	PPU.CSM	Access	Access
9210 0000 <sub>H</sub> - 97FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
9800 0000 <sub>H</sub> - 9FFF FFFF <sub>H</sub>	128 Mbyte	xSPI	Access	Access



### 3 System address map

#### 3.2.10.2 Segment 11

**Table 22 Address map of segment 11**

Address range	Size	Description	Access type	
			Read	Write
B000 0000 <sub>H</sub> - B007 FFFF <sub>H</sub>	512 Kbyte	CPU0.DLMU	Access	Access
B008 0000 <sub>H</sub> - B00F FFFF <sub>H</sub>	512 Kbyte	CPU1.DLMU	Access	Access
B010 0000 <sub>H</sub> - B017 FFFF <sub>H</sub>	512 Kbyte	CPU2.DLMU	Access	Access
B018 0000 <sub>H</sub> - B01F FFFF <sub>H</sub>	512 Kbyte	CPU3.DLMU	Access	Access
B020 0000 <sub>H</sub> - B027 FFFF <sub>H</sub>	512 Kbyte	CPU4.DLMU	Access	Access
B028 0000 <sub>H</sub> - B02F FFFF <sub>H</sub>	512 Kbyte	CPU5.DLMU	Access	Access
B030 0000 <sub>H</sub> - B03F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B040 0000 <sub>H</sub> - B047 FFFF <sub>H</sub>	512 Kbyte	LMU0	Access	Access
B048 0000 <sub>H</sub> - B04F FFFF <sub>H</sub>	512 Kbyte	LMU1	Access	Access
B050 0000 <sub>H</sub> - B057 FFFF <sub>H</sub>	512 Kbyte	LMU2	Access	Access
B058 0000 <sub>H</sub> - B05F FFFF <sub>H</sub>	512 Kbyte	LMU3	Access	Access
B060 0000 <sub>H</sub> - B067 FFFF <sub>H</sub>	512 Kbyte	LMU4	Access	Access
B068 0000 <sub>H</sub> - B06F FFFF <sub>H</sub>	512 Kbyte	LMU5	Access	Access
B070 0000 <sub>H</sub> - B077 FFFF <sub>H</sub>	512 Kbyte	LMU6	Access	Access
B078 0000 <sub>H</sub> - B07F FFFF <sub>H</sub>	512 Kbyte	LMU7	Access	Access
B080 0000 <sub>H</sub> - B087 FFFF <sub>H</sub>	512 Kbyte	LMU8	Access	Access
B088 0000 <sub>H</sub> - B08F FFFF <sub>H</sub>	512 Kbyte	LMU9	Access	Access
B090 0000 <sub>H</sub> - B203 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B204 0000 <sub>H</sub> - B205 FFFF <sub>H</sub>	128 Kbyte	PPU.VMEM	Access	Access
B206 0000 <sub>H</sub> - B207 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B208 0000 <sub>H</sub> - B20F FFFF <sub>H</sub>	512 Kbyte	PPU.CSM	Access	Access
B210 0000 <sub>H</sub> - B7FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B800 0000 <sub>H</sub> - BFFF FFFF <sub>H</sub>	128 Mbyte	xSPI	Access	Access

### 3 System address map

#### 3.2.11 Segment 12

**Table 23** Address map of segment 12

Address range	Size	Description	Access type	
			Read	Write
C000 0000 <sub>H</sub> - CFFF FFFF <sub>H</sub>	-	Reserved <sup>1) 2)</sup>	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

2) See the PPU chapter for details

#### 3.2.12 Segment 13

**Table 24** Address map of segment 13

Address range	Size	Description	Access type	
			Read	Write
D000 0000 <sub>H</sub> - DFFF FFFF <sub>H</sub>	-	Reserved <sup>1)</sup>	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

#### 3.2.13 Segment 14

**Table 25** Address map of segment 14

Address range	Size	Unit	Access type	
			Read	Write
E000 0000 <sub>H</sub> - E97F FFFF <sub>H</sub>	-	Reserved	LLIBE	LLIBE
E980 0000 <sub>H</sub> - E980 FFFF <sub>H</sub>	64 Kbyte	PPU.SFR	Access	Access
E981 0000 <sub>H</sub> - EBFF FFFF <sub>H</sub>	-	Reserved	LLIBE	LLIBE
EC00 0000 <sub>H</sub> - EFFF FFFF <sub>H</sub>	64 Mbyte	PCIe1.DATA	Access	Access

### 3 System address map

#### 3.2.14 Segment 15

The address map of segment 15 includes the following module address ranges:

- Absolute Addressing Range
  - If a module is addressed in the first 16 Kbyte of segment 15, the CPU can access the module with absolute addressing mode
- Others
  - If a module is addressed above the first 16 Kbyte of segment 15, the CPU can access the module with base + offset

**Table 26 Address map of segment 15**

Address range	Size	Unit	Access type	
			Read	Write
F000 0000 <sub>H</sub> - F000 07FF <sub>H</sub>	2 Kbyte	WTU	Access	Access
F000 0800 <sub>H</sub> - F002 3FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F002 4000 <sub>H</sub> - F002 43FF <sub>H</sub>	1 Kbyte	SCU	Access	Access
F002 4400 <sub>H</sub> - F002 7FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F002 8000 <sub>H</sub> - F002 9FFF <sub>H</sub>	8 Kbyte	SMU	Access	Access
F002 A000 <sub>H</sub> - F003 9FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 A000 <sub>H</sub> - F003 A3FF <sub>H</sub>	1 Kbyte	P00	Access	Access
F003 A400 <sub>H</sub> - F003 A7FF <sub>H</sub>	1 Kbyte	P01	Access	Access
F003 A800 <sub>H</sub> - F003 ABFF <sub>H</sub>	1 Kbyte	P02	Access	Access
F003 AC00 <sub>H</sub> - F003 AFFF <sub>H</sub>	1 Kbyte	P03	Access	Access
F003 B000 <sub>H</sub> - F003 B3FF <sub>H</sub>	1 Kbyte	P04	Access	Access
F003 B400 <sub>H</sub> - F003 C7FF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 C800 <sub>H</sub> - F003 CBFF <sub>H</sub>	1 Kbyte	P10	Access	Access
F003 CC00 <sub>H</sub> - F003 D3FF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 D400 <sub>H</sub> - F003 D7FF <sub>H</sub>	1 Kbyte	P13	Access	Access
F003 D800 <sub>H</sub> - F003 DBFF <sub>H</sub>	1 Kbyte	P14	Access	Access
F003 DC00 <sub>H</sub> - F003 DFFF <sub>H</sub>	1 Kbyte	P15	Access	Access
F003 E000 <sub>H</sub> - F003 E3FF <sub>H</sub>	1 Kbyte	P16	Access	Access
F003 E400 <sub>H</sub> - F003 EFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 F000 <sub>H</sub> - F003 F3FF <sub>H</sub>	1 Kbyte	P20	Access	Access
F003 F400 <sub>H</sub> - F003 F7FF <sub>H</sub>	1 Kbyte	P21	Access	Access
F003 F800 <sub>H</sub> - F003 FBFF <sub>H</sub>	1 Kbyte	P22	Access	Access
F003 FC00 <sub>H</sub> - F003 FFFF <sub>H</sub>	1 Kbyte	P23	Access	Access
F004 0000 <sub>H</sub> - F004 03FF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F004 0400 <sub>H</sub> - F004 07FF <sub>H</sub>	1 Kbyte	P25	Access	Access
F004 0800 <sub>H</sub> - F004 17FF <sub>H</sub>	–	Reserved	SPBBE	SPBBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F004 1800 <sub>H</sub> - F004 1BFF <sub>H</sub>	1 Kbyte	P30	Access	Access
F004 1C00 <sub>H</sub> - F004 1FFF <sub>H</sub>	1 Kbyte	P31	Access	Access
F004 2000 <sub>H</sub> - F004 23FF <sub>H</sub>	1 Kbyte	P32	Access	Access
F004 2400 <sub>H</sub> - F004 27FF <sub>H</sub>	1 Kbyte	P33	Access	Access
F004 2800 <sub>H</sub> - F004 2BFF <sub>H</sub>	1 Kbyte	P34	Access	Access
F004 2C00 <sub>H</sub> - F004 2FFF <sub>H</sub>	1 Kbyte	P35	Access	Access
F004 3000 <sub>H</sub> - F004 3FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F004 4000 <sub>H</sub> - F004 43FF <sub>H</sub>	1 Kbyte	P40	Access	Access
F004 4400 <sub>H</sub> - F005 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F006 0000 <sub>H</sub> - F006 3FFF <sub>H</sub>	16 Kbyte	SMM	Access	Access
F006 4000 <sub>H</sub> - F006 7FFF <sub>H</sub>	16 Kbyte	CCU	Access	Access
F006 8000 <sub>H</sub> - F006 80FF <sub>H</sub>	256 Byte	TRI	Access	Access
F006 8100 <sub>H</sub> - F006 BFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F006 C000 <sub>H</sub> - F006 FFFF <sub>H</sub>	16 Kbyte	VTMON	Access	Access
F007 0000 <sub>H</sub> - F023 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F024 0000 <sub>H</sub> - F024 7FFF <sub>H</sub>	32 Kbyte	SCR.XRAM	Access	Access
F024 8000 <sub>H</sub> - F024 8FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F024 9000 <sub>H</sub> - F024 9FFF <sub>H</sub>	4 Kbyte	PMS	Access	Access
F024 A000 <sub>H</sub> - F03F FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F040 0000 <sub>H</sub> - F041 FFFF <sub>H</sub>	128 Kbyte	VMT0	Access	Access
F042 0000 <sub>H</sub> - F043 FFFF <sub>H</sub>	128 Kbyte	VMT1	Access	Access
F044 0000 <sub>H</sub> - F045 FFFF <sub>H</sub>	128 Kbyte	VMT2	Access	Access
F046 0000 <sub>H</sub> - F047 FFFF <sub>H</sub>	128 Kbyte	VMT3	Access	Access
F048 0000 <sub>H</sub> - F049 FFFF <sub>H</sub>	128 Kbyte	VMT4	Access	Access
F04A 0000 <sub>H</sub> - F04B FFFF <sub>H</sub>	128 Kbyte	VMT5	Access	Access
F04C 0000 <sub>H</sub> - F04D FFFF <sub>H</sub>	128 Kbyte	VMT6	Access	Access
F04E 0000 <sub>H</sub> - F1FF FBFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F1FF FC00 <sub>H</sub> - F1FF FFFF <sub>H</sub>	1 Kbyte	SBCU	Access	Access
F200 0000 <sub>H</sub> - F3FF FFFF <sub>H</sub>	32 Mbyte	HSPHY	Access	Access
F400 0000 <sub>H</sub> - F440 07FF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F440 0800 <sub>H</sub> - F440 09FF <sub>H</sub>	512 Byte	MSC0	Access	Access
F440 0A00 <sub>H</sub> - F440 0FFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F440 1000 <sub>H</sub> - F440 11FF <sub>H</sub>	512 Byte	QSPI0	Access	Access
F440 1200 <sub>H</sub> - F440 13FF <sub>H</sub>	512 Byte	QSPI1	Access	Access
F440 1400 <sub>H</sub> - F440 15FF <sub>H</sub>	512 Byte	QSPI2	Access	Access
F440 1600 <sub>H</sub> - F440 17FF <sub>H</sub>	512 Byte	QSPI3	Access	Access
F440 1800 <sub>H</sub> - F440 19FF <sub>H</sub>	512 Byte	QSPI4	Access	Access
F440 1A00 <sub>H</sub> - F440 1BFF <sub>H</sub>	512 Byte	QSPI5	Access	Access
F440 1C00 <sub>H</sub> - F440 1DFF <sub>H</sub>	512 Byte	QSPI6	Access	Access
F440 1E00 <sub>H</sub> - F440 1FFF <sub>H</sub>	512 Byte	QSPI7	Access	Access
F440 2000 <sub>H</sub> - F440 2DFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F440 2E00 <sub>H</sub> - F440 2FFF <sub>H</sub>	512 Byte	FCE0	Access	Access
F440 3000 <sub>H</sub> - F440 3BFF <sub>H</sub>	6 x 512 Byte	SENT0	Access	Access
F440 3C00 <sub>H</sub> - F440 3FFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F440 4000 <sub>H</sub> - F440 4BFF <sub>H</sub>	6 x 512 Byte	SENT1	Access	Access
F440 4C00 <sub>H</sub> - F440 4FFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F440 5000 <sub>H</sub> - F440 5BFF <sub>H</sub>	6 x 512 Byte	PSI5	Access	Access
F440 5C00 <sub>H</sub> - F440 6FFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F440 7000 <sub>H</sub> - F440 7FFF <sub>H</sub>	4 Kbyte	PSI5S0	Access	Access
F440 8000 <sub>H</sub> - F440 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F441 0000 <sub>H</sub> - F441 3FFF <sub>H</sub>	16 Kbyte	DMA0	Access	Access
F441 4000 <sub>H</sub> - F441 7FFF <sub>H</sub>	16 Kbyte	DMA1	Access	Access
F441 8000 <sub>H</sub> - F441 BFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F441 C000 <sub>H</sub> - F441 CFFF <sub>H</sub>	4 Kbyte	ERAY0	Access	Access
F441 D000 <sub>H</sub> - F441 DFFF <sub>H</sub>	4 Kbyte	ERAY1	Access	Access
F441 E000 <sub>H</sub> - F442 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F443 0000 <sub>H</sub> - F443 1FFF <sub>H</sub>	8 Kbyte	IR.SFR	Access	Access
F443 2000 <sub>H</sub> - F443 5FFF <sub>H</sub>	16 Kbyte	IR.SRC	Access	Access
F443 6000 <sub>H</sub> - F447 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F448 0000 <sub>H</sub> - F448 03FF <sub>H</sub>	4 x 256 Byte	HSSL0	Access	Access
F448 0400 <sub>H</sub> - F448 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F449 0000 <sub>H</sub> - F449 FFFF <sub>H</sub>	64 Kbyte	HSCT0	Access	Access
F44A 0000 <sub>H</sub> - F44A 03FF <sub>H</sub>	4 x 256 Byte	HSSL1	Access	Access
F44A 0400 <sub>H</sub> - F44A FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F44B 0000 <sub>H</sub> - F44B FFFF <sub>H</sub>	64 Kbyte	HSCT1	Access	Access
F44C 0000 <sub>H</sub> - F44D FFFF <sub>H</sub>	128 Kbyte	I2C0	Access	Access
F44E 0000 <sub>H</sub> - F44F FFFF <sub>H</sub>	128 Kbyte	I2C1	Access	Access
F450 0000 <sub>H</sub> - F451 FFFF <sub>H</sub>	128 Kbyte	I2C2	Access	Access
F452 0000 <sub>H</sub> - F469 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F46A 0000 <sub>H</sub> - F46A 7FFF <sub>H</sub>	32 Kbyte	PCIe1.SFR	Access	Access
F46A 8000 <sub>H</sub> - F46A FFFF <sub>H</sub>	32 Kbyte	PCIe0.SFR	Access	Access
F46B 0000 <sub>H</sub> - F46B 0FFF <sub>H</sub>	4 Kbyte	SDMMC0	Access	Access
F46B 1000 <sub>H</sub> - F46B FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F46C 0000 <sub>H</sub> - F46C 01FF <sub>H</sub>	512 Byte	ASCLIN0	Access	Access
F46C 0200 <sub>H</sub> - F46C 03FF <sub>H</sub>	512 Byte	ASCLIN1	Access	Access
F46C 0400 <sub>H</sub> - F46C 05FF <sub>H</sub>	512 Byte	ASCLIN2	Access	Access
F46C 0600 <sub>H</sub> - F46C 07FF <sub>H</sub>	512 Byte	ASCLIN3	Access	Access
F46C 0800 <sub>H</sub> - F46C 09FF <sub>H</sub>	512 Byte	ASCLIN4	Access	Access
F46C 0A00 <sub>H</sub> - F46C 0BFF <sub>H</sub>	512 Byte	ASCLIN5	Access	Access
F46C 0C00 <sub>H</sub> - F46C 0DFF <sub>H</sub>	512 Byte	ASCLIN6	Access	Access
F46C 0E00 <sub>H</sub> - F46C 0FFF <sub>H</sub>	512 Byte	ASCLIN7	Access	Access
F46C 1000 <sub>H</sub> - F46C 11FF <sub>H</sub>	512 Byte	ASCLIN8	Access	Access
F46C 1200 <sub>H</sub> - F46C 13FF <sub>H</sub>	512 Byte	ASCLIN9	Access	Access
F46C 1400 <sub>H</sub> - F46C 15FF <sub>H</sub>	512 Byte	ASCLIN10	Access	Access
F46C 1600 <sub>H</sub> - F46C 17FF <sub>H</sub>	512 Byte	ASCLIN11	Access	Access
F46C 1800 <sub>H</sub> - F46C 19FF <sub>H</sub>	512 Byte	ASCLIN12	Access	Access
F46C 1A00 <sub>H</sub> - F46C 1BFF <sub>H</sub>	512 Byte	ASCLIN13	Access	Access
F46C 1C00 <sub>H</sub> - F46C 1DFF <sub>H</sub>	512 Byte	ASCLIN14	Access	Access
F46C 1E00 <sub>H</sub> - F46C 1FFF <sub>H</sub>	512 Byte	ASCLIN15	Access	Access
F46C 2000 <sub>H</sub> - F46C 21FF <sub>H</sub>	512 Byte	ASCLIN16	Access	Access
F46C 2200 <sub>H</sub> - F46C 23FF <sub>H</sub>	512 Byte	ASCLIN17	Access	Access
F46C 2400 <sub>H</sub> - F46C 25FF <sub>H</sub>	512 Byte	ASCLIN18	Access	Access
F46C 2600 <sub>H</sub> - F46C 27FF <sub>H</sub>	512 Byte	ASCLIN19	Access	Access
F46C 2800 <sub>H</sub> - F46C 29FF <sub>H</sub>	512 Byte	ASCLIN20	Access	Access
F46C 2A00 <sub>H</sub> - F46C 2BFF <sub>H</sub>	512 Byte	ASCLIN21	Access	Access
F46C 2C00 <sub>H</sub> - F46C 2DFF <sub>H</sub>	512 Byte	ASCLIN22	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F46C 2E00 <sub>H</sub> - F46C 2FFF <sub>H</sub>	512 Byte	ASCLIN23	Access	Access
F46C 3000 <sub>H</sub> - F46C 31FF <sub>H</sub>	512 Byte	ASCLIN24	Access	Access
F46C 3200 <sub>H</sub> - F46C 33FF <sub>H</sub>	512 Byte	ASCLIN25	Access	Access
F46C 3400 <sub>H</sub> - F46C 35FF <sub>H</sub>	512 Byte	ASCLIN26	Access	Access
F46C 3600 <sub>H</sub> - F46C 37FF <sub>H</sub>	512 Byte	ASCLIN27	Access	Access
F46C 3800 <sub>H</sub> - F46F FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F470 0000 <sub>H</sub> - F470 8FFF <sub>H</sub>	36 Kbyte	CAN0.SRAM	Access	Access
F470 9000 <sub>H</sub> - F470 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F471 0000 <sub>H</sub> - F471 10FF <sub>H</sub>	4352 Byte	CAN0.SFR	Access	Access
F471 1100 <sub>H</sub> - F471 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F472 0000 <sub>H</sub> - F472 4FFF <sub>H</sub>	20 Kbyte	CAN1.SRAM	Access	Access
F472 5000 <sub>H</sub> - F472 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F473 0000 <sub>H</sub> - F473 10FF <sub>H</sub>	4352 Byte	CAN1.SFR	Access	Access
F473 1100 <sub>H</sub> - F473 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F474 0000 <sub>H</sub> - F474 4FFF <sub>H</sub>	20 Kbyte	CAN2.SRAM	Access	Access
F474 5000 <sub>H</sub> - F474 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F475 0000 <sub>H</sub> - F475 10FF <sub>H</sub>	4352 Byte	CAN2.SFR	Access	Access
F475 1100 <sub>H</sub> - F475 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F476 0000 <sub>H</sub> - F476 4FFF <sub>H</sub>	20 Kbyte	CAN3.SRAM	Access	Access
F476 5000 <sub>H</sub> - F476 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F477 0000 <sub>H</sub> - F477 10FF <sub>H</sub>	4352 Byte	CAN3.SFR	Access	Access
F477 1100 <sub>H</sub> - F477 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F478 0000 <sub>H</sub> - F478 4FFF <sub>H</sub>	20 Kbyte	CAN4.SRAM	Access	Access
F478 5000 <sub>H</sub> - F478 FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F479 0000 <sub>H</sub> - F479 10FF <sub>H</sub>	4352 Byte	CAN4.SFR	Access	Access
F479 1100 <sub>H</sub> - F47B FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F47C 0000 <sub>H</sub> - F47C 3FFF <sub>H</sub>	16 Kbyte	CANXL0.SRAM	Access	Access
F47C 4000 <sub>H</sub> - F47C FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F47D 0000 <sub>H</sub> - F47F FFFF <sub>H</sub>	192 Kbyte	CANXL0.SFR	Access	Access
F480 0000 <sub>H</sub> - F4FF FFFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F500 0000 <sub>H</sub> - F500 1FFF <sub>H</sub>	8 Kbyte	ADC.SFR	Access	Access
F500 2000 <sub>H</sub> - F501 F7FF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE

(table continues...)

### 3 System address map

**Table 26 (continued) Address map of segment 15**

Address range	Size	Unit	Access type	
			Read	Write
F501 F800 <sub>H</sub> - F501 FFFF <sub>H</sub>	2 Kbyte	TMADC.SFR	Access	Access
F502 0000 <sub>H</sub> - F502 07FF <sub>H</sub>	2 Kbyte	TMADC0 (Read Write)	Access	Access
F502 0800 <sub>H</sub> - F502 0FFF <sub>H</sub>	2 Kbyte	TMADC1 (Read Write)	Access	Access
F502 1000 <sub>H</sub> - F502 17FF <sub>H</sub>	2 Kbyte	TMADC2 (Read Write)	Access	Access
F502 1800 <sub>H</sub> - F502 1FFF <sub>H</sub>	2 Kbyte	TMADC3 (Read Write)	Access	Access
F502 2000 <sub>H</sub> - F50B F7FF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F50B F800 <sub>H</sub> - F50B FFFF <sub>H</sub>	2 Kbyte	CDSP.SFR	Access	Access
F50C 0000 <sub>H</sub> - F50C 1FFF <sub>H</sub>	8 Kbyte	CDSP0	Access	Access
F50C 2000 <sub>H</sub> - F50C 3FFF <sub>H</sub>	8 Kbyte	CDSP1	Access	Access
F50C 4000 <sub>H</sub> - F5FF FBFF <sub>H</sub>	–	Reserved	COMPBBE	COMPBBE
F5FF FC00 <sub>H</sub> - F5FF FFFF <sub>H</sub>	1 Kbyte	COMBCU	Access	Access
F600 0000 <sub>H</sub> - F600 01FF <sub>H</sub>	512 Byte	CSCU	Access	Access
F600 0200 <sub>H</sub> - F600 03FF <sub>H</sub>	512 Byte	TRNG	Access	Access
F600 0400 <sub>H</sub> - F600 FFFF <sub>H</sub>	–	Reserved	CSPBBE	CSPBBE
F601 0000 <sub>H</sub> - F601 7FFF <sub>H</sub>	32 Kbyte	PKC	Access	Access
F601 8000 <sub>H</sub> - F6FF FBFF <sub>H</sub>	–	Reserved	CSPBBE	CSPBBE
F6FF FC00 <sub>H</sub> - F6FF FFFF <sub>H</sub>	1 Kbyte	CSBCU	Access	Access
F700 0000 <sub>H</sub> - F7FF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F800 0000 <sub>H</sub> - F800 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F800 8000 <sub>H</sub> - F800 8002 <sub>H</sub>	3 Byte	NVM.FSI_HOST (COMMx Registers)	Access	Access
F800 8003 <sub>H</sub> - F802 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F802 8000 <sub>H</sub> - F802 8002 <sub>H</sub>	3 Byte	NVM.FSI_CSRM (COMMx Registers)	Access	Access
F802 8003 <sub>H</sub> - F803 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F804 0000 <sub>H</sub> - F807 FFFF <sub>H</sub>	256 Kbyte	NVM.DMU (incl. SFR)	Access	Access
F808 0000 <sub>H</sub> - F808 FFFF <sub>H</sub>	64 Kbyte	NVM.DMU (Host Cmd. Seq. Interpreter)	SRIBE	Access
F809 0000 <sub>H</sub> - F80B FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F80C 0000 <sub>H</sub> - F80C FFFF <sub>H</sub>	64 Kbyte	NVM.DMU (CS Cmd. Seq. Interpreter)	SRIBE	Access
F80D 0000 <sub>H</sub> - F83F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F840 0000 <sub>H</sub> - F840 0FFF <sub>H</sub>	4 Kbyte	CPU0.FSFR	Access	Access
F840 1000 <sub>H</sub> - F840 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F841 0000 <sub>H</sub> - F841 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWB00.UR	Access	Access
F841 4000 <sub>H</sub> - F841 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE

**(table continues...)**



### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F841 8000 <sub>H</sub> - F841 BFFF <sub>H</sub>	16 Kbyte	NVM.PFRWB01.UR	Access	Access
F841 C000 <sub>H</sub> - F843 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F844 0000 <sub>H</sub> - F844 0FFF <sub>H</sub>	4 Kbyte	CPU1.FSFR	Access	Access
F844 1000 <sub>H</sub> - F844 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F845 0000 <sub>H</sub> - F845 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWB10.UR	Access	Access
F845 4000 <sub>H</sub> - F845 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F845 8000 <sub>H</sub> - F845 BFFF <sub>H</sub>	16 Kbyte	NVM.PFRWB11.UR	Access	Access
F845 C000 <sub>H</sub> - F847 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F848 0000 <sub>H</sub> - F848 0FFF <sub>H</sub>	4 Kbyte	CPU2.FSFR	Access	Access
F848 1000 <sub>H</sub> - F848 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F849 0000 <sub>H</sub> - F849 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWB20.UR	Access	Access
F849 4000 <sub>H</sub> - F849 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F849 8000 <sub>H</sub> - F849 BFFF <sub>H</sub>	16 Kbyte	NVM.PFRWB21.UR	Access	Access
F849 C000 <sub>H</sub> - F84B FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84C 0000 <sub>H</sub> - F84C 0FFF <sub>H</sub>	4 Kbyte	CPU3.FSFR	Access	Access
F84C 1000 <sub>H</sub> - F84C FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84D 0000 <sub>H</sub> - F84D 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWB30.UR	Access	Access
F84D 4000 <sub>H</sub> - F84D 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84D 8000 <sub>H</sub> - F84D BFFF <sub>H</sub>	16 Kbyte	NVM.PFRWB31.UR	Access	Access
F84D C000 <sub>H</sub> - F84F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F850 0000 <sub>H</sub> - F850 0FFF <sub>H</sub>	4 Kbyte	CPU4.FSFR	Access	Access
F850 1000 <sub>H</sub> - F850 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F851 0000 <sub>H</sub> - F851 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWB40.UR	Access	Access
F851 4000 <sub>H</sub> - F851 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F851 8000 <sub>H</sub> - F851 BFFF <sub>H</sub>	16 Kbyte	NVM.PFRWB41.UR	Access	Access
F851 C000 <sub>H</sub> - F853 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F854 0000 <sub>H</sub> - F854 0FFF <sub>H</sub>	4 Kbyte	CPU5.FSFR	Access	Access
F854 1000 <sub>H</sub> - F854 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F855 0000 <sub>H</sub> - F855 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWB50.UR	Access	Access
F855 4000 <sub>H</sub> - F855 7FFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F855 8000 <sub>H</sub> - F855 BFFF <sub>H</sub>	16 Kbyte	NVM.PFRWB51.UR	Access	Access
F855 C000 <sub>H</sub> - F857 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F858 0000 <sub>H</sub> - F858 0FFF <sub>H</sub>	4 Kbyte	CPUcs.FSFR	Access	Access
F858 1000 <sub>H</sub> - F858 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F859 0000 <sub>H</sub> - F859 3FFF <sub>H</sub>	16 Kbyte	NVM.PFRWBcs.UR	Access	Access
F859 4000 <sub>H</sub> - F87F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F880 0000 <sub>H</sub> - F880 FFFF <sub>H</sub>	64 Kbyte	CPU0.SFR (incl. STM0 and OVL)	Access	Access
F881 0000 <sub>H</sub> - F881 FFFF <sub>H</sub>	64 Kbyte	CPU0.HR1.CSFR	Access	Access
F882 0000 <sub>H</sub> - F882 FFFF <sub>H</sub>	64 Kbyte	CPU0.HR2.CSFR	Access	Access
F883 0000 <sub>H</sub> - F883 FFFF <sub>H</sub>	64 Kbyte	CPU0.HR0.CSFR	Access	Access
F884 0000 <sub>H</sub> - F884 FFFF <sub>H</sub>	64 Kbyte	CPU1.SFR (incl. STM1 and OVL)	Access	Access
F885 0000 <sub>H</sub> - F885 FFFF <sub>H</sub>	64 Kbyte	CPU1.HR1.CSFR	Access	Access
F886 0000 <sub>H</sub> - F886 FFFF <sub>H</sub>	64 Kbyte	CPU1.HR2.CSFR	Access	Access
F887 0000 <sub>H</sub> - F887 FFFF <sub>H</sub>	64 Kbyte	CPU1.HR0.CSFR	Access	Access
F888 0000 <sub>H</sub> - F888 FFFF <sub>H</sub>	64 Kbyte	CPU2.SFR (incl. STM2 and OVL)	Access	Access
F889 0000 <sub>H</sub> - F889 FFFF <sub>H</sub>	64 Kbyte	CPU2.HR1.CSFR	Access	Access
F88A 0000 <sub>H</sub> - F88A FFFF <sub>H</sub>	64 Kbyte	CPU2.HR2.CSFR	Access	Access
F88B 0000 <sub>H</sub> - F88B FFFF <sub>H</sub>	64 Kbyte	CPU2.HR0.CSFR	Access	Access
F88C 0000 <sub>H</sub> - F88C FFFF <sub>H</sub>	64 Kbyte	CPU3.SFR (incl. STM3 and OVL)	Access	Access
F88D 0000 <sub>H</sub> - F88D FFFF <sub>H</sub>	64 Kbyte	CPU3.HR1.CSFR	Access	Access
F88E 0000 <sub>H</sub> - F88E FFFF <sub>H</sub>	64 Kbyte	CPU3.HR2.CSFR	Access	Access
F88F 0000 <sub>H</sub> - F88F FFFF <sub>H</sub>	64 Kbyte	CPU3.HR0.CSFR	Access	Access
F890 0000 <sub>H</sub> - F890 FFFF <sub>H</sub>	64 Kbyte	CPU4.SFR (incl. STM4 and OVL)	Access	Access
F891 0000 <sub>H</sub> - F891 FFFF <sub>H</sub>	64 Kbyte	CPU4.HR1.CSFR	Access	Access
F892 0000 <sub>H</sub> - F892 FFFF <sub>H</sub>	64 Kbyte	CPU4.HR2.CSFR	Access	Access
F893 0000 <sub>H</sub> - F893 FFFF <sub>H</sub>	64 Kbyte	CPU4.HR0.CSFR	Access	Access
F894 0000 <sub>H</sub> - F894 FFFF <sub>H</sub>	64 Kbyte	CPU5.SFR (incl. STM5 and OVL)	Access	Access
F895 0000 <sub>H</sub> - F895 FFFF <sub>H</sub>	64 Kbyte	CPU5.HR1.CSFR	Access	Access
F896 0000 <sub>H</sub> - F896 FFFF <sub>H</sub>	64 Kbyte	CPU5.HR2.CSFR	Access	Access
F897 0000 <sub>H</sub> - F897 FFFF <sub>H</sub>	64 Kbyte	CPU5.HR0.CSFR	Access	Access
F898 0000 <sub>H</sub> - F898 FFFF <sub>H</sub>	64 Kbyte	CPUcs.SFR (incl. STMcs)	Access	Access
F899 0000 <sub>H</sub> - F899 FFFF <sub>H</sub>	64 Kbyte	CPUcs.HR1.CSFR	Access	Access
F89A 0000 <sub>H</sub> - F8CF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F8D0 0000 <sub>H</sub> - F8D4 FFFF <sub>H</sub>	320 Kbyte	CSS	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F8D5 0000 <sub>H</sub> - F8EF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F8F0 0000 <sub>H</sub> - F8F0 FFFF <sub>H</sub>	64 Kbyte	SRI0.SFR	Access	Access
F8F1 0000 <sub>H</sub> - F8F1 FFFF <sub>H</sub>	64 Kbyte	SRI1.SFR	Access	Access
F8F2 0000 <sub>H</sub> - F8F2 FFFF <sub>H</sub>	64 Kbyte	SRI2.SFR	Access	Access
F8F3 0000 <sub>H</sub> - F8F3 FFFF <sub>H</sub>	64 Kbyte	SRI3.SFR	Access	Access
F8F4 0000 <sub>H</sub> - F8F4 FFFF <sub>H</sub>	64 Kbyte	SRI4.SFR	Access	Access
F8F5 0000 <sub>H</sub> - F8F5 FFFF <sub>H</sub>	64 Kbyte	SRI5.SFR	Access	Access
F8F6 0000 <sub>H</sub> - F8F6 FFFF <sub>H</sub>	64 Kbyte	SRI6.SFR	Access	Access
F8F7 0000 <sub>H</sub> - F8F7 FFFF <sub>H</sub>	64 Kbyte	SRI7.SFR	Access	Access
F8F8 0000 <sub>H</sub> - F8FF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F900 0000 <sub>H</sub> - F901 FFFF <sub>H</sub>	128 Kbyte	GETH0.SFR	Access	Access
F902 0000 <sub>H</sub> - F902 FFFF <sub>H</sub>	64 Kbyte	GETH0.RAM	Access	Access
F903 0000 <sub>H</sub> - F903 7FFF <sub>H</sub>	32 Kbyte	DRE.SFR	Access	Access
F903 8000 <sub>H</sub> - F903 FFFF <sub>H</sub>	32 Kbyte	DRE.RAM	Access	Access
F904 0000 <sub>H</sub> - F904 FFFF <sub>H</sub>	64 Kbyte	xSPI.SFR	Access	Access
F905 0000 <sub>H</sub> - F907 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F908 0000 <sub>H</sub> - F909 FFFF <sub>H</sub>	128 Kbyte	eGTM.CLUSTER0	Access	Access
F90A 0000 <sub>H</sub> - F90B FFFF <sub>H</sub>	128 Kbyte	eGTM.CLUSTER1	Access	Access
F90C 0000 <sub>H</sub> - F90D FFFF <sub>H</sub>	128 Kbyte	eGTM.CLUSTER2	Access	Access
F90E 0000 <sub>H</sub> - F90E FFFF <sub>H</sub>	64 Kbyte	eGTM.SFR	Access	Access
F90F 0000 <sub>H</sub> - F93F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F940 0000 <sub>H</sub> - F943 FFFF <sub>H</sub>	256 Kbyte	LETH0	Access	Access
F944 0000 <sub>H</sub> - F980 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F981 0000 <sub>H</sub> - F981 FFFF <sub>H</sub>	64 Kbyte	PPU.STUDMI	Access	Access
F982 0000 <sub>H</sub> - F982 FFFF <sub>H</sub>	64 Kbyte	PPU.DEBUG	Access	Access
F983 0000 <sub>H</sub> - F983 FFFF <sub>H</sub>	64 Kbyte	PPU.SAFETY	Access	Access
F984 0000 <sub>H</sub> - F984 FFFF <sub>H</sub>	64 Kbyte	PPU.AP	Access	Access
F985 0000 <sub>H</sub> - F985 FFFF <sub>H</sub>	64 Kbyte	PPU.CSM.AP	Access	Access
F986 0000 <sub>H</sub> - F986 FFFF <sub>H</sub>	64 Kbyte	PPU.VMEM.AP	Access	Access
F987 0000 <sub>H</sub> - F98F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F990 0000 <sub>H</sub> - F990 FFFF <sub>H</sub>	64 Kbyte	LLI.SFR	Access	Access
F991 0000 <sub>H</sub> - F9FF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
FA00 0000 <sub>H</sub> - FA01 FFFF <sub>H</sub>	128 Kbyte	MCDS4P (incl. 32 Kbyte TBUF)	Access	Access
FA02 0000 <sub>H</sub> - FA03 FFFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FA04 0000 <sub>H</sub> - FA05 FFFF <sub>H</sub>	128 Kbyte	MCDS2P (incl. 16 Kbyte TBUF)	Access	Access
FA06 0000 <sub>H</sub> - FA0F FFFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FA10 0000 <sub>H</sub> - FA10 1FFF <sub>H</sub>	8 Kbyte	TRIF	Access	Access
FA10 2000 <sub>H</sub> - FA17 FFFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FA18 0000 <sub>H</sub> - FA18 03FF <sub>H</sub>	1 Kbyte	CBS	Access	Access
FA18 0400 <sub>H</sub> - FAFF FBFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FAFF FC00 <sub>H</sub> - FAFF FFFF <sub>H</sub>	1 Kbyte	TBCU	Access	Access
FB00 0000 <sub>H</sub> - FB00 FFFF <sub>H</sub>	64 Kbyte	LMU0.SFR	Access	Access
FB01 0000 <sub>H</sub> - FB01 FFFF <sub>H</sub>	64 Kbyte	LMU1.SFR	Access	Access
FB02 0000 <sub>H</sub> - FB02 FFFF <sub>H</sub>	64 Kbyte	LMU2.SFR	Access	Access
FB03 0000 <sub>H</sub> - FB03 FFFF <sub>H</sub>	64 Kbyte	LMU3.SFR	Access	Access
FB04 0000 <sub>H</sub> - FB04 FFFF <sub>H</sub>	64 Kbyte	LMU4.SFR	Access	Access
FB05 0000 <sub>H</sub> - FB05 FFFF <sub>H</sub>	64 Kbyte	LMU5.SFR	Access	Access
FB06 0000 <sub>H</sub> - FB06 FFFF <sub>H</sub>	64 Kbyte	LMU6.SFR	Access	Access
FB07 0000 <sub>H</sub> - FB07 FFFF <sub>H</sub>	64 Kbyte	LMU7.SFR	Access	Access
FB08 0000 <sub>H</sub> - FB08 FFFF <sub>H</sub>	64 Kbyte	LMU8.SFR	Access	Access
FB09 0000 <sub>H</sub> - FB09 FFFF <sub>H</sub>	64 Kbyte	LMU9.SFR	Access	Access
FB0A 0000 <sub>H</sub> - FBFF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
FC00 0000 <sub>H</sub> - FFFF FFFF <sub>H</sub>	64 Mbyte	PCIe0.DATA	Access	Access