

### 3 System address map

#### 3.2 Address map

The address maps define which address ranges are valid or invalid for the respective segment. The access type column describes the error status location if the access is not permitted.

For the detailed address space of a specific functional block (module), please refer to the specific user manual chapter describing that block.

##### 3.2.1 Segment 0

**Table 7 Address map of segment 0**

Address range	Size	Description	Access type	
			Read	Write
0000 0000 <sub>H</sub> - 0FFF FFFF <sub>H</sub>	-	Reserved	SRIBE <sup>1)</sup>	SRIBE <sup>1)</sup>

<sup>1)</sup> Any CPU load or store operation accessing 0000 0000<sub>H</sub> will trap

##### 3.2.2 Segment 1

**Table 8 Address map of segment 1**

Address range	Size	Description	Access type	
			Read	Write
1000 0000 <sub>H</sub> - 1001 BFFF <sub>H</sub>	112 Kbyte	CPUcs.DSPR	Access	Access
1001 C000 <sub>H</sub> - 1001 FFFF <sub>H</sub>	16 Kbyte	CPUcs.DSPR (extension) or CPUcs.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1002 0000 <sub>H</sub> - 100F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1010 0000 <sub>H</sub> - 1010 FFFF <sub>H</sub>	64 Kbyte	CPUcs.PSPR	Access	Access
1011 0000 <sub>H</sub> - 1011 7FFF <sub>H</sub>	32 Kbyte	CPUcs.PSPR (extension) or CPUcs.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1011 8000 <sub>H</sub> - 1011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1012 0000 <sub>H</sub> - 1012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1013 0000 <sub>H</sub> - 1013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1013 8000 <sub>H</sub> - 1013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1014 0000 <sub>H</sub> - 1014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1015 0000 <sub>H</sub> - 1015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1015 8000 <sub>H</sub> - 1015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1016 0000 <sub>H</sub> - 1016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1017 0000 <sub>H</sub> - 1017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1017 8000 <sub>H</sub> - 1017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1018 0000 <sub>H</sub> - 1018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1019 0000 <sub>H</sub> - 1019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access

(table continues...)

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**Table 8** (continued) Address map of segment 1

Address range	Size	Description	Access type	
			Read	Write
1019 8000 <sub>H</sub> - 1019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
101A 0000 <sub>H</sub> - 101A FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101B 0000 <sub>H</sub> - 101B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101B 8000 <sub>H</sub> - 101B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
101C 0000 <sub>H</sub> - 101C FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101D 0000 <sub>H</sub> - 101D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101D 8000 <sub>H</sub> - 101D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
101E 0000 <sub>H</sub> - 101E FFFF <sub>H</sub>	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101F 0000 <sub>H</sub> - 101F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101F 8000 <sub>H</sub> - 101F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1020 0000 <sub>H</sub> - 1020 17FF <sub>H</sub>	-	CPUcs.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1020 1800 <sub>H</sub> - 102F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
1030 0000 <sub>H</sub> - 1030 2FFF <sub>H</sub>	-	CPUcs.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
1030 3000 <sub>H</sub> - 1FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

#### 3.2.3 Segment 2

**Table 9** Address map of segment 2

Address range	Size	Description	Access type	
			Read	Write
2000 0000 <sub>H</sub> - 2FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

#### 3.2.4 Segment 3

**Table 10** Address map of segment 3

Address range	Size	Description	Access type	
			Read	Write
3000 0000 <sub>H</sub> - 3FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

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#### 3.2.5 Segment 4

**Table 11** Address map of segment 4

Address range	Size	Description	Access type	
			Read	Write
4000 0000 <sub>H</sub> - 4003 BFFF <sub>H</sub>	240 Kbyte	CPU3.DSPR	Access	Access
4003 C000 <sub>H</sub> - 4003 FFFF <sub>H</sub>	16 Kbyte	CPU3.DSPR (extension) or CPU3.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4004 0000 <sub>H</sub> - 400F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4010 0000 <sub>H</sub> - 4010 FFFF <sub>H</sub>	64 Kbyte	CPU3.PSPR	Access	Access
4011 0000 <sub>H</sub> - 4011 7FFF <sub>H</sub>	32 Kbyte	CPU3.PSPR (extension) or CPU3.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4011 8000 <sub>H</sub> - 4011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4012 0000 <sub>H</sub> - 4012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4013 0000 <sub>H</sub> - 4013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4013 8000 <sub>H</sub> - 4013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4014 0000 <sub>H</sub> - 4014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4015 0000 <sub>H</sub> - 4015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4015 8000 <sub>H</sub> - 4015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4016 0000 <sub>H</sub> - 4016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4017 0000 <sub>H</sub> - 4017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4017 8000 <sub>H</sub> - 4017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4018 0000 <sub>H</sub> - 4018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
4019 0000 <sub>H</sub> - 4019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
4019 8000 <sub>H</sub> - 4019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
401A 0000 <sub>H</sub> - 401A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
401B 0000 <sub>H</sub> - 401B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
401B 8000 <sub>H</sub> - 401B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
401C 0000 <sub>H</sub> - 401C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
401D 0000 <sub>H</sub> - 401D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
401D 8000 <sub>H</sub> - 401D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
401E 0000 <sub>H</sub> - 401E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU3.PSPR	Access	Access
401F 0000 <sub>H</sub> - 401F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
401F 8000 <sub>H</sub> - 401F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
4020 0000 <sub>H</sub> - 4020 17FF <sub>H</sub>	-	CPU3.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4020 1800 <sub>H</sub> - 402F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 11** (continued) Address map of segment 4

Address range	Size	Description	Access type	
			Read	Write
4030 0000 <sub>H</sub> - 4030 2FFF <sub>H</sub>	-	CPU3.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
4030 3000 <sub>H</sub> - 4FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.6 Segment 5

**Table 12** Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5000 0000 <sub>H</sub> - 5003 BFFF <sub>H</sub>	240 Kbyte	CPU2.DSPR	Access	Access
5003 C000 <sub>H</sub> - 5003 FFFF <sub>H</sub>	16 Kbyte	CPU2.DSPR (extension) or CPU2.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5004 0000 <sub>H</sub> - 500F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5010 0000 <sub>H</sub> - 5010 FFFF <sub>H</sub>	64 Kbyte	CPU2.PSPR	Access	Access
5011 0000 <sub>H</sub> - 5011 7FFF <sub>H</sub>	32 Kbyte	CPU2.PSPR (extension) or CPU2.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5011 8000 <sub>H</sub> - 5011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5012 0000 <sub>H</sub> - 5012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5013 0000 <sub>H</sub> - 5013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5013 8000 <sub>H</sub> - 5013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5014 0000 <sub>H</sub> - 5014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5015 0000 <sub>H</sub> - 5015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5015 8000 <sub>H</sub> - 5015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5016 0000 <sub>H</sub> - 5016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5017 0000 <sub>H</sub> - 5017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5017 8000 <sub>H</sub> - 5017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5018 0000 <sub>H</sub> - 5018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5019 0000 <sub>H</sub> - 5019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5019 8000 <sub>H</sub> - 5019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
501A 0000 <sub>H</sub> - 501A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501B 0000 <sub>H</sub> - 501B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501B 8000 <sub>H</sub> - 501B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
501C 0000 <sub>H</sub> - 501C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501D 0000 <sub>H</sub> - 501D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501D 8000 <sub>H</sub> - 501D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
501E 0000 <sub>H</sub> - 501E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501F 0000 <sub>H</sub> - 501F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501F 8000 <sub>H</sub> - 501F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
5020 0000 <sub>H</sub> - 5020 17FF <sub>H</sub>	-	CPU2.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5020 1800 <sub>H</sub> - 502F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 12** (continued) Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5030 0000 <sub>H</sub> - 5030 2FFF <sub>H</sub>	-	CPU2.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
5030 3000 <sub>H</sub> - 5FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.7 Segment 6

**Table 13** Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6000 0000 <sub>H</sub> - 6003 BFFF <sub>H</sub>	240 Kbyte	CPU1.DSPR	Access	Access
6003 C000 <sub>H</sub> - 6003 FFFF <sub>H</sub>	16 Kbyte	CPU1.DSPR (extension) or CPU1.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6004 0000 <sub>H</sub> - 600F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6010 0000 <sub>H</sub> - 6010 FFFF <sub>H</sub>	64 Kbyte	CPU1.PSPR	Access	Access
6011 0000 <sub>H</sub> - 6011 7FFF <sub>H</sub>	32 Kbyte	CPU1.PSPR (extension) or CPU1.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6011 8000 <sub>H</sub> - 6011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6012 0000 <sub>H</sub> - 6012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6013 0000 <sub>H</sub> - 6013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6013 8000 <sub>H</sub> - 6013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6014 0000 <sub>H</sub> - 6014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6015 0000 <sub>H</sub> - 6015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6015 8000 <sub>H</sub> - 6015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6016 0000 <sub>H</sub> - 6016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6017 0000 <sub>H</sub> - 6017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6017 8000 <sub>H</sub> - 6017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6018 0000 <sub>H</sub> - 6018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6019 0000 <sub>H</sub> - 6019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6019 8000 <sub>H</sub> - 6019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
601A 0000 <sub>H</sub> - 601A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601B 0000 <sub>H</sub> - 601B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601B 8000 <sub>H</sub> - 601B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
601C 0000 <sub>H</sub> - 601C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601D 0000 <sub>H</sub> - 601D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601D 8000 <sub>H</sub> - 601D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
601E 0000 <sub>H</sub> - 601E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601F 0000 <sub>H</sub> - 601F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601F 8000 <sub>H</sub> - 601F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
6020 0000 <sub>H</sub> - 6020 17FF <sub>H</sub>	-	CPU1.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6020 1800 <sub>H</sub> - 602F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 13** (continued) Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6030 0000 <sub>H</sub> - 6030 2FFF <sub>H</sub>	-	CPU1.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
6030 3000 <sub>H</sub> - 6FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space



### 3 System address map

#### 3.2.8 Segment 7

**Table 14** Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7000 0000 <sub>H</sub> - 7003 BFFF <sub>H</sub>	240 Kbyte	CPU0.DSPR	Access	Access
7003 C000 <sub>H</sub> - 7003 FFFF <sub>H</sub>	16 Kbyte	CPU0.DSPR (extension) or CPU0.DCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7004 0000 <sub>H</sub> - 700F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7010 0000 <sub>H</sub> - 7010 FFFF <sub>H</sub>	64 Kbyte	CPU0.PSPR	Access	Access
7011 0000 <sub>H</sub> - 7011 7FFF <sub>H</sub>	32 Kbyte	CPU0.PSPR (extension) or CPU0.PCACHE (RAM)	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7011 8000 <sub>H</sub> - 7011 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7012 0000 <sub>H</sub> - 7012 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7013 0000 <sub>H</sub> - 7013 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7013 8000 <sub>H</sub> - 7013 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7014 0000 <sub>H</sub> - 7014 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7015 0000 <sub>H</sub> - 7015 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7015 8000 <sub>H</sub> - 7015 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7016 0000 <sub>H</sub> - 7016 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7017 0000 <sub>H</sub> - 7017 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7017 8000 <sub>H</sub> - 7017 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7018 0000 <sub>H</sub> - 7018 FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7019 0000 <sub>H</sub> - 7019 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7019 8000 <sub>H</sub> - 7019 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
701A 0000 <sub>H</sub> - 701A FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701B 0000 <sub>H</sub> - 701B 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701B 8000 <sub>H</sub> - 701B FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
701C 0000 <sub>H</sub> - 701C FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701D 0000 <sub>H</sub> - 701D 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701D 8000 <sub>H</sub> - 701D FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
701E 0000 <sub>H</sub> - 701E FFFF <sub>H</sub>	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701F 0000 <sub>H</sub> - 701F 7FFF <sub>H</sub>	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701F 8000 <sub>H</sub> - 701F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
7020 0000 <sub>H</sub> - 7020 17FF <sub>H</sub>	-	CPU0.DTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7020 1800 <sub>H</sub> - 702F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

(table continues...)

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**Table 14** (continued) Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7030 0000 <sub>H</sub> - 7030 2FFF <sub>H</sub>	-	CPU0.PTAG	Access <sup>1)</sup> / SRIBE	Access <sup>1)</sup> / SRIBE
7030 3000 <sub>H</sub> - 7FFF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

### 3 System address map

#### 3.2.9 Segments 8 and 10

The read address of the program flash and erase counters varies for the following modes:

- Linear address map
- SOTA address map A
- SOTA address map B

##### 3.2.9.1 Segment 8 linear address map

**Table 15** Linear address map of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 <sub>H</sub> - 801F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8020 0000 <sub>H</sub> - 803F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
8040 0000 <sub>H</sub> - 805F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8060 0000 <sub>H</sub> - 807F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
8080 0000 <sub>H</sub> - 808F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
8090 0000 <sub>H</sub> - 809F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
80A0 0000 <sub>H</sub> - 80AF FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM30 (Program RRAM)	Access	SRIBE
80B0 0000 <sub>H</sub> - 80BF FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM31 (Program RRAM)	Access	SRIBE
80C0 0000 <sub>H</sub> - 83FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8400 0000 <sub>H</sub> - 8407 FFFF <sub>H</sub>	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
8408 0000 <sub>H</sub> - 8FDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FE0 0000 <sub>H</sub> - 8FE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 <sub>H</sub> - 8FFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FFE 0000 <sub>H</sub> - 8FFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 <sub>H</sub> - 8FFF FFFF <sub>H</sub>	64 Kbyte	NVMR.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.2 Segment 10 linear address map

**Table 16** Linear address map of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 <sub>H</sub> - A01F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A020 0000 <sub>H</sub> - A03F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
A040 0000 <sub>H</sub> - A05F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A060 0000 <sub>H</sub> - A07F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
A080 0000 <sub>H</sub> - A08F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
A090 0000 <sub>H</sub> - A09F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
A0A0 0000 <sub>H</sub> - A0AF FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM30 (Program RRAM)	Access	SRIBE
A0B0 0000 <sub>H</sub> - A0BF FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM31 (Program RRAM)	Access	SRIBE
A0C0 0000 <sub>H</sub> - A3FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A400 0000 <sub>H</sub> - A407 FFFF <sub>H</sub>	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
A408 0000 <sub>H</sub> - ADFE FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE00 0000 <sub>H</sub> - AE07 FFFF <sub>H</sub>	512 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE08 0000 <sub>H</sub> - AE3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE40 0000 <sub>H</sub> - AE40 7FFF <sub>H</sub>	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 <sub>H</sub> - AE7F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE80 0000 <sub>H</sub> - AE81 FFFF <sub>H</sub>	128 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE82 0000 <sub>H</sub> - AEBF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AEC0 0000 <sub>H</sub> - AEC0 7FFF <sub>H</sub>	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 <sub>H</sub> - AFDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFE0 0000 <sub>H</sub> - AFE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 <sub>H</sub> - AFFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFFE 0000 <sub>H</sub> - AFFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 <sub>H</sub> - AFFF FFFF <sub>H</sub>	64 Kbyte	NVMR.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.3 Segment 8 SOTA address map A

**Table 17** SOTA address map A of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 <sub>H</sub> - 801F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8020 0000 <sub>H</sub> - 803F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8040 0000 <sub>H</sub> - 804F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
8050 0000 <sub>H</sub> - 805F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM30 (Program RRAM)	Access	SRIBE
8060 0000 <sub>H</sub> - 81FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8200 0000 <sub>H</sub> - 821F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
8220 0000 <sub>H</sub> - 823F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
8240 0000 <sub>H</sub> - 824F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
8250 0000 <sub>H</sub> - 825F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM31 (Program RRAM)	Access	SRIBE
8260 0000 <sub>H</sub> - 83FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8400 0000 <sub>H</sub> - 8407 FFFF <sub>H</sub>	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
8408 0000 <sub>H</sub> - 8FDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FE0 0000 <sub>H</sub> - 8FE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 <sub>H</sub> - 8FFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FFE 0000 <sub>H</sub> - 8FFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 <sub>H</sub> - 8FFF FFFF <sub>H</sub>	64 Kbyte	NVMR.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.4 Segment 10 SOTA address map A

**Table 18** SOTA address map A of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 <sub>H</sub> - A01F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A020 0000 <sub>H</sub> - A03F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A040 0000 <sub>H</sub> - A04F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
A050 0000 <sub>H</sub> - A05F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM30 (Program RRAM)	Access	SRIBE
A060 0000 <sub>H</sub> - A1FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A200 0000 <sub>H</sub> - A21F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
A220 0000 <sub>H</sub> - A23F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
A240 0000 <sub>H</sub> - A24F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
A250 0000 <sub>H</sub> - A25F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM31 (Program RRAM)	Access	SRIBE
A260 0000 <sub>H</sub> - A3FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A400 0000 <sub>H</sub> - A407 FFFF <sub>H</sub>	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
A408 0000 <sub>H</sub> - ADFE FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE00 0000 <sub>H</sub> - AE07 FFFF <sub>H</sub>	512 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE08 0000 <sub>H</sub> - AE3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE40 0000 <sub>H</sub> - AE40 7FFF <sub>H</sub>	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 <sub>H</sub> - AE7F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE80 0000 <sub>H</sub> - AE81 FFFF <sub>H</sub>	128 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE82 0000 <sub>H</sub> - AEBF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AEC0 0000 <sub>H</sub> - AEC0 7FFF <sub>H</sub>	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 <sub>H</sub> - AFDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFE0 0000 <sub>H</sub> - AFE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 <sub>H</sub> - AFFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFFE 0000 <sub>H</sub> - AFFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 <sub>H</sub> - AFFF FFFF <sub>H</sub>	64 Kbyte	NVMR.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.5 Segment 8 SOTA address map B

**Table 19** SOTA address map B of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 <sub>H</sub> - 801F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
8020 0000 <sub>H</sub> - 803F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
8040 0000 <sub>H</sub> - 804F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
8050 0000 <sub>H</sub> - 805F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM31 (Program RRAM)	Access	SRIBE
8060 0000 <sub>H</sub> - 81FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8200 0000 <sub>H</sub> - 821F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8220 0000 <sub>H</sub> - 823F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8240 0000 <sub>H</sub> - 824F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
8250 0000 <sub>H</sub> - 825F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM30 (Program RRAM)	Access	SRIBE
8260 0000 <sub>H</sub> - 83FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8400 0000 <sub>H</sub> - 8407 FFFF <sub>H</sub>	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
8408 0000 <sub>H</sub> - 8FDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FE0 0000 <sub>H</sub> - 8FE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 <sub>H</sub> - 8FFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
8FFE 0000 <sub>H</sub> - 8FFE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 <sub>H</sub> - 8FFF FFFF <sub>H</sub>	64 Kbyte	NVMR.BROM	Access	SRIBE

### 3 System address map

#### 3.2.9.6 Segment 10 SOTA address map B

**Table 20** SOTA address map B of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 <sub>H</sub> - A01F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
A020 0000 <sub>H</sub> - A03F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
A040 0000 <sub>H</sub> - A04F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
A050 0000 <sub>H</sub> - A05F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM31 (Program RRAM)	Access	SRIBE
A060 0000 <sub>H</sub> - A1FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A200 0000 <sub>H</sub> - A21F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A220 0000 <sub>H</sub> - A23F FFFF <sub>H</sub>	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A240 0000 <sub>H</sub> - A24F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
A250 0000 <sub>H</sub> - A25F FFFF <sub>H</sub>	1 Mbyte	NVMR.PRRAM30 (Program RRAM)	Access	SRIBE
A260 0000 <sub>H</sub> - A3FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
A400 0000 <sub>H</sub> - A407 FFFF <sub>H</sub>	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
A408 0000 <sub>H</sub> - ADFE FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE00 0000 <sub>H</sub> - AE07 FFFF <sub>H</sub>	512 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE08 0000 <sub>H</sub> - AE3F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE40 0000 <sub>H</sub> - AE40 7FFF <sub>H</sub>	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 <sub>H</sub> - AE7F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AE80 0000 <sub>H</sub> - AE81 FFFF <sub>H</sub>	128 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE82 0000 <sub>H</sub> - AEBF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AEC0 0000 <sub>H</sub> - AEC0 7FFF <sub>H</sub>	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 <sub>H</sub> - AFDF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFE0 0000 <sub>H</sub> - AFE7 FFFF <sub>H</sub>	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 <sub>H</sub> - AFFD FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
AFFE 0000 <sub>H</sub> - AFDE FFFF <sub>H</sub>	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 <sub>H</sub> - AFFF FFFF <sub>H</sub>	64 Kbyte	NVMR.BROM	Access	SRIBE



### 3 System address map

#### 3.2.10 Segments 9 and 11

##### 3.2.10.1 Segment 9

**Table 21** Address map of segment 9

Address range	Size	Description	Access type	
			Read	Write
9000 0000 <sub>H</sub> - 9003 FFFF <sub>H</sub>	256 Kbyte	CPU0.DLMU	Access	Access
9004 0000 <sub>H</sub> - 9007 FFFF <sub>H</sub>	256 Kbyte	CPU1.DLMU	Access	Access
9008 0000 <sub>H</sub> - 900B FFFF <sub>H</sub>	256 Kbyte	CPU2.DLMU	Access	Access
900C 0000 <sub>H</sub> - 900D FFFF <sub>H</sub>	128 Kbyte	CPU3.DLMU	Access	Access
900E 0000 <sub>H</sub> - 903F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
9040 0000 <sub>H</sub> - 9047 FFFF <sub>H</sub>	512 Kbyte	LMU0	Access	Access
9048 0000 <sub>H</sub> - 9207 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
9208 0000 <sub>H</sub> - 920B FFFF <sub>H</sub>	256 Kbyte	PPU.CSM	Access	Access
920C 0000 <sub>H</sub> - 97FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
9800 0000 <sub>H</sub> - 9FFF FFFF <sub>H</sub>	128 Mbyte	xSPI	Access	Access

### 3 System address map

#### 3.2.10.2 Segment 11

**Table 22** Address map of segment 11

Address range	Size	Description	Access type	
			Read	Write
B000 0000 <sub>H</sub> - B003 FFFF <sub>H</sub>	256 Kbyte	CPU0.DLMU	Access	Access
B004 0000 <sub>H</sub> - B007 FFFF <sub>H</sub>	256 Kbyte	CPU1.DLMU	Access	Access
B008 0000 <sub>H</sub> - B00B FFFF <sub>H</sub>	256 Kbyte	CPU2.DLMU	Access	Access
B00C 0000 <sub>H</sub> - B00D FFFF <sub>H</sub>	128 Kbyte	CPU3.DLMU	Access	Access
B00E 0000 <sub>H</sub> - B03F FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B040 0000 <sub>H</sub> - B047 FFFF <sub>H</sub>	512 Kbyte	LMU0	Access	Access
B048 0000 <sub>H</sub> - B203 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B204 0000 <sub>H</sub> - B205 FFFF <sub>H</sub>	128 Kbyte	PPU.VMEM	Access	Access
B206 0000 <sub>H</sub> - B207 FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B208 0000 <sub>H</sub> - B20B FFFF <sub>H</sub>	256 Kbyte	PPU.CSM	Access	Access
B20C 0000 <sub>H</sub> - B7FF FFFF <sub>H</sub>	-	Reserved	SRIBE	SRIBE
B800 0000 <sub>H</sub> - BFFF FFFF <sub>H</sub>	128 Mbyte	xSPI	Access	Access

### 3 System address map

#### 3.2.11 Segment 12

**Table 23** Address map of segment 12

Address range	Size	Description	Access type	
			Read	Write
C000 0000 <sub>H</sub> - CFFF FFFF <sub>H</sub>	-	Reserved <sup>1) 2)</sup>	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

2) See the PPU chapter for details

#### 3.2.12 Segment 13

**Table 24** Address map of segment 13

Address range	Size	Description	Access type	
			Read	Write
D000 0000 <sub>H</sub> - DFFF FFFF <sub>H</sub>	-	Reserved <sup>1)</sup>	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

### 3 System address map

#### 3.2.13 Segment 14

**Table 25** Address map of segment 14

Address range	Size	Unit	Access type	
			Read	Write
E000 0000 <sub>H</sub> - E501 FFFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE
E502 0000 <sub>H</sub> - E502 07FF <sub>H</sub>	2 Kbyte	TMADC0 (Read Only)	Access	LLIBE
E502 0800 <sub>H</sub> - E502 0FFF <sub>H</sub>	2 Kbyte	TMADC1 (Read Only)	Access	LLIBE
E502 1000 <sub>H</sub> - E502 17FF <sub>H</sub>	2 Kbyte	TMADC2 (Read Only)	Access	LLIBE
E502 1800 <sub>H</sub> - E502 1FFF <sub>H</sub>	2 Kbyte	TMADC3 (Read Only)	Access	LLIBE
E502 2000 <sub>H</sub> - E502 3FFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE
E502 4000 <sub>H</sub> - E502 43FF <sub>H</sub>	1 Kbyte	DSADC0 (Read Only)	Access	LLIBE
E502 4400 <sub>H</sub> - E502 47FF <sub>H</sub>	1 Kbyte	DSADC1 (Read Only)	Access	LLIBE
E502 4800 <sub>H</sub> - E502 4BFF <sub>H</sub>	1 Kbyte	DSADC2 (Read Only)	Access	LLIBE
E502 4C00 <sub>H</sub> - E502 4FFF <sub>H</sub>	1 Kbyte	DSADC3 (Read Only)	Access	LLIBE
E502 5000 <sub>H</sub> - E502 53FF <sub>H</sub>	1 Kbyte	DSADC4 (Read Only)	Access	LLIBE
E502 5400 <sub>H</sub> - E502 57FF <sub>H</sub>	1 Kbyte	DSADC5 (Read Only)	Access	LLIBE
E502 5800 <sub>H</sub> - E502 5BFF <sub>H</sub>	1 Kbyte	DSADC6 (Read Only)	Access	LLIBE
E502 5C00 <sub>H</sub> - E502 5FFF <sub>H</sub>	1 Kbyte	DSADC7 (Read Only)	Access	LLIBE
E502 6000 <sub>H</sub> - E502 63FF <sub>H</sub>	1 Kbyte	DSADC8 (Read Only)	Access	LLIBE
E502 6400 <sub>H</sub> - E502 67FF <sub>H</sub>	1 Kbyte	DSADC9 (Read Only)	Access	LLIBE
E502 6800 <sub>H</sub> - E502 7FFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE
E502 8000 <sub>H</sub> - E502 83FF <sub>H</sub>	1 Kbyte	EXMOD0 (Read Only)	Access	LLIBE
E502 8400 <sub>H</sub> - E502 87FF <sub>H</sub>	1 Kbyte	EXMOD1 (Read Only)	Access	LLIBE
E502 8800 <sub>H</sub> - E502 8BFF <sub>H</sub>	1 Kbyte	EXMOD2 (Read Only)	Access	LLIBE
E502 8C00 <sub>H</sub> - E502 8FFF <sub>H</sub>	1 Kbyte	EXMOD3 (Read Only)	Access	LLIBE
E502 9000 <sub>H</sub> - E502 93FF <sub>H</sub>	1 Kbyte	EXMOD4 (Read Only)	Access	LLIBE
E502 9400 <sub>H</sub> - E502 97FF <sub>H</sub>	1 Kbyte	EXMOD5 (Read Only)	Access	LLIBE
E502 9800 <sub>H</sub> - E502 9BFF <sub>H</sub>	1 Kbyte	EXMOD6 (Read Only)	Access	LLIBE
E502 9C00 <sub>H</sub> - E502 9FFF <sub>H</sub>	1 Kbyte	EXMOD7 (Read Only)	Access	LLIBE
E502 A000 <sub>H</sub> - E502 AFFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE
E502 B000 <sub>H</sub> - E502 CFFF <sub>H</sub>	8 Kbyte	CDSP0 (Read Only)	Access	LLIBE
E502 D000 <sub>H</sub> - E502 EFFF <sub>H</sub>	8 Kbyte	CDSP1 (Read Only)	Access	LLIBE
E502 F000 <sub>H</sub> - E503 0FFF <sub>H</sub>	8 Kbyte	CDSP2 (Read Only)	Access	LLIBE
E503 1000 <sub>H</sub> - E503 2FFF <sub>H</sub>	8 Kbyte	CDSP3 (Read Only)	Access	LLIBE
E503 3000 <sub>H</sub> - E503 4FFF <sub>H</sub>	8 Kbyte	CDSP4 (Read Only)	Access	LLIBE

(table continues...)

### 3 System address map

**Table 25** (continued) Address map of segment 14

Address range	Size	Unit	Access type	
			Read	Write
E503 5000 <sub>H</sub> - E503 6FFF <sub>H</sub>	8 Kbyte	CDSP5 (Read Only)	Access	LLIBE
E503 7000 <sub>H</sub> - E503 8FFF <sub>H</sub>	8 Kbyte	CDSP6 (Read Only)	Access	LLIBE
E503 9000 <sub>H</sub> - E503 AFFF <sub>H</sub>	8 Kbyte	CDSP7 (Read Only)	Access	LLIBE
E503 B000 <sub>H</sub> - E503 CFFF <sub>H</sub>	8 Kbyte	CDSP8 (Read Only)	Access	LLIBE
E503 D000 <sub>H</sub> - E503 EFFF <sub>H</sub>	8 Kbyte	CDSP9 (Read Only)	Access	LLIBE
E503 F000 <sub>H</sub> - E504 0FFF <sub>H</sub>	8 Kbyte	CDSP10 (Read Only)	Access	LLIBE
E504 1000 <sub>H</sub> - E504 2FFF <sub>H</sub>	8 Kbyte	CDSP11 (Read Only)	Access	LLIBE
E504 3000 <sub>H</sub> - E907 FFFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE
E908 0000 <sub>H</sub> - E909 FFFF <sub>H</sub>	128 Kbyte	eGTM.CLUSTER0	Access	Access
E90A 0000 <sub>H</sub> - E90B FFFF <sub>H</sub>	128 Kbyte	eGTM.CLUSTER1	Access	Access
E90C 0000 <sub>H</sub> - E90D FFFF <sub>H</sub>	128 Kbyte	eGTM.CLUSTER2	Access	Access
E90E 0000 <sub>H</sub> - E90E 1FFF <sub>H</sub>	8 Kbyte	eGTM.SFR	Access	Access
E90E 2000 <sub>H</sub> - E90E 23FF <sub>H</sub>	1 Kbyte	HRPWM.SFR	Access	Access
E90E 2400 <sub>H</sub> - E90E 27FF <sub>H</sub>	1 Kbyte	HRPWM0	Access	Access
E90E 2800 <sub>H</sub> - E90E 2BFF <sub>H</sub>	1 Kbyte	HRPWM1	Access	Access
E90E 2C00 <sub>H</sub> - E90E 2FFF <sub>H</sub>	1 Kbyte	HRPWM2	Access	Access
E90E 3000 <sub>H</sub> - E97F FFFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE
E980 0000 <sub>H</sub> - E980 FFFF <sub>H</sub>	64 Kbyte	PPU.SFR	Access	Access
E981 0000 <sub>H</sub> - EFFF FFFF <sub>H</sub>	–	Reserved	LLIBE	LLIBE

### 3 System address map

#### 3.2.14 Segment 15

The address map of segment 15 includes the following module address ranges:

- Absolute Addressing Range
  - If a module is addressed in the first 16 Kbyte of segment 15, the CPU can access the module with absolute addressing mode
- Others
  - If a module is addressed above the first 16 Kbyte of segment 15, the CPU can access the module with base + offset

**Table 26 Address map of segment 15**

Address range	Size	Unit	Access type	
			Read	Write
F000 0000 <sub>H</sub> - F000 07FF <sub>H</sub>	2 Kbyte	WTU	Access	Access
F000 0800 <sub>H</sub> - F000 09FF <sub>H</sub>	512 Byte	MSC0	Access	Access
F000 0A00 <sub>H</sub> - F000 0BFF <sub>H</sub>	512 Byte	MSC1	Access	Access
F000 0C00 <sub>H</sub> - F000 0FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F000 1000 <sub>H</sub> - F000 11FF <sub>H</sub>	512 Byte	QSPI0	Access	Access
F000 1200 <sub>H</sub> - F000 13FF <sub>H</sub>	512 Byte	QSPI1	Access	Access
F000 1400 <sub>H</sub> - F000 15FF <sub>H</sub>	512 Byte	QSPI2	Access	Access
F000 1600 <sub>H</sub> - F000 17FF <sub>H</sub>	512 Byte	QSPI3	Access	Access
F000 1800 <sub>H</sub> - F000 19FF <sub>H</sub>	512 Byte	QSPI4	Access	Access
F000 1A00 <sub>H</sub> - F000 1BFF <sub>H</sub>	512 Byte	QSPI5	Access	Access
F000 1C00 <sub>H</sub> - F000 1DFF <sub>H</sub>	512 Byte	QSPI6	Access	Access
F000 1E00 <sub>H</sub> - F000 1FFF <sub>H</sub>	512 Byte	QSPI7	Access	Access
F000 2000 <sub>H</sub> - F000 2DFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F000 2E00 <sub>H</sub> - F000 2FFF <sub>H</sub>	512 Byte	FCE0	Access	Access
F000 3000 <sub>H</sub> - F000 3BFF <sub>H</sub>	6 x 512 Byte	SENT0	Access	Access
F000 3C00 <sub>H</sub> - F000 3FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F000 4000 <sub>H</sub> - F000 4BFF <sub>H</sub>	6 x 512 Byte	SENT1	Access	Access
F000 4C00 <sub>H</sub> - F000 4FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F000 5000 <sub>H</sub> - F000 5BFF <sub>H</sub>	6 x 512 Byte	PSI5	Access	Access
F000 5C00 <sub>H</sub> - F000 6FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F000 7000 <sub>H</sub> - F000 7FFF <sub>H</sub>	4 Kbyte	PSI5S0	Access	Access
F000 8000 <sub>H</sub> - F000 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F001 0000 <sub>H</sub> - F001 3FFF <sub>H</sub>	16 Kbyte	DMA0	Access	Access
F001 4000 <sub>H</sub> - F001 BFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F001 C000 <sub>H</sub> - F001 CFFF <sub>H</sub>	4 Kbyte	ERAY0	Access	Access
F001 D000 <sub>H</sub> - F001 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F002 0000 <sub>H</sub> - F002 01FF <sub>H</sub>	512 Byte	GPT120	Access	Access
F002 0200 <sub>H</sub> - F002 03FF <sub>H</sub>	512 Byte	GPT121	Access	Access
F002 0400 <sub>H</sub> - F002 05FF <sub>H</sub>	512 Byte	GPT122	Access	Access
F002 0600 <sub>H</sub> - F002 07FF <sub>H</sub>	512 Byte	GPT123	Access	Access
F002 0800 <sub>H</sub> - F002 3FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F002 4000 <sub>H</sub> - F002 43FF <sub>H</sub>	1 Kbyte	SCU	Access	Access
F002 4400 <sub>H</sub> - F002 7FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F002 8000 <sub>H</sub> - F002 9FFF <sub>H</sub>	8 Kbyte	SMU	Access	Access
F002 A000 <sub>H</sub> - F002 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 0000 <sub>H</sub> - F003 1FFF <sub>H</sub>	8 Kbyte	IR.SFR	Access	Access
F003 2000 <sub>H</sub> - F003 5FFF <sub>H</sub>	16 Kbyte	IR.SRC	Access	Access
F003 6000 <sub>H</sub> - F003 9FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 A000 <sub>H</sub> - F003 A3FF <sub>H</sub>	1 Kbyte	P00	Access	Access
F003 A400 <sub>H</sub> - F003 A7FF <sub>H</sub>	1 Kbyte	P01	Access	Access
F003 A800 <sub>H</sub> - F003 ABFF <sub>H</sub>	1 Kbyte	P02	Access	Access
F003 AC00 <sub>H</sub> - F003 C7FF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 C800 <sub>H</sub> - F003 CBFF <sub>H</sub>	1 Kbyte	P10	Access	Access
F003 CC00 <sub>H</sub> - F003 CFFF <sub>H</sub>	1 Kbyte	P11	Access	Access
F003 D000 <sub>H</sub> - F003 D3FF <sub>H</sub>	1 Kbyte	P12	Access	Access
F003 D400 <sub>H</sub> - F003 D7FF <sub>H</sub>	1 Kbyte	P13	Access	Access
F003 D800 <sub>H</sub> - F003 DBFF <sub>H</sub>	1 Kbyte	P14	Access	Access
F003 DC00 <sub>H</sub> - F003 DFFF <sub>H</sub>	1 Kbyte	P15	Access	Access
F003 E000 <sub>H</sub> - F003 EFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F003 F000 <sub>H</sub> - F003 F3FF <sub>H</sub>	1 Kbyte	P20	Access	Access
F003 F400 <sub>H</sub> - F003 F7FF <sub>H</sub>	1 Kbyte	P21	Access	Access
F003 F800 <sub>H</sub> - F003 FBFF <sub>H</sub>	1 Kbyte	P22	Access	Access
F003 FC00 <sub>H</sub> - F003 FFFF <sub>H</sub>	1 Kbyte	P23	Access	Access
F004 0000 <sub>H</sub> - F004 03FF <sub>H</sub>	1 Kbyte	P24	Access	Access
F004 0400 <sub>H</sub> - F004 17FF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F004 1800 <sub>H</sub> - F004 1BFF <sub>H</sub>	1 Kbyte	P30	Access	Access
F004 1C00 <sub>H</sub> - F004 1FFF <sub>H</sub>	1 Kbyte	P31	Access	Access
F004 2000 <sub>H</sub> - F004 23FF <sub>H</sub>	1 Kbyte	P32	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F004 2400 <sub>H</sub> - F004 27FF <sub>H</sub>	1 Kbyte	P33	Access	Access
F004 2800 <sub>H</sub> - F004 2BFF <sub>H</sub>	1 Kbyte	P34	Access	Access
F004 2C00 <sub>H</sub> - F004 3FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F004 4000 <sub>H</sub> - F004 43FF <sub>H</sub>	1 Kbyte	P40	Access	Access
F004 4400 <sub>H</sub> - F004 47FF <sub>H</sub>	1 Kbyte	P41	Access	Access
F004 4800 <sub>H</sub> - F005 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F006 0000 <sub>H</sub> - F006 3FFF <sub>H</sub>	16 Kbyte	SMM	Access	Access
F006 4000 <sub>H</sub> - F006 7FFF <sub>H</sub>	16 Kbyte	CCU	Access	Access
F006 8000 <sub>H</sub> - F006 80FF <sub>H</sub>	256 Byte	TRI	Access	Access
F006 8100 <sub>H</sub> - F006 BFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F006 C000 <sub>H</sub> - F006 FFFF <sub>H</sub>	16 Kbyte	VTMON	Access	Access
F007 0000 <sub>H</sub> - F007 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F008 0000 <sub>H</sub> - F008 03FF <sub>H</sub>	4 x 256 Byte	HSSL0	Access	Access
F008 0400 <sub>H</sub> - F008 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F009 0000 <sub>H</sub> - F009 FFFF <sub>H</sub>	64 Kbyte	HSCT0	Access	Access
F00A 0000 <sub>H</sub> - F00B FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F00C 0000 <sub>H</sub> - F00D FFFF <sub>H</sub>	128 Kbyte	I2C0	Access	Access
F00E 0000 <sub>H</sub> - F00F FFFF <sub>H</sub>	128 Kbyte	I2C1	Access	Access
F010 0000 <sub>H</sub> - F023 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F024 0000 <sub>H</sub> - F024 7FFF <sub>H</sub>	32 Kbyte	SCR.XRAM	Access	Access
F024 8000 <sub>H</sub> - F024 8FFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F024 9000 <sub>H</sub> - F024 9FFF <sub>H</sub>	4 Kbyte	PMS	Access	Access
F024 A000 <sub>H</sub> - F02A FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F02B 0000 <sub>H</sub> - F02B 0FFF <sub>H</sub>	4 Kbyte	SDMMC0	Access	Access
F02B 1000 <sub>H</sub> - F02B FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F02C 0000 <sub>H</sub> - F02C 01FF <sub>H</sub>	512 Byte	ASCLIN0	Access	Access
F02C 0200 <sub>H</sub> - F02C 03FF <sub>H</sub>	512 Byte	ASCLIN1	Access	Access
F02C 0400 <sub>H</sub> - F02C 05FF <sub>H</sub>	512 Byte	ASCLIN2	Access	Access
F02C 0600 <sub>H</sub> - F02C 07FF <sub>H</sub>	512 Byte	ASCLIN3	Access	Access
F02C 0800 <sub>H</sub> - F02C 09FF <sub>H</sub>	512 Byte	ASCLIN4	Access	Access
F02C 0A00 <sub>H</sub> - F02C 0BFF <sub>H</sub>	512 Byte	ASCLIN5	Access	Access
F02C 0C00 <sub>H</sub> - F02C 0DFF <sub>H</sub>	512 Byte	ASCLIN6	Access	Access

(table continues...)



### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F02C 0E00 <sub>H</sub> - F02C 0FFF <sub>H</sub>	512 Byte	ASCLIN7	Access	Access
F02C 1000 <sub>H</sub> - F02F FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F030 0000 <sub>H</sub> - F030 8FFF <sub>H</sub>	36 Kbyte	CAN0.SRAM	Access	Access
F030 9000 <sub>H</sub> - F030 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F031 0000 <sub>H</sub> - F031 10FF <sub>H</sub>	4352 Byte	CAN0.SFR	Access	Access
F031 1100 <sub>H</sub> - F031 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F032 0000 <sub>H</sub> - F032 4FFF <sub>H</sub>	20 Kbyte	CAN1.SRAM	Access	Access
F032 5000 <sub>H</sub> - F032 FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F033 0000 <sub>H</sub> - F033 10FF <sub>H</sub>	4352 Byte	CAN1.SFR	Access	Access
F033 1100 <sub>H</sub> - F03F FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F040 0000 <sub>H</sub> - F041 FFFF <sub>H</sub>	128 Kbyte	VMT0	Access	Access
F042 0000 <sub>H</sub> - F043 FFFF <sub>H</sub>	128 Kbyte	VMT1	Access	Access
F044 0000 <sub>H</sub> - F045 FFFF <sub>H</sub>	128 Kbyte	VMT2	Access	Access
F046 0000 <sub>H</sub> - F047 FFFF <sub>H</sub>	128 Kbyte	VMT3	Access	Access
F048 0000 <sub>H</sub> - F049 FFFF <sub>H</sub>	128 Kbyte	VMT4	Access	Access
F04A 0000 <sub>H</sub> - F04B FFFF <sub>H</sub>	128 Kbyte	VMT5	Access	Access
F04C 0000 <sub>H</sub> - F04D FFFF <sub>H</sub>	128 Kbyte	VMT6	Access	Access
F04E 0000 <sub>H</sub> - F17B FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F17C 0000 <sub>H</sub> - F17C 1FFF <sub>H</sub>	8 Kbyte	CANXL0.SRAM	Access	Access
F17C 2000 <sub>H</sub> - F17C FFFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F17D 0000 <sub>H</sub> - F17F FFFF <sub>H</sub>	192 Kbyte	CANXL0.SFR	Access	Access
F180 0000 <sub>H</sub> - F1FF FBFF <sub>H</sub>	–	Reserved	SPBBE	SPBBE
F1FF FC00 <sub>H</sub> - F1FF FFFF <sub>H</sub>	1 Kbyte	SBCU	Access	Access
F200 0000 <sub>H</sub> - F3FF FFFF <sub>H</sub>	32 Mbyte	HSPHY	Access	Access
F400 0000 <sub>H</sub> - F41F FFFF <sub>H</sub>	2 Mbyte	GTM	Access	Access
F420 0000 <sub>H</sub> - F4FF FFFF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F500 0000 <sub>H</sub> - F500 1FFF <sub>H</sub>	8 Kbyte	ADC.SFR	Access	Access
F500 2000 <sub>H</sub> - F501 F7FF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F501 F800 <sub>H</sub> - F501 FFFF <sub>H</sub>	2 Kbyte	TMADC.SFR	Access	Access
F502 0000 <sub>H</sub> - F502 07FF <sub>H</sub>	2 Kbyte	TMADC0 (Read Write)	Access	Access
F502 0800 <sub>H</sub> - F502 0FFF <sub>H</sub>	2 Kbyte	TMADC1 (Read Write)	Access	Access
F502 1000 <sub>H</sub> - F502 17FF <sub>H</sub>	2 Kbyte	TMADC2 (Read Write)	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F502 1800 <sub>H</sub> - F502 1FFF <sub>H</sub>	2 Kbyte	TMADC3 (Read Write)	Access	Access
F502 2000 <sub>H</sub> - F503 F7FF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F503 F800 <sub>H</sub> - F503 FFFF <sub>H</sub>	2 Kbyte	FCC.SFR	Access	Access
F504 0000 <sub>H</sub> - F504 03FF <sub>H</sub>	1 Kbyte	FCC0	Access	Access
F504 0400 <sub>H</sub> - F504 07FF <sub>H</sub>	1 Kbyte	FCC1	Access	Access
F504 0800 <sub>H</sub> - F504 0BFF <sub>H</sub>	1 Kbyte	FCC2	Access	Access
F504 0C00 <sub>H</sub> - F504 0FFF <sub>H</sub>	1 Kbyte	FCC3	Access	Access
F504 1000 <sub>H</sub> - F504 13FF <sub>H</sub>	1 Kbyte	FCC4	Access	Access
F504 1400 <sub>H</sub> - F504 17FF <sub>H</sub>	1 Kbyte	FCC5	Access	Access
F504 1800 <sub>H</sub> - F504 1BFF <sub>H</sub>	1 Kbyte	FCC6	Access	Access
F504 1C00 <sub>H</sub> - F504 1FFF <sub>H</sub>	1 Kbyte	FCC7	Access	Access
F504 2000 <sub>H</sub> - F504 23FF <sub>H</sub>	1 Kbyte	FCC8	Access	Access
F504 2400 <sub>H</sub> - F504 27FF <sub>H</sub>	1 Kbyte	FCC9	Access	Access
F504 2800 <sub>H</sub> - F505 F7FF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F505 F800 <sub>H</sub> - F505 FFFF <sub>H</sub>	2 Kbyte	DSADC.SFR	Access	Access
F506 0000 <sub>H</sub> - F506 03FF <sub>H</sub>	1 Kbyte	DSADC0	Access	Access
F506 0400 <sub>H</sub> - F506 07FF <sub>H</sub>	1 Kbyte	DSADC1	Access	Access
F506 0800 <sub>H</sub> - F506 0BFF <sub>H</sub>	1 Kbyte	DSADC2	Access	Access
F506 0C00 <sub>H</sub> - F506 0FFF <sub>H</sub>	1 Kbyte	DSADC3	Access	Access
F506 1000 <sub>H</sub> - F506 13FF <sub>H</sub>	1 Kbyte	DSADC4	Access	Access
F506 1400 <sub>H</sub> - F506 17FF <sub>H</sub>	1 Kbyte	DSADC5	Access	Access
F506 1800 <sub>H</sub> - F506 1BFF <sub>H</sub>	1 Kbyte	DSADC6	Access	Access
F506 1C00 <sub>H</sub> - F506 1FFF <sub>H</sub>	1 Kbyte	DSADC7	Access	Access
F506 2000 <sub>H</sub> - F506 23FF <sub>H</sub>	1 Kbyte	DSADC8	Access	Access
F506 2400 <sub>H</sub> - F506 27FF <sub>H</sub>	1 Kbyte	DSADC9	Access	Access
F506 2800 <sub>H</sub> - F506 37FF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F506 3800 <sub>H</sub> - F506 3BFF <sub>H</sub>	1 Kbyte	EXMOD0	Access	Access
F506 3C00 <sub>H</sub> - F506 3FFF <sub>H</sub>	1 Kbyte	EXMOD1	Access	Access
F506 4000 <sub>H</sub> - F506 43FF <sub>H</sub>	1 Kbyte	EXMOD2	Access	Access
F506 4400 <sub>H</sub> - F506 47FF <sub>H</sub>	1 Kbyte	EXMOD3	Access	Access
F506 4800 <sub>H</sub> - F506 4BFF <sub>H</sub>	1 Kbyte	EXMOD4	Access	Access
F506 4C00 <sub>H</sub> - F506 4FFF <sub>H</sub>	1 Kbyte	EXMOD5	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F506 5000 <sub>H</sub> - F506 53FF <sub>H</sub>	1 Kbyte	EXMOD6	Access	Access
F506 5400 <sub>H</sub> - F506 57FF <sub>H</sub>	1 Kbyte	EXMOD7	Access	Access
F506 5800 <sub>H</sub> - F50B F7FF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F50B F800 <sub>H</sub> - F50B FFFF <sub>H</sub>	2 Kbyte	CDSP.SFR	Access	Access
F50C 0000 <sub>H</sub> - F50C 1FFF <sub>H</sub>	8 Kbyte	CDSP0	Access	Access
F50C 2000 <sub>H</sub> - F50C 3FFF <sub>H</sub>	8 Kbyte	CDSP1	Access	Access
F50C 4000 <sub>H</sub> - F50C 5FFF <sub>H</sub>	8 Kbyte	CDSP2	Access	Access
F50C 6000 <sub>H</sub> - F50C 7FFF <sub>H</sub>	8 Kbyte	CDSP3	Access	Access
F50C 8000 <sub>H</sub> - F50C 9FFF <sub>H</sub>	8 Kbyte	CDSP4	Access	Access
F50C A000 <sub>H</sub> - F50C BFFF <sub>H</sub>	8 Kbyte	CDSP5	Access	Access
F50C C000 <sub>H</sub> - F50C DFFF <sub>H</sub>	8 Kbyte	CDSP6	Access	Access
F50C E000 <sub>H</sub> - F50C FFFF <sub>H</sub>	8 Kbyte	CDSP7	Access	Access
F50D 0000 <sub>H</sub> - F50D 1FFF <sub>H</sub>	8 Kbyte	CDSP8	Access	Access
F50D 2000 <sub>H</sub> - F50D 3FFF <sub>H</sub>	8 Kbyte	CDSP9	Access	Access
F50D 4000 <sub>H</sub> - F50D 5FFF <sub>H</sub>	8 Kbyte	CDSP10	Access	Access
F50D 6000 <sub>H</sub> - F50D 7FFF <sub>H</sub>	8 Kbyte	CDSP11	Access	Access
F50D 8000 <sub>H</sub> - F5FF FBFF <sub>H</sub>	–	Reserved	CPBBE	CPBBE
F5FF FC00 <sub>H</sub> - F5FF FFFF <sub>H</sub>	1 Kbyte	CBCU	Access	Access
F600 0000 <sub>H</sub> - F600 01FF <sub>H</sub>	512 Byte	CSCU	Access	Access
F600 0200 <sub>H</sub> - F600 03FF <sub>H</sub>	512 Byte	TRNG	Access	Access
F600 0400 <sub>H</sub> - F600 FFFF <sub>H</sub>	–	Reserved	CSPBBE	CSPBBE
F601 0000 <sub>H</sub> - F601 7FFF <sub>H</sub>	32 Kbyte	PKC	Access	Access
F601 8000 <sub>H</sub> - F6FF FBFF <sub>H</sub>	–	Reserved	CSPBBE	CSPBBE
F6FF FC00 <sub>H</sub> - F6FF FFFF <sub>H</sub>	1 Kbyte	CSBCU	Access	Access
F700 0000 <sub>H</sub> - F7FF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F800 0000 <sub>H</sub> - F807 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F808 0000 <sub>H</sub> - F808 7FFF <sub>H</sub>	32 Kbyte	NVMR.DMUR0.UR	Access	Access
F808 8000 <sub>H</sub> - F80B FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F80C 0000 <sub>H</sub> - F80C 7FFF <sub>H</sub>	32 Kbyte	NVMR.DMUR1.UR	Access	Access
F80C 8000 <sub>H</sub> - F83F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F840 0000 <sub>H</sub> - F840 0FFF <sub>H</sub>	4 Kbyte	CPU0.FSFR	Access	Access
F840 1000 <sub>H</sub> - F840 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F841 0000 <sub>H</sub> - F841 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR00.UR	Access	Access
F841 8000 <sub>H</sub> - F841 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F842 0000 <sub>H</sub> - F842 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR01.UR	Access	Access
F842 8000 <sub>H</sub> - F843 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F844 0000 <sub>H</sub> - F844 0FFF <sub>H</sub>	4 Kbyte	CPU1.FSFR	Access	Access
F844 1000 <sub>H</sub> - F844 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F845 0000 <sub>H</sub> - F845 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR10.UR	Access	Access
F845 8000 <sub>H</sub> - F845 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F846 0000 <sub>H</sub> - F846 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR11.UR	Access	Access
F846 8000 <sub>H</sub> - F847 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F848 0000 <sub>H</sub> - F848 0FFF <sub>H</sub>	4 Kbyte	CPU2.FSFR	Access	Access
F848 1000 <sub>H</sub> - F848 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F849 0000 <sub>H</sub> - F849 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR20.UR	Access	Access
F849 8000 <sub>H</sub> - F849 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84A 0000 <sub>H</sub> - F84A 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR21.UR	Access	Access
F84A 8000 <sub>H</sub> - F84B FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84C 0000 <sub>H</sub> - F84C 0FFF <sub>H</sub>	4 Kbyte	CPU3.FSFR	Access	Access
F84C 1000 <sub>H</sub> - F84C FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84D 0000 <sub>H</sub> - F84D 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR30.UR	Access	Access
F84D 8000 <sub>H</sub> - F84D FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F84E 0000 <sub>H</sub> - F84E 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMUR31.UR	Access	Access
F84E 8000 <sub>H</sub> - F857 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F858 0000 <sub>H</sub> - F858 0FFF <sub>H</sub>	4 Kbyte	CPUcs.FSFR	Access	Access
F858 1000 <sub>H</sub> - F858 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F859 0000 <sub>H</sub> - F859 7FFF <sub>H</sub>	32 Kbyte	NVMR.PMURcs.UR	Access	Access
F859 8000 <sub>H</sub> - F87F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F880 0000 <sub>H</sub> - F880 FFFF <sub>H</sub>	64 Kbyte	CPU0.SFR (incl. STM0 and OVL)	Access	Access
F881 0000 <sub>H</sub> - F881 FFFF <sub>H</sub>	64 Kbyte	CPU0.HR1.CSFR	Access	Access
F882 0000 <sub>H</sub> - F882 FFFF <sub>H</sub>	64 Kbyte	CPU0.HR2.CSFR	Access	Access
F883 0000 <sub>H</sub> - F883 FFFF <sub>H</sub>	64 Kbyte	CPU0.HR0.CSFR	Access	Access
F884 0000 <sub>H</sub> - F884 FFFF <sub>H</sub>	64 Kbyte	CPU1.SFR (incl. STM1 and OVL)	Access	Access
F885 0000 <sub>H</sub> - F885 FFFF <sub>H</sub>	64 Kbyte	CPU1.HR1.CSFR	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F886 0000 <sub>H</sub> - F886 FFFF <sub>H</sub>	64 Kbyte	CPU1.HR2.CSFR	Access	Access
F887 0000 <sub>H</sub> - F887 FFFF <sub>H</sub>	64 Kbyte	CPU1.HR0.CSFR	Access	Access
F888 0000 <sub>H</sub> - F888 FFFF <sub>H</sub>	64 Kbyte	CPU2.SFR (incl. STM2 and OVL)	Access	Access
F889 0000 <sub>H</sub> - F889 FFFF <sub>H</sub>	64 Kbyte	CPU2.HR1.CSFR	Access	Access
F88A 0000 <sub>H</sub> - F88A FFFF <sub>H</sub>	64 Kbyte	CPU2.HR2.CSFR	Access	Access
F88B 0000 <sub>H</sub> - F88B FFFF <sub>H</sub>	64 Kbyte	CPU2.HR0.CSFR	Access	Access
F88C 0000 <sub>H</sub> - F88C FFFF <sub>H</sub>	64 Kbyte	CPU3.SFR (incl. STM3 and OVL)	Access	Access
F88D 0000 <sub>H</sub> - F88D FFFF <sub>H</sub>	64 Kbyte	CPU3.HR1.CSFR	Access	Access
F88E 0000 <sub>H</sub> - F88E FFFF <sub>H</sub>	64 Kbyte	CPU3.HR2.CSFR	Access	Access
F88F 0000 <sub>H</sub> - F88F FFFF <sub>H</sub>	64 Kbyte	CPU3.HR0.CSFR	Access	Access
F890 0000 <sub>H</sub> - F897 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F898 0000 <sub>H</sub> - F898 FFFF <sub>H</sub>	64 Kbyte	CPUcs.SFR (incl. STMcs)	Access	Access
F899 0000 <sub>H</sub> - F899 FFFF <sub>H</sub>	64 Kbyte	CPUcs.HR1.CSFR	Access	Access
F89A 0000 <sub>H</sub> - F8CF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F8D0 0000 <sub>H</sub> - F8D4 FFFF <sub>H</sub>	320 Kbyte	CSS	Access	Access
F8D5 0000 <sub>H</sub> - F8EF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F8F0 0000 <sub>H</sub> - F8F0 FFFF <sub>H</sub>	64 Kbyte	SRI0.SFR	Access	Access
F8F1 0000 <sub>H</sub> - F8F1 FFFF <sub>H</sub>	64 Kbyte	SRI1.SFR	Access	Access
F8F2 0000 <sub>H</sub> - F8F2 FFFF <sub>H</sub>	64 Kbyte	SRI2.SFR	Access	Access
F8F3 0000 <sub>H</sub> - F8F3 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F8F4 0000 <sub>H</sub> - F8F4 FFFF <sub>H</sub>	64 Kbyte	SRI4.SFR	Access	Access
F8F5 0000 <sub>H</sub> - F902 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F903 0000 <sub>H</sub> - F903 7FFF <sub>H</sub>	32 Kbyte	DRE.SFR	Access	Access
F903 8000 <sub>H</sub> - F903 FFFF <sub>H</sub>	32 Kbyte	DRE.RAM	Access	Access
F904 0000 <sub>H</sub> - F904 FFFF <sub>H</sub>	64 Kbyte	xSPI.SFR	Access	Access
F905 0000 <sub>H</sub> - F93F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F940 0000 <sub>H</sub> - F943 FFFF <sub>H</sub>	256 Kbyte	LETH0	Access	Access
F944 0000 <sub>H</sub> - F947 FFFF <sub>H</sub>	256 Kbyte	LETH1	Access	Access
F948 0000 <sub>H</sub> - F980 FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F981 0000 <sub>H</sub> - F981 FFFF <sub>H</sub>	64 Kbyte	PPU.STUDMI	Access	Access
F982 0000 <sub>H</sub> - F982 FFFF <sub>H</sub>	64 Kbyte	PPU.DEBUG	Access	Access
F983 0000 <sub>H</sub> - F983 FFFF <sub>H</sub>	64 Kbyte	PPU.SAFETY	Access	Access

(table continues...)

### 3 System address map

**Table 26** (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F984 0000 <sub>H</sub> - F984 FFFF <sub>H</sub>	64 Kbyte	PPU.AP	Access	Access
F985 0000 <sub>H</sub> - F985 FFFF <sub>H</sub>	64 Kbyte	PPU.CSM.AP	Access	Access
F986 0000 <sub>H</sub> - F986 FFFF <sub>H</sub>	64 Kbyte	PPU.VMEM.AP	Access	Access
F987 0000 <sub>H</sub> - F98F FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
F990 0000 <sub>H</sub> - F990 FFFF <sub>H</sub>	64 Kbyte	LLI.SFR	Access	Access
F991 0000 <sub>H</sub> - F9FF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE
FA00 0000 <sub>H</sub> - FA01 FFFF <sub>H</sub>	128 Kbyte	MCDS4P (incl. 32 Kbyte TBUF)	Access	Access
FA02 0000 <sub>H</sub> - FA0F FFFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FA10 0000 <sub>H</sub> - FA10 1FFF <sub>H</sub>	8 Kbyte	TRIF	Access	Access
FA10 2000 <sub>H</sub> - FA17 FFFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FA18 0000 <sub>H</sub> - FA18 03FF <sub>H</sub>	1 Kbyte	CBS	Access	Access
FA18 0400 <sub>H</sub> - FAFF FBFF <sub>H</sub>	–	Reserved	TPBBE	TPBBE
FAFF FC00 <sub>H</sub> - FAFF FFFF <sub>H</sub>	1 Kbyte	TBCU	Access	Access
FB00 0000 <sub>H</sub> - FB00 FFFF <sub>H</sub>	64 Kbyte	LMU0.SFR	Access	Access
FB01 0000 <sub>H</sub> - FFFF FFFF <sub>H</sub>	–	Reserved	SRIBE	SRIBE

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## 3 System address map

### 3.3 System address map revision history

Initial release of the chapter.