

3 System address map

3.2 Address map

The address maps define which address ranges are valid or invalid for the respective segment. The access type column describes the error status location if the access is not permitted.

For the detailed address space of a specific functional block (module), please refer to the specific user manual chapter describing that block.

3.2.1 Segment 0

Table 7 Address map of segment 0

Address range	Size	Description	Access type	
			Read	Write
0000 0000 _H - 0FFF FFFF _H	-	Reserved	SRIBE ¹⁾	SRIBE ¹⁾

¹⁾ Any CPU load or store operation accessing 0000 0000_H will trap

3.2.2 Segment 1

Table 8 Address map of segment 1

Address range	Size	Description	Access type	
			Read	Write
1000 0000 _H - 1001 BFFF _H	112 Kbyte	CPUcs.DSPR	Access	Access
1001 C000 _H - 1001 FFFF _H	16 Kbyte	CPUcs.DSPR (extension) or CPUcs.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1002 0000 _H - 100F FFFF _H	-	Reserved	SRIBE	SRIBE
1010 0000 _H - 1010 FFFF _H	64 Kbyte	CPUcs.PSPR	Access	Access
1011 0000 _H - 1011 7FFF _H	32 Kbyte	CPUcs.PSPR (extension) or CPUcs.PCACH (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1011 8000 _H - 1011 FFFF _H	-	Reserved	SRIBE	SRIBE
1012 0000 _H - 1012 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1013 0000 _H - 1013 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1013 8000 _H - 1013 FFFF _H	-	Reserved	SRIBE	SRIBE
1014 0000 _H - 1014 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1015 0000 _H - 1015 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1015 8000 _H - 1015 FFFF _H	-	Reserved	SRIBE	SRIBE
1016 0000 _H - 1016 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1017 0000 _H - 1017 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1017 8000 _H - 1017 FFFF _H	-	Reserved	SRIBE	SRIBE
1018 0000 _H - 1018 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1019 0000 _H - 1019 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access

(table continues...)

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Table 8 (continued) Address map of segment 1

Address range	Size	Description	Access type	
			Read	Write
1019 8000 _H - 1019 FFFF _H	-	Reserved	SRIBE	SRIBE
101A 0000 _H - 101A FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101B 0000 _H - 101B 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101B 8000 _H - 101B FFFF _H	-	Reserved	SRIBE	SRIBE
101C 0000 _H - 101C FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101D 0000 _H - 101D 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101D 8000 _H - 101D FFFF _H	-	Reserved	SRIBE	SRIBE
101E 0000 _H - 101E FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101F 0000 _H - 101F 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101F 8000 _H - 101F FFFF _H	-	Reserved	SRIBE	SRIBE
1020 0000 _H - 1020 17FF _H	-	CPUcs.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1020 1800 _H - 102F FFFF _H	-	Reserved	SRIBE	SRIBE
1030 0000 _H - 1030 2FFF _H	-	CPUcs.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1030 3000 _H - 1FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.3 Segment 2

Table 9 Address map of segment 2

Address range	Size	Description	Access type	
			Read	Write
2000 0000 _H - 2FFF FFFF _H	-	Reserved	SRIBE	SRIBE

3.2.4 Segment 3

Table 10 Address map of segment 3

Address range	Size	Description	Access type	
			Read	Write
3000 0000 _H - 3FFF FFFF _H	-	Reserved	SRIBE	SRIBE

3.2.5 Segment 4

Table 11 Address map of segment 4

Address range	Size	Description	Access type	
			Read	Write
4000 0000 _H - 4FFF FFFF _H	-	Reserved	SRIBE	SRIBE

3.2.6 Segment 5

Table 12 Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5000 0000 _H - 5FFF FFFF _H	-	Reserved	SRIBE	SRIBE

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3.2.7 Segment 6

Table 13 Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6000 0000 _H - 6003 BFFF _H	240 Kbyte	CPU1.DSPR	Access	Access
6003 C000 _H - 6003 FFFF _H	16 Kbyte	CPU1.DSPR (extension) or CPU1.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6004 0000 _H - 600F FFFF _H	-	Reserved	SRIBE	SRIBE
6010 0000 _H - 6010 7FFF _H	32 Kbyte	CPU1.PSPR	Access	Access
6010 8000 _H - 6010 FFFF _H	32 Kbyte	CPU1.PSPR (extension) or CPU1.PCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6011 0000 _H - 6011 FFFF _H	-	Reserved	SRIBE	SRIBE
6012 0000 _H - 6012 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
6012 8000 _H - 6012 FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6013 0000 _H - 6013 FFFF _H	-	Reserved	SRIBE	SRIBE
6014 0000 _H - 6014 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
6014 8000 _H - 6014 FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6015 0000 _H - 6015 FFFF _H	-	Reserved	SRIBE	SRIBE
6016 0000 _H - 6016 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
6016 8000 _H - 6016 FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6017 0000 _H - 6017 FFFF _H	-	Reserved	SRIBE	SRIBE
6018 0000 _H - 6018 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
6018 8000 _H - 6018 FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6019 0000 _H - 6019 FFFF _H	-	Reserved	SRIBE	SRIBE
601A 0000 _H - 601A 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
601A 8000 _H - 601A FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601B 0000 _H - 601B FFFF _H	-	Reserved	SRIBE	SRIBE
601C 0000 _H - 601C 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
601C 8000 _H - 601C FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601D 0000 _H - 601D FFFF _H	-	Reserved	SRIBE	SRIBE
601E 0000 _H - 601E 7FFF _H	32 Kbyte	Alias for CPU1.PSPR	Access	Access
601E 8000 _H - 601E FFFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601F 0000 _H - 601F FFFF _H	-	Reserved	SRIBE	SRIBE
6020 0000 _H - 6020 17FF _H	-	CPU1.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6020 1800 _H - 602F FFFF _H	-	Reserved	SRIBE	SRIBE

(table continues...)

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Table 13 (continued) Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6030 0000 _H - 6030 2FFF _H	-	CPU1.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6030 3000 _H - 6FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.8 Segment 7

Table 14 Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7000 0000 _H - 7003 BFFF _H	240 Kbyte	CPU0.DSPR	Access	Access
7003 C000 _H - 7003 FFFF _H	16 Kbyte	CPU0.DSPR (extension) or CPU0.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7004 0000 _H - 700F FFFF _H	-	Reserved	SRIBE	SRIBE
7010 0000 _H - 7010 7FFF _H	32 Kbyte	CPU0.PSPR	Access	Access
7010 8000 _H - 7010 FFFF _H	32 Kbyte	CPU0.PSPR (extension) or CPU0.PCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7011 0000 _H - 7011 FFFF _H	-	Reserved	SRIBE	SRIBE
7012 0000 _H - 7012 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
7012 8000 _H - 7012 FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7013 0000 _H - 7013 FFFF _H	-	Reserved	SRIBE	SRIBE
7014 0000 _H - 7014 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
7014 8000 _H - 7014 FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7015 0000 _H - 7015 FFFF _H	-	Reserved	SRIBE	SRIBE
7016 0000 _H - 7016 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
7016 8000 _H - 7016 FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7017 0000 _H - 7017 FFFF _H	-	Reserved	SRIBE	SRIBE
7018 0000 _H - 7018 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
7018 8000 _H - 7018 FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7019 0000 _H - 7019 FFFF _H	-	Reserved	SRIBE	SRIBE
701A 0000 _H - 701A 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
701A 8000 _H - 701A FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701B 0000 _H - 701B FFFF _H	-	Reserved	SRIBE	SRIBE
701C 0000 _H - 701C 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
701C 8000 _H - 701C FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701D 0000 _H - 701D FFFF _H	-	Reserved	SRIBE	SRIBE
701E 0000 _H - 701E 7FFF _H	32 Kbyte	Alias for CPU0.PSPR	Access	Access
701E 8000 _H - 701E FFFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701F 0000 _H - 701F FFFF _H	-	Reserved	SRIBE	SRIBE
7020 0000 _H - 7020 17FF _H	-	CPU0.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7020 1800 _H - 702F FFFF _H	-	Reserved	SRIBE	SRIBE

(table continues...)

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Table 14 (continued) Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7030 0000 _H - 7030 2FFF _H	-	CPU0.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7030 3000 _H - 7FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.9 Segments 8 and 10

3.2.9.1 Segment 8

Table 15 Address map of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8020 0000 _H - 803F FFFF _H	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8040 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.9.2 Segment 10

Table 16 Address map of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A020 0000 _H - A03F FFFF _H	2 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A040 0000 _H - ADFE FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE00 FFFF _H	64 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE01 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE40 7FFF _H	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE80 FFFF _H	64 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE81 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 7FFF _H	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.10 Segments 9 and 11

3.2.10.1 Segment 9

Table 17 Address map of segment 9

Address range	Size	Description	Access type	
			Read	Write
9000 0000 _H - 9000 FFFF _H	64 Kbyte	CPU0.DLMU	Access	Access
9001 0000 _H - 9001 FFFF _H	64 Kbyte	CPU1.DLMU	Access	Access
9002 0000 _H - 90FF FFFF _H	-	Reserved	SRIBE	SRIBE
9100 0000 _H - 910F FFFF _H	1 Mbyte	RMEM0	Access	Access
9110 0000 _H - 911F FFFF _H	1 Mbyte	RMEM1	Access	Access
9120 0000 _H - 912F FFFF _H	1 Mbyte	RMEM2	Access	Access
9130 0000 _H - 913F FFFF _H	1 Mbyte	RMEM3	Access	Access
9140 0000 _H - 914F FFFF _H	1 Mbyte	RMEM4	Access	Access
9150 0000 _H - 915F FFFF _H	1 Mbyte	RMEM5	Access	Access
9160 0000 _H - 916F FFFF _H	1 Mbyte	RMEM6	Access	Access
9170 0000 _H - 917F FFFF _H	1 Mbyte	RMEM7	Access	Access
9180 0000 _H - 918F FFFF _H	1 Mbyte	RMEM8	Access	Access
9190 0000 _H - 919F FFFF _H	1 Mbyte	RMEM9	Access	Access
91A0 0000 _H - 9207 FFFF _H	-	Reserved	SRIBE	SRIBE
9208 0000 _H - 920F FFFF _H	512 Kbyte	PPU.CSM	Access	Access
9210 0000 _H - 9FFF FFFF _H	-	Reserved	SRIBE	SRIBE

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3.2.10.2 Segment 11

Table 18 Address map of segment 11

Address range	Size	Description	Access type	
			Read	Write
B000 0000 _H - B000 FFFF _H	64 Kbyte	CPU0.DLMU	Access	Access
B001 0000 _H - B001 FFFF _H	64 Kbyte	CPU1.DLMU	Access	Access
B002 0000 _H - B0FF FFFF _H	-	Reserved	SRIBE	SRIBE
B100 0000 _H - B10F FFFF _H	1 Mbyte	RMEM0	Access	Access
B110 0000 _H - B11F FFFF _H	1 Mbyte	RMEM1	Access	Access
B120 0000 _H - B12F FFFF _H	1 Mbyte	RMEM2	Access	Access
B130 0000 _H - B13F FFFF _H	1 Mbyte	RMEM3	Access	Access
B140 0000 _H - B14F FFFF _H	1 Mbyte	RMEM4	Access	Access
B150 0000 _H - B15F FFFF _H	1 Mbyte	RMEM5	Access	Access
B160 0000 _H - B16F FFFF _H	1 Mbyte	RMEM6	Access	Access
B170 0000 _H - B17F FFFF _H	1 Mbyte	RMEM7	Access	Access
B180 0000 _H - B18F FFFF _H	1 Mbyte	RMEM8	Access	Access
B190 0000 _H - B19F FFFF _H	1 Mbyte	RMEM9	Access	Access
B1A0 0000 _H - B203 FFFF _H	-	Reserved	SRIBE	SRIBE
B204 0000 _H - B205 FFFF _H	128 Kbyte	PPU.VMEM	Access	Access
B206 0000 _H - B207 FFFF _H	-	Reserved	SRIBE	SRIBE
B208 0000 _H - B20F FFFF _H	512 Kbyte	PPU.CSM	Access	Access
B210 0000 _H - BFFF FFFF _H	-	Reserved	SRIBE	SRIBE

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3.2.11 Segment 12

Table 19 Address map of segment 12

Address range	Size	Description	Access type	
			Read	Write
C000 0000 _H - CFFF FFFF _H	-	Reserved ^{1) 2)}	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

2) See the PPU chapter for details

3.2.12 Segment 13

Table 20 Address map of segment 13

Address range	Size	Description	Access type	
			Read	Write
D000 0000 _H - DFFF FFFF _H	-	Reserved ¹⁾	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

3.2.13 Segment 14

Table 21 Address map of segment 14

Address range	Size	Unit	Access type	
			Read	Write
E000 0000 _H - E97F FFFF _H	-	Reserved	LLIBE	LLIBE
E980 0000 _H - E980 FFFF _H	64 Kbyte	PPU.SFR	Access	Access
E981 0000 _H - EBFF FFFF _H	-	Reserved	LLIBE	LLIBE
EC00 0000 _H - EFFF FFFF _H	64 Mbyte	PCIe1.DATA	Access	Access

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3.2.14 Segment 15

The address map of segment 15 includes the following module address ranges:

- Absolute Addressing Range
 - If a module is addressed in the first 16 Kbyte of segment 15, the CPU can access the module with absolute addressing mode
- Others
 - If a module is addressed above the first 16 Kbyte of segment 15, the CPU can access the module with base + offset

Table 22 Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F000 0000 _H - F000 07FF _H	2 Kbyte	WTU	Access	Access
F000 0800 _H - F000 0FFF _H	–	Reserved	SPBBE	SPBBE
F000 1000 _H - F000 11FF _H	512 Byte	QSPIO	Access	Access
F000 1200 _H - F000 13FF _H	512 Byte	QSPIO1	Access	Access
F000 1400 _H - F000 15FF _H	512 Byte	QSPIO2	Access	Access
F000 1600 _H - F000 17FF _H	512 Byte	QSPIO3	Access	Access
F000 1800 _H - F000 2DFF _H	–	Reserved	SPBBE	SPBBE
F000 2E00 _H - F000 2FFF _H	512 Byte	FCE0	Access	Access
F000 3000 _H - F000 FFFF _H	–	Reserved	SPBBE	SPBBE
F001 0000 _H - F001 3FFF _H	16 Kbyte	DMA0	Access	Access
F001 4000 _H - F001 FFFF _H	–	Reserved	SPBBE	SPBBE
F002 0000 _H - F002 01FF _H	512 Byte	GPT120	Access	Access
F002 0200 _H - F002 03FF _H	512 Byte	GPT121	Access	Access
F002 0400 _H - F002 05FF _H	512 Byte	GPT122	Access	Access
F002 0600 _H - F002 07FF _H	512 Byte	GPT123	Access	Access
F002 0800 _H - F002 09FF _H	512 Byte	GPT124	Access	Access
F002 0A00 _H - F002 0BFF _H	512 Byte	GPT125	Access	Access
F002 0C00 _H - F002 0DFF _H	512 Byte	GPT126	Access	Access
F002 0E00 _H - F002 0FFF _H	512 Byte	GPT127	Access	Access
F002 1000 _H - F002 11FF _H	512 Byte	GPT128	Access	Access
F002 1200 _H - F002 13FF _H	512 Byte	GPT129	Access	Access
F002 1400 _H - F002 15FF _H	512 Byte	GPT1210	Access	Access
F002 1600 _H - F002 17FF _H	512 Byte	GPT1211	Access	Access
F002 1800 _H - F002 1FFF _H	–	Reserved	SPBBE	SPBBE
F002 2000 _H - F002 21FF _H	512 Byte	GST	Access	Access
F002 2200 _H - F002 3FFF _H	–	Reserved	SPBBE	SPBBE

(table continues...)

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Table 22 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F002 4000 _H - F002 43FF _H	1 Kbyte	SCU	Access	Access
F002 4400 _H - F002 7FFF _H	-	Reserved	SPBBE	SPBBE
F002 8000 _H - F002 9FFF _H	8 Kbyte	SMU	Access	Access
F002 A000 _H - F002 FFFF _H	-	Reserved	SPBBE	SPBBE
F003 0000 _H - F003 1FFF _H	8 Kbyte	IR.SFR	Access	Access
F003 2000 _H - F003 5FFF _H	16 Kbyte	IR.SRC	Access	Access
F003 6000 _H - F003 9FFF _H	-	Reserved	SPBBE	SPBBE
F003 A000 _H - F003 A3FF _H	1 Kbyte	P00	Access	Access
F003 A400 _H - F003 A7FF _H	1 Kbyte	P01	Access	Access
F003 A800 _H - F003 ABFF _H	1 Kbyte	P02	Access	Access
F003 AC00 _H - F003 C7FF _H	-	Reserved	SPBBE	SPBBE
F003 C800 _H - F003 CBFF _H	1 Kbyte	P10	Access	Access
F003 CC00 _H - F003 D3FF _H	-	Reserved	SPBBE	SPBBE
F003 D400 _H - F003 D7FF _H	1 Kbyte	P13	Access	Access
F003 D800 _H - F003 DBFF _H	1 Kbyte	P14	Access	Access
F003 DC00 _H - F003 DFFF _H	1 Kbyte	P15	Access	Access
F003 E000 _H - F003 E3FF _H	1 Kbyte	P16	Access	Access
F003 E400 _H - F003 EFFF _H	-	Reserved	SPBBE	SPBBE
F003 F000 _H - F003 F3FF _H	1 Kbyte	P20	Access	Access
F003 F400 _H - F003 F7FF _H	1 Kbyte	P21	Access	Access
F003 F800 _H - F003 FBFF _H	1 Kbyte	P22	Access	Access
F003 FC00 _H - F003 FFFF _H	1 Kbyte	P23	Access	Access
F004 0000 _H - F004 1FFF _H	-	Reserved	SPBBE	SPBBE
F004 2000 _H - F004 23FF _H	1 Kbyte	P32	Access	Access
F004 2400 _H - F004 27FF _H	1 Kbyte	P33	Access	Access
F004 2800 _H - F004 2BFF _H	1 Kbyte	P34	Access	Access
F004 2C00 _H - F005 FFFF _H	-	Reserved	SPBBE	SPBBE
F006 0000 _H - F006 3FFF _H	16 Kbyte	SMM	Access	Access
F006 4000 _H - F006 7FFF _H	16 Kbyte	CCU	Access	Access
F006 8000 _H - F006 80FF _H	256 Byte	TRI	Access	Access
F006 8100 _H - F006 BFFF _H	-	Reserved	SPBBE	SPBBE
F006 C000 _H - F006 FFFF _H	16 Kbyte	VTMON	Access	Access

(table continues...)

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Table 22 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F007 0000 _H - F00B FFFF _H	–	Reserved	SPBBE	SPBBE
F00C 0000 _H - F00D FFFF _H	128 Kbyte	I2C0	Access	Access
F00E 0000 _H - F00F FFFF _H	128 Kbyte	I2C1	Access	Access
F010 0000 _H - F023 FFFF _H	–	Reserved	SPBBE	SPBBE
F024 0000 _H - F024 7FFF _H	32 Kbyte	SCR.XRAM	Access	Access
F024 8000 _H - F024 8FFF _H	4 Kbyte	SCR.PER.SFR	Access	Access
F024 9000 _H - F024 9FFF _H	4 Kbyte	PMS	Access	Access
F024 A000 _H - F029 FFFF _H	–	Reserved	SPBBE	SPBBE
F02A 0000 _H - F02A 7FFF _H	32 Kbyte	PCIe1.SFR	Access	Access
F02A 8000 _H - F02B FFFF _H	–	Reserved	SPBBE	SPBBE
F02C 0000 _H - F02C 01FF _H	512 Byte	ASCLIN0	Access	Access
F02C 0200 _H - F02C 03FF _H	512 Byte	ASCLIN1	Access	Access
F02C 0400 _H - F03F FFFF _H	–	Reserved	SPBBE	SPBBE
F040 0000 _H - F041 FFFF _H	128 Kbyte	VMT0	Access	Access
F042 0000 _H - F043 FFFF _H	128 Kbyte	VMT1	Access	Access
F044 0000 _H - F045 FFFF _H	–	Reserved	SPBBE	SPBBE
F046 0000 _H - F047 FFFF _H	128 Kbyte	VMT3	Access	Access
F048 0000 _H - F049 FFFF _H	128 Kbyte	VMT4	Access	Access
F04A 0000 _H - F04B FFFF _H	128 Kbyte	VMT5	Access	Access
F04C 0000 _H - F04D FFFF _H	128 Kbyte	VMT6	Access	Access
F04E 0000 _H - F04F FFFF _H	128 Kbyte	VMT7	Access	Access
F050 0000 _H - F0FF FFFF _H	–	Reserved	SPBBE	SPBBE
F100 0000 _H - F100 1FFF _H	8 Kbyte	ADC.SFR	Access	Access
F100 2000 _H - F101 F7FF _H	–	Reserved	SPBBE	SPBBE
F101 F800 _H - F101 FFFF _H	2 Kbyte	TMADC.SFR	Access	Access
F102 0000 _H - F102 07FF _H	2 Kbyte	TMADC0 (Read Write)	Access	Access
F102 0800 _H - F17B FFFF _H	–	Reserved	SPBBE	SPBBE
F17C 0000 _H - F17C 1FFF _H	8 Kbyte	CANXL0.SRAM	Access	Access
F17C 2000 _H - F17C FFFF _H	–	Reserved	SPBBE	SPBBE
F17D 0000 _H - F17F FFFF _H	192 Kbyte	CANXL0.SFR	Access	Access
F180 0000 _H - F1FF FBFF _H	–	Reserved	SPBBE	SPBBE
F1FF FC00 _H - F1FF FFFF _H	1 Kbyte	SBCU	Access	Access

(table continues...)

3 System address map

Table 22 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F200 0000 _H - F3FF FFFF _H	32 Mbyte	HSPHY	Access	Access
F400 0000 _H - F47F FFFF _H	–	Reserved	RCBBE	RCBBE
F480 0000 _H - F481 FFFF _H	128 Kbyte	SPU0.CFG	Access	Access
F482 0000 _H - F483 FFFF _H	–	Reserved	RCBBE	RCBBE
F484 0000 _H - F484 07FF _H	2 Kbyte	SPU0	Access	Access
F484 0800 _H - F48F FFFF _H	–	Reserved	RCBBE	RCBBE
F490 0000 _H - F490 3FFF _H	16 Kbyte	RDMA0	Access	Access
F490 4000 _H - F4BF FFFF _H	–	Reserved	RCBBE	RCBBE
F4C0 0000 _H - F4C0 07FF _H	2 Kbyte	RIF0.SFR	Access	Access
F4C0 0800 _H - F4C0 0FFF _H	2 Kbyte	RIF0.CSI2	Access	Access
F4C0 1000 _H - F4CF FFFF _H	–	Reserved	RCBBE	RCBBE
F4D0 0000 _H - F4D0 07FF _H	2 Kbyte	RIF1.SFR	Access	Access
F4D0 0800 _H - F4D0 0FFF _H	2 Kbyte	RIF1.CSI2	Access	Access
F4D0 1000 _H - F4DF FFFF _H	–	Reserved	RCBBE	RCBBE
F4E0 0000 _H - F4E0 07FF _H	2 Kbyte	RIF2.SFR	Access	Access
F4E0 0800 _H - F4E0 0FFF _H	2 Kbyte	RIF2.CSI2	Access	Access
F4E0 1000 _H - F5FF FBFF _H	–	Reserved	RCBBE	RCBBE
F5FF FC00 _H - F5FF FFFF _H	1 Kbyte	RBCU	Access	Access
F600 0000 _H - F600 01FF _H	512 Byte	CSCU	Access	Access
F600 0200 _H - F600 03FF _H	512 Byte	TRNG	Access	Access
F600 0400 _H - F600 FFFF _H	–	Reserved	CSPBBE	CSPBBE
F601 0000 _H - F601 7FFF _H	32 Kbyte	PKC	Access	Access
F601 8000 _H - F6FF FBFF _H	–	Reserved	CSPBBE	CSPBBE
F6FF FC00 _H - F6FF FFFF _H	1 Kbyte	CSBCU	Access	Access
F700 0000 _H - F7FF FFFF _H	–	Reserved	SRIBE	SRIBE
F800 0000 _H - F807 FFFF _H	–	Reserved	SRIBE	SRIBE
F808 0000 _H - F808 7FFF _H	32 Kbyte	NVMR.DMUR0.UR	Access	Access
F808 8000 _H - F80B FFFF _H	–	Reserved	SRIBE	SRIBE
F80C 0000 _H - F80C 7FFF _H	32 Kbyte	NVMR.DMUR1.UR	Access	Access
F80C 8000 _H - F83F FFFF _H	–	Reserved	SRIBE	SRIBE
F840 0000 _H - F840 0FFF _H	4 Kbyte	CPU0.FSFR	Access	Access
F840 1000 _H - F840 FFFF _H	–	Reserved	SRIBE	SRIBE

(table continues...)

3 System address map

Table 22 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F841 0000 _H - F841 7FFF _H	32 Kbyte	NVMR.PMUR00.UR	Access	Access
F841 8000 _H - F841 FFFF _H	–	Reserved	SRIBE	SRIBE
F842 0000 _H - F843 FFFF _H	–	Reserved	SRIBE	SRIBE
F844 0000 _H - F844 0FFF _H	4 Kbyte	CPU1.FSFR	Access	Access
F844 1000 _H - F844 FFFF _H	–	Reserved	SRIBE	SRIBE
F845 0000 _H - F845 7FFF _H	32 Kbyte	NVMR.PMUR10.UR	Access	Access
F845 8000 _H - F845 FFFF _H	–	Reserved	SRIBE	SRIBE
F846 0000 _H - F87F FFFF _H	–	Reserved	SRIBE	SRIBE
F880 0000 _H - F880 FFFF _H	64 Kbyte	CPU0.SFR (incl. STM0)	Access	Access
F881 0000 _H - F881 FFFF _H	64 Kbyte	CPU0.HR1.CSFR	Access	Access
F882 0000 _H - F883 FFFF _H	–	Reserved	SRIBE	SRIBE
F884 0000 _H - F884 FFFF _H	64 Kbyte	CPU1.SFR (incl. STM1)	Access	Access
F885 0000 _H - F885 FFFF _H	64 Kbyte	CPU1.HR1.CSFR	Access	Access
F886 0000 _H - F897 FFFF _H	–	Reserved	SRIBE	SRIBE
F898 0000 _H - F898 FFFF _H	64 Kbyte	CPUcs.SFR (incl. STMcs)	Access	Access
F899 0000 _H - F899 FFFF _H	64 Kbyte	CPUcs.HR1.CSFR	Access	Access
F89A 0000 _H - F8CF FFFF _H	–	Reserved	SRIBE	SRIBE
F8D0 0000 _H - F8D4 FFFF _H	320 Kbyte	CSS	Access	Access
F8D5 0000 _H - F8EF FFFF _H	–	Reserved	SRIBE	SRIBE
F8F0 0000 _H - F8F0 FFFF _H	64 Kbyte	SRI0.SFR	Access	Access
F8F1 0000 _H - F8F1 FFFF _H	64 Kbyte	SRI1.SFR	Access	Access
F8F2 0000 _H - F8F3 FFFF _H	–	Reserved	SRIBE	SRIBE
F8F4 0000 _H - F8F4 FFFF _H	64 Kbyte	SRI4.SFR	Access	Access
F8F5 0000 _H - F8F5 FFFF _H	–	Reserved	SRIBE	SRIBE
F8F6 0000 _H - F8F6 FFFF _H	64 Kbyte	SRI6.SFR	Access	Access
F8F7 0000 _H - F8F7 FFFF _H	64 Kbyte	SRI7.SFR	Access	Access
F8F8 0000 _H - F943 FFFF _H	–	Reserved	SRIBE	SRIBE
F944 0000 _H - F947 FFFF _H	256 Kbyte	LETH1	Access	Access
F948 0000 _H - F980 FFFF _H	–	Reserved	SRIBE	SRIBE
F981 0000 _H - F981 FFFF _H	64 Kbyte	PPU.STUDMI	Access	Access
F982 0000 _H - F982 FFFF _H	64 Kbyte	PPU.DEBUG	Access	Access
F983 0000 _H - F983 FFFF _H	64 Kbyte	PPU.SAFETY	Access	Access

(table continues...)

3 System address map

Table 22 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F984 0000 _H - F984 FFFF _H	64 Kbyte	PPU.AP	Access	Access
F985 0000 _H - F985 FFFF _H	64 Kbyte	PPU.CSM.AP	Access	Access
F986 0000 _H - F986 FFFF _H	64 Kbyte	PPU.VMEM.AP	Access	Access
F987 0000 _H - F98F FFFF _H	–	Reserved	SRIBE	SRIBE
F990 0000 _H - F990 FFFF _H	64 Kbyte	LLI.SFR	Access	Access
F991 0000 _H - F9FF FFFF _H	–	Reserved	SRIBE	SRIBE
FA00 0000 _H - FA03 FFFF _H	–	Reserved	TPBBE	TPBBE
FA04 0000 _H - FA05 FFFF _H	128 Kbyte	MCDS2P (incl. 16 Kbyte TBUF)	Access	Access
FA06 0000 _H - FA0F FFFF _H	–	Reserved	TPBBE	TPBBE
FA10 0000 _H - FA10 1FFF _H	8 Kbyte	TRIF	Access	Access
FA10 2000 _H - FA17 FFFF _H	–	Reserved	TPBBE	TPBBE
FA18 0000 _H - FA18 03FF _H	1 Kbyte	CBS	Access	Access
FA18 0400 _H - FAFF FBFF _H	–	Reserved	TPBBE	TPBBE
FAFF FC00 _H - FAFF FFFF _H	1 Kbyte	TBCU	Access	Access
FB00 0000 _H - FB00 FFFF _H	64 Kbyte	RMEM0.SFR	Access	Access
FB01 0000 _H - FB01 FFFF _H	64 Kbyte	RMEM1.SFR	Access	Access
FB02 0000 _H - FB02 FFFF _H	64 Kbyte	RMEM2.SFR	Access	Access
FB03 0000 _H - FB03 FFFF _H	64 Kbyte	RMEM3.SFR	Access	Access
FB04 0000 _H - FB04 FFFF _H	64 Kbyte	RMEM4.SFR	Access	Access
FB05 0000 _H - FB05 FFFF _H	64 Kbyte	RMEM5.SFR	Access	Access
FB06 0000 _H - FB06 FFFF _H	64 Kbyte	RMEM6.SFR	Access	Access
FB07 0000 _H - FB07 FFFF _H	64 Kbyte	RMEM7.SFR	Access	Access
FB08 0000 _H - FB08 FFFF _H	64 Kbyte	RMEM8.SFR	Access	Access
FB09 0000 _H - FB09 FFFF _H	64 Kbyte	RMEM9.SFR	Access	Access
FB0A 0000 _H - FFFF FFFF _H	–	Reserved	SRIBE	SRIBE