

3 System address map

3.2 Address map

The address maps define which address ranges are valid or invalid for the respective segment. The access type column describes the error status location if the access is not permitted.

For the detailed address space of a specific functional block (module), please refer to the specific user manual chapter describing that block.

3.2.1 Segment 0

Table 7 Address map of segment 0

Address range	Size	Description	Access type	
			Read	Write
0000 0000 _H - 0FFF FFFF _H	-	Reserved	SRIBE ¹⁾	SRIBE ¹⁾

¹⁾ Any CPU load or store operation accessing 0000 0000_H will trap

3.2.2 Segment 1

Table 8 Address map of segment 1

Address range	Size	Description	Access type	
			Read	Write
1000 0000 _H - 1001 BFFF _H	112 Kbyte	CPUcs.DSPR	Access	Access
1001 C000 _H - 1001 FFFF _H	16 Kbyte	CPUcs.DSPR (extension) or CPUcs.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1002 0000 _H - 100F FFFF _H	-	Reserved	SRIBE	SRIBE
1010 0000 _H - 1010 FFFF _H	64 Kbyte	CPUcs.PSPR	Access	Access
1011 0000 _H - 1011 7FFF _H	32 Kbyte	CPUcs.PSPR (extension) or CPUcs.PCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1011 8000 _H - 1011 FFFF _H	-	Reserved	SRIBE	SRIBE
1012 0000 _H - 1012 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1013 0000 _H - 1013 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1013 8000 _H - 1013 FFFF _H	-	Reserved	SRIBE	SRIBE
1014 0000 _H - 1014 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1015 0000 _H - 1015 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1015 8000 _H - 1015 FFFF _H	-	Reserved	SRIBE	SRIBE
1016 0000 _H - 1016 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1017 0000 _H - 1017 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
1017 8000 _H - 1017 FFFF _H	-	Reserved	SRIBE	SRIBE
1018 0000 _H - 1018 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
1019 0000 _H - 1019 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access

(table continues...)

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Table 8 (continued) Address map of segment 1

Address range	Size	Description	Access type	
			Read	Write
1019 8000 _H - 1019 FFFF _H	-	Reserved	SRIBE	SRIBE
101A 0000 _H - 101A FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101B 0000 _H - 101B 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101B 8000 _H - 101B FFFF _H	-	Reserved	SRIBE	SRIBE
101C 0000 _H - 101C FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101D 0000 _H - 101D 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101D 8000 _H - 101D FFFF _H	-	Reserved	SRIBE	SRIBE
101E 0000 _H - 101E FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
101F 0000 _H - 101F 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
101F 8000 _H - 101F FFFF _H	-	Reserved	SRIBE	SRIBE
1020 0000 _H - 1020 17FF _H	-	CPUcs.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1020 1800 _H - 102F FFFF _H	-	Reserved	SRIBE	SRIBE
1030 0000 _H - 1030 2FFF _H	-	CPUcs.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
1030 3000 _H - 1FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.3 Segment 2

Table 9 Address map of segment 2

Address range	Size	Description	Access type	
			Read	Write
2000 0000 _H - 2FFF FFFF _H	-	Reserved	SRIBE	SRIBE

3.2.4 Segment 3

Table 10 Address map of segment 3

Address range	Size	Description	Access type	
			Read	Write
3000 0000 _H - 3FFF FFFF _H	-	Reserved	SRIBE	SRIBE

3.2.5 Segment 4

Table 11 Address map of segment 4

Address range	Size	Description	Access type	
			Read	Write
4000 0000 _H - 4FFF FFFF _H	-	Reserved	SRIBE	SRIBE

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3.2.6 Segment 5

Table 12 Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5000 0000 _H - 5001 BFFF _H	112 Kbyte	CPU2.DSPR	Access	Access
5001 C000 _H - 5001 FFFF _H	16 Kbyte	CPU2.DSPR (extension) or CPU2.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
5002 0000 _H - 500F FFFF _H	-	Reserved	SRIBE	SRIBE
5010 0000 _H - 5010 FFFF _H	64 Kbyte	CPU2.PSPR	Access	Access
5011 0000 _H - 5011 7FFF _H	32 Kbyte	CPU2.PSPR (extension) or CPU2.PCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
5011 8000 _H - 5011 FFFF _H	-	Reserved	SRIBE	SRIBE
5012 0000 _H - 5012 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5013 0000 _H - 5013 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5013 8000 _H - 5013 FFFF _H	-	Reserved	SRIBE	SRIBE
5014 0000 _H - 5014 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5015 0000 _H - 5015 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5015 8000 _H - 5015 FFFF _H	-	Reserved	SRIBE	SRIBE
5016 0000 _H - 5016 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5017 0000 _H - 5017 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5017 8000 _H - 5017 FFFF _H	-	Reserved	SRIBE	SRIBE
5018 0000 _H - 5018 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
5019 0000 _H - 5019 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
5019 8000 _H - 5019 FFFF _H	-	Reserved	SRIBE	SRIBE
501A 0000 _H - 501A FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501B 0000 _H - 501B 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501B 8000 _H - 501B FFFF _H	-	Reserved	SRIBE	SRIBE
501C 0000 _H - 501C FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501D 0000 _H - 501D 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501D 8000 _H - 501D FFFF _H	-	Reserved	SRIBE	SRIBE
501E 0000 _H - 501E FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
501F 0000 _H - 501F 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
501F 8000 _H - 501F FFFF _H	-	Reserved	SRIBE	SRIBE
5020 0000 _H - 5020 17FF _H	-	CPU2.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
5020 1800 _H - 502F FFFF _H	-	Reserved	SRIBE	SRIBE

(table continues...)

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Table 12 (continued) Address map of segment 5

Address range	Size	Description	Access type	
			Read	Write
5030 0000 _H - 5030 2FFF _H	-	CPU2.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
5030 3000 _H - 5FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.7 Segment 6

Table 13 Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6000 0000 _H - 6003 BFFF _H	240 Kbyte	CPU1.DSPR	Access	Access
6003 C000 _H - 6003 FFFF _H	16 Kbyte	CPU1.DSPR (extension) or CPU1.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6004 0000 _H - 600F FFFF _H	-	Reserved	SRIBE	SRIBE
6010 0000 _H - 6010 FFFF _H	64 Kbyte	CPU1.PSPR	Access	Access
6011 0000 _H - 6011 7FFF _H	32 Kbyte	CPU1.PSPR (extension) or CPU1.PCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6011 8000 _H - 6011 FFFF _H	-	Reserved	SRIBE	SRIBE
6012 0000 _H - 6012 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6013 0000 _H - 6013 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6013 8000 _H - 6013 FFFF _H	-	Reserved	SRIBE	SRIBE
6014 0000 _H - 6014 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6015 0000 _H - 6015 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6015 8000 _H - 6015 FFFF _H	-	Reserved	SRIBE	SRIBE
6016 0000 _H - 6016 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6017 0000 _H - 6017 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6017 8000 _H - 6017 FFFF _H	-	Reserved	SRIBE	SRIBE
6018 0000 _H - 6018 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
6019 0000 _H - 6019 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
6019 8000 _H - 6019 FFFF _H	-	Reserved	SRIBE	SRIBE
601A 0000 _H - 601A FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601B 0000 _H - 601B 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601B 8000 _H - 601B FFFF _H	-	Reserved	SRIBE	SRIBE
601C 0000 _H - 601C FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601D 0000 _H - 601D 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601D 8000 _H - 601D FFFF _H	-	Reserved	SRIBE	SRIBE
601E 0000 _H - 601E FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
601F 0000 _H - 601F 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
601F 8000 _H - 601F FFFF _H	-	Reserved	SRIBE	SRIBE
6020 0000 _H - 6020 17FF _H	-	CPU1.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6020 1800 _H - 602F FFFF _H	-	Reserved	SRIBE	SRIBE

(table continues...)

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Table 13 (continued) Address map of segment 6

Address range	Size	Description	Access type	
			Read	Write
6030 0000 _H - 6030 2FFF _H	-	CPU1.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
6030 3000 _H - 6FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.8 Segment 7

Table 14 Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7000 0000 _H - 7003 BFFF _H	240 Kbyte	CPU0.DSPR	Access	Access
7003 C000 _H - 7003 FFFF _H	16 Kbyte	CPU0.DSPR (extension) or CPU0.DCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7004 0000 _H - 700F FFFF _H	-	Reserved	SRIBE	SRIBE
7010 0000 _H - 7010 FFFF _H	64 Kbyte	CPU0.PSPR	Access	Access
7011 0000 _H - 7011 7FFF _H	32 Kbyte	CPU0.PSPR (extension) or CPU0.PCACHE (RAM)	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7011 8000 _H - 7011 FFFF _H	-	Reserved	SRIBE	SRIBE
7012 0000 _H - 7012 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7013 0000 _H - 7013 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7013 8000 _H - 7013 FFFF _H	-	Reserved	SRIBE	SRIBE
7014 0000 _H - 7014 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7015 0000 _H - 7015 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7015 8000 _H - 7015 FFFF _H	-	Reserved	SRIBE	SRIBE
7016 0000 _H - 7016 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7017 0000 _H - 7017 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7017 8000 _H - 7017 FFFF _H	-	Reserved	SRIBE	SRIBE
7018 0000 _H - 7018 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
7019 0000 _H - 7019 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
7019 8000 _H - 7019 FFFF _H	-	Reserved	SRIBE	SRIBE
701A 0000 _H - 701A FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701B 0000 _H - 701B 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701B 8000 _H - 701B FFFF _H	-	Reserved	SRIBE	SRIBE
701C 0000 _H - 701C FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701D 0000 _H - 701D 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701D 8000 _H - 701D FFFF _H	-	Reserved	SRIBE	SRIBE
701E 0000 _H - 701E FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
701F 0000 _H - 701F 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
701F 8000 _H - 701F FFFF _H	-	Reserved	SRIBE	SRIBE
7020 0000 _H - 7020 17FF _H	-	CPU0.DTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7020 1800 _H - 702F FFFF _H	-	Reserved	SRIBE	SRIBE

(table continues...)

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Table 14 (continued) Address map of segment 7

Address range	Size	Description	Access type	
			Read	Write
7030 0000 _H - 7030 2FFF _H	-	CPU0.PTAG	Access ¹⁾ / SRIBE	Access ¹⁾ / SRIBE
7030 3000 _H - 7FFF FFFF _H	-	Reserved	SRIBE	SRIBE

1) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

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3.2.9 Segments 8 and 10

The read address of the program flash and erase counters varies for the following modes:

- Linear address map
- SOTA address map A
- SOTA address map B

3.2.9.1 Segment 8 linear address map

Table 15 Linear address map of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8020 0000 _H - 803F FFFF _H	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
8040 0000 _H - 804F FFFF _H	1 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8050 0000 _H - 805F FFFF _H	1 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
8060 0000 _H - 806F FFFF _H	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
8070 0000 _H - 807F FFFF _H	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
8080 0000 _H - 83FF FFFF _H	-	Reserved	SRIBE	SRIBE
8400 0000 _H - 8407 FFFF _H	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
8408 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.9.2 Segment 10 linear address map

Table 16 Linear address map of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A020 0000 _H - A03F FFFF _H	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
A040 0000 _H - A04F FFFF _H	1 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A050 0000 _H - A05F FFFF _H	1 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
A060 0000 _H - A06F FFFF _H	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
A070 0000 _H - A07F FFFF _H	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
A080 0000 _H - A3FF FFFF _H	-	Reserved	SRIBE	SRIBE
A400 0000 _H - A407 FFFF _H	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
A408 0000 _H - ADFE FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE03 FFFF _H	256 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE04 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE40 7FFF _H	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE81 FFFF _H	128 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE82 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 7FFF _H	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.9.3 Segment 8 SOTA address map A

Table 17 SOTA address map A of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8020 0000 _H - 802F FFFF _H	1 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8030 0000 _H - 803F FFFF _H	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
8040 0000 _H - 81FF FFFF _H	-	Reserved	SRIBE	SRIBE
8200 0000 _H - 821F FFFF _H	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
8220 0000 _H - 822F FFFF _H	1 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
8230 0000 _H - 823F FFFF _H	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
8240 0000 _H - 83FF FFFF _H	-	Reserved	SRIBE	SRIBE
8400 0000 _H - 8407 FFFF _H	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
8408 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.9.4 Segment 10 SOTA address map A

Table 18 SOTA address map A of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A020 0000 _H - A02F FFFF _H	1 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A030 0000 _H - A03F FFFF _H	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
A040 0000 _H - A1FF FFFF _H	-	Reserved	SRIBE	SRIBE
A200 0000 _H - A21F FFFF _H	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
A220 0000 _H - A22F FFFF _H	1 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
A230 0000 _H - A23F FFFF _H	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
A240 0000 _H - A3FF FFFF _H	-	Reserved	SRIBE	SRIBE
A400 0000 _H - A407 FFFF _H	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
A408 0000 _H - ADFF FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE03 FFFF _H	256 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE04 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE40 7FFF _H	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE81 FFFF _H	128 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE82 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 7FFF _H	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.9.5 Segment 8 SOTA address map B

Table 19 SOTA address map B of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
8020 0000 _H - 802F FFFF _H	1 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
8030 0000 _H - 803F FFFF _H	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
8040 0000 _H - 81FF FFFF _H	-	Reserved	SRIBE	SRIBE
8200 0000 _H - 821F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
8220 0000 _H - 822F FFFF _H	1 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
8230 0000 _H - 823F FFFF _H	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
8240 0000 _H - 83FF FFFF _H	-	Reserved	SRIBE	SRIBE
8400 0000 _H - 8407 FFFF _H	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
8408 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

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3.2.9.6 Segment 10 SOTA address map B

Table 20 SOTA address map B of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVMR.PRRAM01 (Program RRAM)	Access	SRIBE
A020 0000 _H - A02F FFFF _H	1 Mbyte	NVMR.PRRAM11 (Program RRAM)	Access	SRIBE
A030 0000 _H - A03F FFFF _H	1 Mbyte	NVMR.PRRAM21 (Program RRAM)	Access	SRIBE
A040 0000 _H - A1FF FFFF _H	-	Reserved	SRIBE	SRIBE
A200 0000 _H - A21F FFFF _H	2 Mbyte	NVMR.PRRAM00 (Program RRAM)	Access	SRIBE
A220 0000 _H - A22F FFFF _H	1 Mbyte	NVMR.PRRAM10 (Program RRAM)	Access	SRIBE
A230 0000 _H - A23F FFFF _H	1 Mbyte	NVMR.PRRAM20 (Program RRAM)	Access	SRIBE
A240 0000 _H - A3FF FFFF _H	-	Reserved	SRIBE	SRIBE
A400 0000 _H - A407 FFFF _H	512 Kbyte	NVMR.PRRAMcs (Program RRAM)	Access	SRIBE
A408 0000 _H - ADFF FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE03 FFFF _H	256 Kbyte	NVMR.EEPROM0	Access	SRIBE
AE04 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE40 7FFF _H	32 Kbyte	NVMR.UCB0	Access	SRIBE
AE40 8000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE81 FFFF _H	128 Kbyte	NVMR.EEPROM1	Access	SRIBE
AE82 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 7FFF _H	32 Kbyte	NVMR.UCB1	Access	SRIBE
AEC0 8000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVMR.BROM	Access	SRIBE

3 System address map

3.2.10 Segments 9 and 11

3.2.10.1 Segment 9

Table 21 Address map of segment 9

Address range	Size	Description	Access type	
			Read	Write
9000 0000 _H - 9003 FFFF _H	256 Kbyte	CPU0.DLMU	Access	Access
9004 0000 _H - 9005 FFFF _H	128 Kbyte	CPU1.DLMU	Access	Access
9006 0000 _H - 9007 FFFF _H	128 Kbyte	CPU2.DLMU	Access	Access
9008 0000 _H - 97FF FFFF _H	-	Reserved	SRIBE	SRIBE
9800 0000 _H - 9FFF FFFF _H	128 Mbyte	xSPI	Access	Access

3.2.10.2 Segment 11

Table 22 Address map of segment 11

Address range	Size	Description	Access type	
			Read	Write
B000 0000 _H - B003 FFFF _H	256 Kbyte	CPU0.DLMU	Access	Access
B004 0000 _H - B005 FFFF _H	128 Kbyte	CPU1.DLMU	Access	Access
B006 0000 _H - B007 FFFF _H	128 Kbyte	CPU2.DLMU	Access	Access
B008 0000 _H - B7FF FFFF _H	-	Reserved	SRIBE	SRIBE
B800 0000 _H - BFFF FFFF _H	128 Mbyte	xSPI	Access	Access

3 System address map

3.2.11 Segment 12

Table 23 Address map of segment 12

Address range	Size	Description	Access type	
			Read	Write
C000 0000 _H - CFFF FFFF _H	-	Reserved ^{1) 2)}	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

2) See the PPU chapter for details

3.2.12 Segment 13

Table 24 Address map of segment 13

Address range	Size	Description	Access type	
			Read	Write
D000 0000 _H - DFFF FFFF _H	-	Reserved ¹⁾	SRIBE	SRIBE

1) See the CPU chapter Local and global addressing sub-chapter for details

3.2.13 Segment 14

Table 25 Address map of segment 14

Address range	Size	Unit	Access type	
			Read	Write
E000 0000 _H - EFFF FFFF _H	-	Reserved	LLIBE	LLIBE

3 System address map

3.2.14 Segment 15

The address map of segment 15 includes the following module address ranges:

- Absolute Addressing Range
 - If a module is addressed in the first 16 Kbyte of segment 15, the CPU can access the module with absolute addressing mode
- Others
 - If a module is addressed above the first 16 Kbyte of segment 15, the CPU can access the module with base + offset

Table 26 Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F000 0000 _H - F000 07FF _H	2 Kbyte	WTU	Access	Access
F000 0800 _H - F000 0FFF _H	–	Reserved	SPBBE	SPBBE
F000 1000 _H - F000 11FF _H	512 Byte	QSPIO	Access	Access
F000 1200 _H - F000 13FF _H	512 Byte	QSPIO1	Access	Access
F000 1400 _H - F000 15FF _H	512 Byte	QSPIO2	Access	Access
F000 1600 _H - F000 17FF _H	512 Byte	QSPIO3	Access	Access
F000 1800 _H - F000 19FF _H	512 Byte	QSPIO4	Access	Access
F000 1A00 _H - F000 1BFF _H	512 Byte	QSPIO5	Access	Access
F000 1C00 _H - F000 1DFF _H	512 Byte	QSPIO6	Access	Access
F000 1E00 _H - F000 1FFF _H	512 Byte	QSPIO7	Access	Access
F000 2000 _H - F000 2DFF _H	–	Reserved	SPBBE	SPBBE
F000 2E00 _H - F000 2FFF _H	512 Byte	FCE0	Access	Access
F000 3000 _H - F000 3BFF _H	6 x 512 Byte	SENT0	Access	Access
F000 3C00 _H - F000 3FFF _H	–	Reserved	SPBBE	SPBBE
F000 4000 _H - F000 4BFF _H	6 x 512 Byte	SENT1	Access	Access
F000 4C00 _H - F000 4FFF _H	–	Reserved	SPBBE	SPBBE
F000 5000 _H - F000 5BFF _H	6 x 512 Byte	PSI5	Access	Access
F000 5C00 _H - F000 FFFF _H	–	Reserved	SPBBE	SPBBE
F001 0000 _H - F001 3FFF _H	16 Kbyte	DMA0	Access	Access
F001 4000 _H - F001 BFFF _H	–	Reserved	SPBBE	SPBBE
F001 C000 _H - F001 CFFF _H	4 Kbyte	ERAY0	Access	Access
F001 D000 _H - F001 FFFF _H	–	Reserved	SPBBE	SPBBE
F002 0000 _H - F002 01FF _H	512 Byte	GPT120	Access	Access
F002 0200 _H - F002 03FF _H	512 Byte	GPT121	Access	Access
F002 0400 _H - F002 05FF _H	512 Byte	GPT122	Access	Access
F002 0600 _H - F002 07FF _H	512 Byte	GPT123	Access	Access

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F002 0800 _H - F002 3FFF _H	–	Reserved	SPBBE	SPBBE
F002 4000 _H - F002 43FF _H	1 Kbyte	SCU	Access	Access
F002 4400 _H - F002 7FFF _H	–	Reserved	SPBBE	SPBBE
F002 8000 _H - F002 9FFF _H	8 Kbyte	SMU	Access	Access
F002 A000 _H - F002 FFFF _H	–	Reserved	SPBBE	SPBBE
F003 0000 _H - F003 1FFF _H	8 Kbyte	IR.SFR	Access	Access
F003 2000 _H - F003 5FFF _H	16 Kbyte	IR.SRC	Access	Access
F003 6000 _H - F003 9FFF _H	–	Reserved	SPBBE	SPBBE
F003 A000 _H - F003 A3FF _H	1 Kbyte	P00	Access	Access
F003 A400 _H - F003 A7FF _H	1 Kbyte	P01	Access	Access
F003 A800 _H - F003 ABFF _H	1 Kbyte	P02	Access	Access
F003 AC00 _H - F003 AFFF _H	1 Kbyte	P03	Access	Access
F003 B000 _H - F003 C7FF _H	–	Reserved	SPBBE	SPBBE
F003 C800 _H - F003 CBFF _H	1 Kbyte	P10	Access	Access
F003 CC00 _H - F003 CFFF _H	1 Kbyte	P11	Access	Access
F003 D000 _H - F003 D3FF _H	1 Kbyte	P12	Access	Access
F003 D400 _H - F003 D7FF _H	1 Kbyte	P13	Access	Access
F003 D800 _H - F003 DBFF _H	1 Kbyte	P14	Access	Access
F003 DC00 _H - F003 DFFF _H	1 Kbyte	P15	Access	Access
F003 E000 _H - F003 E3FF _H	1 Kbyte	P16	Access	Access
F003 E400 _H - F003 EFFF _H	–	Reserved	SPBBE	SPBBE
F003 F000 _H - F003 F3FF _H	1 Kbyte	P20	Access	Access
F003 F400 _H - F003 F7FF _H	1 Kbyte	P21	Access	Access
F003 F800 _H - F003 FBFF _H	1 Kbyte	P22	Access	Access
F003 FC00 _H - F003 FFFF _H	1 Kbyte	P23	Access	Access
F004 0000 _H - F004 1FFF _H	–	Reserved	SPBBE	SPBBE
F004 2000 _H - F004 23FF _H	1 Kbyte	P32	Access	Access
F004 2400 _H - F004 27FF _H	1 Kbyte	P33	Access	Access
F004 2800 _H - F004 2BFF _H	1 Kbyte	P34	Access	Access
F004 2C00 _H - F004 3FFF _H	–	Reserved	SPBBE	SPBBE
F004 4000 _H - F004 43FF _H	1 Kbyte	P40	Access	Access
F004 4400 _H - F005 FFFF _H	–	Reserved	SPBBE	SPBBE

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F006 0000 _H - F006 3FFF _H	16 Kbyte	SMM	Access	Access
F006 4000 _H - F006 7FFF _H	16 Kbyte	CCU	Access	Access
F006 8000 _H - F006 80FF _H	256 Byte	TRI	Access	Access
F006 8100 _H - F006 BFFF _H	–	Reserved	SPBBE	SPBBE
F006 C000 _H - F006 FFFF _H	16 Kbyte	VTMON	Access	Access
F007 0000 _H - F007 FFFF _H	–	Reserved	SPBBE	SPBBE
F008 0000 _H - F008 03FF _H	4 x 256 Byte	HSSL0	Access	Access
F008 0400 _H - F008 FFFF _H	–	Reserved	SPBBE	SPBBE
F009 0000 _H - F009 FFFF _H	64 Kbyte	HSCT0	Access	Access
F00A 0000 _H - F00B FFFF _H	–	Reserved	SPBBE	SPBBE
F00C 0000 _H - F00D FFFF _H	128 Kbyte	I2C0	Access	Access
F00E 0000 _H - F00F FFFF _H	128 Kbyte	I2C1	Access	Access
F010 0000 _H - F023 FFFF _H	–	Reserved	SPBBE	SPBBE
F024 0000 _H - F024 7FFF _H	32 Kbyte	SCR.XRAM	Access	Access
F024 8000 _H - F024 8FFF _H	–	Reserved	SPBBE	SPBBE
F024 9000 _H - F024 9FFF _H	4 Kbyte	PMS	Access	Access
F024 A000 _H - F02A FFFF _H	–	Reserved	SPBBE	SPBBE
F02B 0000 _H - F02B 0FFF _H	4 Kbyte	SDMMC0	Access	Access
F02B 1000 _H - F02B FFFF _H	–	Reserved	SPBBE	SPBBE
F02C 0000 _H - F02C 01FF _H	512 Byte	ASCLIN0	Access	Access
F02C 0200 _H - F02C 03FF _H	512 Byte	ASCLIN1	Access	Access
F02C 0400 _H - F02C 05FF _H	512 Byte	ASCLIN2	Access	Access
F02C 0600 _H - F02C 07FF _H	512 Byte	ASCLIN3	Access	Access
F02C 0800 _H - F02C 09FF _H	512 Byte	ASCLIN4	Access	Access
F02C 0A00 _H - F02C 0BFF _H	512 Byte	ASCLIN5	Access	Access
F02C 0C00 _H - F02C 0DFF _H	512 Byte	ASCLIN6	Access	Access
F02C 0E00 _H - F02C 0FFF _H	512 Byte	ASCLIN7	Access	Access
F02C 1000 _H - F02C 11FF _H	512 Byte	ASCLIN8	Access	Access
F02C 1200 _H - F02C 13FF _H	512 Byte	ASCLIN9	Access	Access
F02C 1400 _H - F02C 15FF _H	512 Byte	ASCLIN10	Access	Access
F02C 1600 _H - F02C 17FF _H	512 Byte	ASCLIN11	Access	Access
F02C 1800 _H - F02C 19FF _H	512 Byte	ASCLIN12	Access	Access

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F02C 1A00 _H - F02C 1BFF _H	512 Byte	ASCLIN13	Access	Access
F02C 1C00 _H - F02C 1DFF _H	512 Byte	ASCLIN14	Access	Access
F02C 1E00 _H - F02C 1FFF _H	512 Byte	ASCLIN15	Access	Access
F02C 2000 _H - F02C 21FF _H	512 Byte	ASCLIN16	Access	Access
F02C 2200 _H - F02C 23FF _H	512 Byte	ASCLIN17	Access	Access
F02C 2400 _H - F02C 25FF _H	512 Byte	ASCLIN18	Access	Access
F02C 2600 _H - F02C 27FF _H	512 Byte	ASCLIN19	Access	Access
F02C 2800 _H - F02C 29FF _H	512 Byte	ASCLIN20	Access	Access
F02C 2A00 _H - F02C 2BFF _H	512 Byte	ASCLIN21	Access	Access
F02C 2C00 _H - F02C 2DFF _H	512 Byte	ASCLIN22	Access	Access
F02C 2E00 _H - F02C 2FFF _H	512 Byte	ASCLIN23	Access	Access
F02C 3000 _H - F02F FFFF _H	–	Reserved	SPBBE	SPBBE
F030 0000 _H - F030 8FFF _H	36 Kbyte	CAN0.SRAM	Access	Access
F030 9000 _H - F030 FFFF _H	–	Reserved	SPBBE	SPBBE
F031 0000 _H - F031 10FF _H	4352 Byte	CAN0.SFR	Access	Access
F031 1100 _H - F031 FFFF _H	–	Reserved	SPBBE	SPBBE
F032 0000 _H - F032 4FFF _H	20 Kbyte	CAN1.SRAM	Access	Access
F032 5000 _H - F032 FFFF _H	–	Reserved	SPBBE	SPBBE
F033 0000 _H - F033 10FF _H	4352 Byte	CAN1.SFR	Access	Access
F033 1100 _H - F033 FFFF _H	–	Reserved	SPBBE	SPBBE
F034 0000 _H - F034 4FFF _H	20 Kbyte	CAN2.SRAM	Access	Access
F034 5000 _H - F034 FFFF _H	–	Reserved	SPBBE	SPBBE
F035 0000 _H - F035 10FF _H	4352 Byte	CAN2.SFR	Access	Access
F035 1100 _H - F03F FFFF _H	–	Reserved	SPBBE	SPBBE
F040 0000 _H - F041 FFFF _H	128 Kbyte	VMT0	Access	Access
F042 0000 _H - F043 FFFF _H	128 Kbyte	VMT1	Access	Access
F044 0000 _H - F045 FFFF _H	128 Kbyte	VMT2	Access	Access
F046 0000 _H - F047 FFFF _H	128 Kbyte	VMT3	Access	Access
F048 0000 _H - F049 FFFF _H	128 Kbyte	VMT4	Access	Access
F04A 0000 _H - F04B FFFF _H	128 Kbyte	VMT5	Access	Access
F04C 0000 _H - F05F FFFF _H	–	Reserved	SPBBE	SPBBE
F060 0000 _H - F060 FFFF _H	64 Kbyte	AUDIO	Access	Access

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F061 0000 _H - F17B FFFF _H	–	Reserved	SPBBE	SPBBE
F17C 0000 _H - F17C 1FFF _H	8 Kbyte	CANXL0.SRAM	Access	Access
F17C 2000 _H - F17C FFFF _H	–	Reserved	SPBBE	SPBBE
F17D 0000 _H - F17F FFFF _H	192 Kbyte	CANXL0.SFR	Access	Access
F180 0000 _H - F1FF FBFF _H	–	Reserved	SPBBE	SPBBE
F1FF FC00 _H - F1FF FFFF _H	1 Kbyte	SBCU	Access	Access
F200 0000 _H - F3FF FFFF _H	32 Mbyte	HSPHY	Access	Access
F400 0000 _H - F4FF FFFF _H	–	Reserved	CPBBE	CPBBE
F500 0000 _H - F500 1FFF _H	8 Kbyte	ADC.SFR	Access	Access
F500 2000 _H - F501 F7FF _H	–	Reserved	CPBBE	CPBBE
F501 F800 _H - F501 FFFF _H	2 Kbyte	TMADC.SFR	Access	Access
F502 0000 _H - F502 07FF _H	2 Kbyte	TMADC0 (Read Write)	Access	Access
F502 0800 _H - F502 0FFF _H	2 Kbyte	TMADC1 (Read Write)	Access	Access
F502 1000 _H - F502 17FF _H	2 Kbyte	TMADC2 (Read Write)	Access	Access
F502 1800 _H - F502 1FFF _H	2 Kbyte	TMADC3 (Read Write)	Access	Access
F502 2000 _H - F50B F7FF _H	–	Reserved	CPBBE	CPBBE
F50B F800 _H - F50B FFFF _H	2 Kbyte	CDSP.SFR	Access	Access
F50C 0000 _H - F50C 1FFF _H	8 Kbyte	CDSP0	Access	Access
F50C 2000 _H - F5FF FBFF _H	–	Reserved	CPBBE	CPBBE
F5FF FC00 _H - F5FF FFFF _H	1 Kbyte	CBCU	Access	Access
F600 0000 _H - F600 01FF _H	512 Byte	CSCU	Access	Access
F600 0200 _H - F600 03FF _H	512 Byte	TRNG	Access	Access
F600 0400 _H - F600 FFFF _H	–	Reserved	CSPBBE	CSPBBE
F601 0000 _H - F601 7FFF _H	32 Kbyte	PKC	Access	Access
F601 8000 _H - F6FF FBFF _H	–	Reserved	CSPBBE	CSPBBE
F6FF FC00 _H - F6FF FFFF _H	1 Kbyte	CSBCU	Access	Access
F700 0000 _H - F7FF FFFF _H	–	Reserved	SRIBE	SRIBE
F800 0000 _H - F807 FFFF _H	–	Reserved	SRIBE	SRIBE
F808 0000 _H - F808 7FFF _H	32 Kbyte	NVMR.DMUR0.UR	Access	Access
F808 8000 _H - F80B FFFF _H	–	Reserved	SRIBE	SRIBE
F80C 0000 _H - F80C 7FFF _H	32 Kbyte	NVMR.DMUR1.UR	Access	Access
F80C 8000 _H - F83F FFFF _H	–	Reserved	SRIBE	SRIBE

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F840 0000 _H - F840 0FFF _H	4 Kbyte	CPU0.FSFR	Access	Access
F840 1000 _H - F840 FFFF _H	–	Reserved	SRIBE	SRIBE
F841 0000 _H - F841 7FFF _H	32 Kbyte	NVMR.PMUR00.UR	Access	Access
F841 8000 _H - F841 FFFF _H	–	Reserved	SRIBE	SRIBE
F842 0000 _H - F842 7FFF _H	32 Kbyte	NVMR.PMUR01.UR	Access	Access
F842 8000 _H - F843 FFFF _H	–	Reserved	SRIBE	SRIBE
F844 0000 _H - F844 0FFF _H	4 Kbyte	CPU1.FSFR	Access	Access
F844 1000 _H - F844 FFFF _H	–	Reserved	SRIBE	SRIBE
F845 0000 _H - F845 7FFF _H	32 Kbyte	NVMR.PMUR10.UR	Access	Access
F845 8000 _H - F845 FFFF _H	–	Reserved	SRIBE	SRIBE
F846 0000 _H - F846 7FFF _H	32 Kbyte	NVMR.PMUR11.UR	Access	Access
F846 8000 _H - F847 FFFF _H	–	Reserved	SRIBE	SRIBE
F848 0000 _H - F848 0FFF _H	4 Kbyte	CPU2.FSFR	Access	Access
F848 1000 _H - F848 FFFF _H	–	Reserved	SRIBE	SRIBE
F849 0000 _H - F849 7FFF _H	32 Kbyte	NVMR.PMUR20.UR	Access	Access
F849 8000 _H - F849 FFFF _H	–	Reserved	SRIBE	SRIBE
F84A 0000 _H - F84A 7FFF _H	32 Kbyte	NVMR.PMUR21.UR	Access	Access
F84A 8000 _H - F857 FFFF _H	–	Reserved	SRIBE	SRIBE
F858 0000 _H - F858 0FFF _H	4 Kbyte	CPUcs.FSFR	Access	Access
F858 1000 _H - F858 FFFF _H	–	Reserved	SRIBE	SRIBE
F859 0000 _H - F859 7FFF _H	32 Kbyte	NVMR.PMURcs.UR	Access	Access
F859 8000 _H - F87F FFFF _H	–	Reserved	SRIBE	SRIBE
F880 0000 _H - F880 FFFF _H	64 Kbyte	CPU0.SFR (incl. STM0 and OVL)	Access	Access
F881 0000 _H - F881 FFFF _H	64 Kbyte	CPU0.HR1.CSFR	Access	Access
F882 0000 _H - F882 FFFF _H	64 Kbyte	CPU0.HR2.CSFR	Access	Access
F883 0000 _H - F883 FFFF _H	64 Kbyte	CPU0.HR0.CSFR	Access	Access
F884 0000 _H - F884 FFFF _H	64 Kbyte	CPU1.SFR (incl. STM1 and OVL)	Access	Access
F885 0000 _H - F885 FFFF _H	64 Kbyte	CPU1.HR1.CSFR	Access	Access
F886 0000 _H - F886 FFFF _H	64 Kbyte	CPU1.HR2.CSFR	Access	Access
F887 0000 _H - F887 FFFF _H	64 Kbyte	CPU1.HR0.CSFR	Access	Access
F888 0000 _H - F888 FFFF _H	64 Kbyte	CPU2.SFR (incl. STM2 and OVL)	Access	Access
F889 0000 _H - F889 FFFF _H	64 Kbyte	CPU2.HR1.CSFR	Access	Access

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F88A 0000 _H - F88A FFFF _H	64 Kbyte	CPU2.HR2.CSFR	Access	Access
F88B 0000 _H - F88B FFFF _H	64 Kbyte	CPU2.HR0.CSFR	Access	Access
F88C 0000 _H - F897 FFFF _H	–	Reserved	SRIBE	SRIBE
F898 0000 _H - F898 FFFF _H	64 Kbyte	CPUcs.SFR (incl. STMcs)	Access	Access
F899 0000 _H - F899 FFFF _H	64 Kbyte	CPUcs.HR1.CSFR	Access	Access
F89A 0000 _H - F8CF FFFF _H	–	Reserved	SRIBE	SRIBE
F8D0 0000 _H - F8D4 FFFF _H	320 Kbyte	CSS	Access	Access
F8D5 0000 _H - F8EF FFFF _H	–	Reserved	SRIBE	SRIBE
F8F0 0000 _H - F8F0 FFFF _H	64 Kbyte	SRI0.SFR	Access	Access
F8F1 0000 _H - F8F1 FFFF _H	–	Reserved	SRIBE	SRIBE
F8F2 0000 _H - F8F2 FFFF _H	64 Kbyte	SRI2.SFR	Access	Access
F8F3 0000 _H - F8F3 FFFF _H	–	Reserved	SRIBE	SRIBE
F8F4 0000 _H - F8F4 FFFF _H	64 Kbyte	SRI4.SFR	Access	Access
F8F5 0000 _H - F8FF FFFF _H	–	Reserved	SRIBE	SRIBE
F900 0000 _H - F901 FFFF _H	128 Kbyte	GETH0.SFR	Access	Access
F902 0000 _H - F902 FFFF _H	64 Kbyte	GETH0.RAM	Access	Access
F903 0000 _H - F903 7FFF _H	32 Kbyte	DRE.SFR	Access	Access
F903 8000 _H - F903 FFFF _H	32 Kbyte	DRE.RAM	Access	Access
F904 0000 _H - F904 FFFF _H	64 Kbyte	xSPI.SFR	Access	Access
F905 0000 _H - F907 FFFF _H	–	Reserved	SRIBE	SRIBE
F908 0000 _H - F909 FFFF _H	128 Kbyte	eGTM.CLUSTER0	Access	Access
F90A 0000 _H - F90B FFFF _H	128 Kbyte	eGTM.CLUSTER1	Access	Access
F90C 0000 _H - F90D FFFF _H	128 Kbyte	eGTM.CLUSTER2	Access	Access
F90E 0000 _H - F90E 1FFF _H	8 Kbyte	eGTM.SFR	Access	Access
F90E 2000 _H - F90E 23FF _H	1 Kbyte	HRPWM.SFR	Access	Access
F90E 2400 _H - F90E 27FF _H	1 Kbyte	HRPWM0	Access	Access
F90E 2800 _H - F90E 2BFF _H	1 Kbyte	HRPWM1	Access	Access
F90E 2C00 _H - F90E 2FFF _H	1 Kbyte	HRPWM2	Access	Access
F90E 3000 _H - F93F FFFF _H	–	Reserved	SRIBE	SRIBE
F940 0000 _H - F943 FFFF _H	256 Kbyte	LETH0	Access	Access
F944 0000 _H - F9FF FFFF _H	–	Reserved	SRIBE	SRIBE
FA00 0000 _H - FA01 FFFF _H	128 Kbyte	MCDS4P (incl. 32 Kbyte TBUF)	Access	Access

(table continues...)

3 System address map

Table 26 (continued) Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
FA02 0000 _H - FA0F FFFF _H	–	Reserved	TPBBE	TPBBE
FA10 0000 _H - FA10 1FFF _H	8 Kbyte	TRIF	Access	Access
FA10 2000 _H - FA17 FFFF _H	–	Reserved	TPBBE	TPBBE
FA18 0000 _H - FA18 03FF _H	1 Kbyte	CBS	Access	Access
FA18 0400 _H - FAFF FBFF _H	–	Reserved	TPBBE	TPBBE
FAFF FC00 _H - FAFF FFFF _H	1 Kbyte	TBCU	Access	Access
FB00 0000 _H - FFFF FFFF _H	–	Reserved	SRIBE	SRIBE

3 System address map

3.3 System address map revision history

Initial release of the chapter.