

2 System address map

2.2 Address map

The address maps define which address ranges are valid or invalid for the respective segment. The access type column describes the error status location if the access is not permitted.

For the detailed address space of a module, please see the related chapter.

2.2.1 Segments 0 to 7

Table 7 Address map of segments 0 to 7

Segment	Address range	Size	Description	Access type	
				Read	Write
0	0000 0000 _H - 0FFF FFFF _H	-	Reserved	SRIBE ⁽¹⁾	SRIBE ⁽¹⁾
1	1000 0000 _H - 1003 BFFF _H	240 Kbyte	CPUcs.DSPR	Access	Access
	1003 C000 _H - 1003 FFFF _H	16 Kbyte	CPUcs.DSPR (extension) or CPUcs.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	1004 0000 _H - 100F FFFF _H	-	Reserved	SRIBE	SRIBE
	1010 0000 _H - 1010 FFFF _H	64 Kbyte	CPUcs.PSPR	Access	Access
	1011 0000 _H - 1011 7FFF _H	32 Kbyte	CPUcs.PSPR (extension) or CPUcs.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	1011 8000 _H - 1011 FFFF _H	-	Reserved	SRIBE	SRIBE
	1012 0000 _H - 1012 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
	1013 0000 _H - 1013 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	1013 8000 _H - 1013 FFFF _H	-	Reserved	SRIBE	SRIBE
	1014 0000 _H - 1014 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
	1015 0000 _H - 1015 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	1015 8000 _H - 1015 FFFF _H	-	Reserved	SRIBE	SRIBE
	1016 0000 _H - 1016 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
	1017 0000 _H - 1017 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	1017 8000 _H - 1017 FFFF _H	-	Reserved	SRIBE	SRIBE
	1018 0000 _H - 1018 FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
	1019 0000 _H - 1019 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	1019 8000 _H - 1019 FFFF _H	-	Reserved	SRIBE	SRIBE
	101A 0000 _H - 101A FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
	101B 0000 _H - 101B 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	101B 8000 _H - 101B FFFF _H	-	Reserved	SRIBE	SRIBE
	101C 0000 _H - 101C FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access
	101D 0000 _H - 101D 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	101D 8000 _H - 101D FFFF _H	-	Reserved	SRIBE	SRIBE
	101E 0000 _H - 101E FFFF _H	64 Kbyte	Alias for CPUcs.PSPR	Access	Access

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	101F 0000 _H - 101F 7FFF _H	32 Kbyte	Alias for CPUcs.PSPR (extension)	Access	Access
	101F 8000 _H - 101F FFFF _H	-	Reserved	SRIBE	SRIBE
	1020 0000 _H - 1020 17FF _H	-	CPUcs.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	1020 1800 _H - 102F FFFF _H	-	Reserved	SRIBE	SRIBE
	1030 0000 _H - 1030 2FFF _H	-	CPUcs.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	1030 3000 _H - 1FFF FFFF _H	-	Reserved	SRIBE	SRIBE
2	2000 0000 _H - 2001 BFFF _H	112 Kbyte	CPU5.DSPR	Access	Access
	2001 C000 _H - 2001 FFFF _H	16 Kbyte	CPU5.DSPR (extension) or CPU5.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	2002 0000 _H - 200F FFFF _H	-	Reserved	SRIBE	SRIBE
	2010 0000 _H - 2010 FFFF _H	64 Kbyte	CPU5.PSPR	Access	Access
	2011 0000 _H - 2011 7FFF _H	32 Kbyte	CPU5.PSPR (extension) or CPU5.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	2011 8000 _H - 2011 FFFF _H	-	Reserved	SRIBE	SRIBE
	2012 0000 _H - 2012 FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	2013 0000 _H - 2013 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	2013 8000 _H - 2013 FFFF _H	-	Reserved	SRIBE	SRIBE
	2014 0000 _H - 2014 FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	2015 0000 _H - 2015 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	2015 8000 _H - 2015 FFFF _H	-	Reserved	SRIBE	SRIBE
	2016 0000 _H - 2016 FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	2017 0000 _H - 2017 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	2017 8000 _H - 2017 FFFF _H	-	Reserved	SRIBE	SRIBE
	2018 0000 _H - 2018 FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	2019 0000 _H - 2019 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	2019 8000 _H - 2019 FFFF _H	-	Reserved	SRIBE	SRIBE
	201A 0000 _H - 201A FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	201B 0000 _H - 201B 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	201B 8000 _H - 201B FFFF _H	-	Reserved	SRIBE	SRIBE
	201C 0000 _H - 201C FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	201D 0000 _H - 201D 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	201D 8000 _H - 201D FFFF _H	-	Reserved	SRIBE	SRIBE

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	201E 0000 _H - 201E FFFF _H	64 Kbyte	Alias for CPU5.PSPR	Access	Access
	201F 0000 _H - 201F 7FFF _H	32 Kbyte	Alias for CPU5.PSPR (extension)	Access	Access
	201F 8000 _H - 201F FFFF _H	-	Reserved	SRIBE	SRIBE
	2020 0000 _H - 2020 17FF _H	-	CPU5.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	2020 1800 _H - 202F FFFF _H	-	Reserved	SRIBE	SRIBE
	2030 0000 _H - 2030 2FFF _H	-	CPU5.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	2030 3000 _H - 2FFF FFFF _H	-	Reserved	SRIBE	SRIBE
3	3000 0000 _H - 3001 BFFF _H	112 Kbyte	CPU4.DSPR	Access	Access
	3001 C000 _H - 3001 FFFF _H	16 Kbyte	CPU4.DSPR (extension) or CPU4.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	3002 0000 _H - 300F FFFF _H	-	Reserved	SRIBE	SRIBE
	3010 0000 _H - 3010 FFFF _H	64 Kbyte	CPU4.PSPR	Access	Access
	3011 0000 _H - 3011 7FFF _H	32 Kbyte	CPU4.PSPR (extension) or CPU4.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	3011 8000 _H - 3011 FFFF _H	-	Reserved	SRIBE	SRIBE
	3012 0000 _H - 3012 FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	3013 0000 _H - 3013 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
	3013 8000 _H - 3013 FFFF _H	-	Reserved	SRIBE	SRIBE
	3014 0000 _H - 3014 FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	3015 0000 _H - 3015 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
	3015 8000 _H - 3015 FFFF _H	-	Reserved	SRIBE	SRIBE
	3016 0000 _H - 3016 FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	3017 0000 _H - 3017 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
	3017 8000 _H - 3017 FFFF _H	-	Reserved	SRIBE	SRIBE
	3018 0000 _H - 3018 FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	3019 0000 _H - 3019 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
	3019 8000 _H - 3019 FFFF _H	-	Reserved	SRIBE	SRIBE
	301A 0000 _H - 301A FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	301B 0000 _H - 301B 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
	301B 8000 _H - 301B FFFF _H	-	Reserved	SRIBE	SRIBE
	301C 0000 _H - 301C FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	301D 0000 _H - 301D 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	301D 8000 _H - 301D FFFF _H	-	Reserved	SRIBE	SRIBE
	301E 0000 _H - 301E FFFF _H	64 Kbyte	Alias for CPU4.PSPR	Access	Access
	301F 0000 _H - 301F 7FFF _H	32 Kbyte	Alias for CPU4.PSPR (extension)	Access	Access
	301F 8000 _H - 301F FFFF _H	-	Reserved	SRIBE	SRIBE
	3020 0000 _H - 3020 17FF _H	-	CPU4.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	3020 1800 _H - 302F FFFF _H	-	Reserved	SRIBE	SRIBE
	3030 0000 _H - 3030 2FFF _H	-	CPU4.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	3030 3000 _H - 3FFF FFFF _H	-	Reserved	SRIBE	SRIBE
4	4000 0000 _H - 4001 BFFF _H	112 Kbyte	CPU3.DSPR	Access	Access
	4001 C000 _H - 4001 FFFF _H	16 Kbyte	CPU3.DSPR (extension) or CPU3.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	4002 0000 _H - 400F FFFF _H	-	Reserved	SRIBE	SRIBE
	4010 0000 _H - 4010 FFFF _H	64 Kbyte	CPU3.PSPR	Access	Access
	4011 0000 _H - 4011 7FFF _H	32 Kbyte	CPU3.PSPR (extension) or CPU3.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	4011 8000 _H - 4011 FFFF _H	-	Reserved	SRIBE	SRIBE
	4012 0000 _H - 4012 FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access
	4013 0000 _H - 4013 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	4013 8000 _H - 4013 FFFF _H	-	Reserved	SRIBE	SRIBE
	4014 0000 _H - 4014 FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access
	4015 0000 _H - 4015 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	4015 8000 _H - 4015 FFFF _H	-	Reserved	SRIBE	SRIBE
	4016 0000 _H - 4016 FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access
	4017 0000 _H - 4017 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	4017 8000 _H - 4017 FFFF _H	-	Reserved	SRIBE	SRIBE
	4018 0000 _H - 4018 FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access
	4019 0000 _H - 4019 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	4019 8000 _H - 4019 FFFF _H	-	Reserved	SRIBE	SRIBE
	401A 0000 _H - 401A FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access
	401B 0000 _H - 401B 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	401B 8000 _H - 401B FFFF _H	-	Reserved	SRIBE	SRIBE
	401C 0000 _H - 401C FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	401D 0000 _H - 401D 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	401D 8000 _H - 401D FFFF _H	-	Reserved	SRIBE	SRIBE
	401E 0000 _H - 401E FFFF _H	64 Kbyte	Alias for CPU3.PSPR	Access	Access
	401F 0000 _H - 401F 7FFF _H	32 Kbyte	Alias for CPU3.PSPR (extension)	Access	Access
	401F 8000 _H - 401F FFFF _H	-	Reserved	SRIBE	SRIBE
	4020 0000 _H - 4020 17FF _H	-	CPU3.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	4020 1800 _H - 402F FFFF _H	-	Reserved	SRIBE	SRIBE
	4030 0000 _H - 4030 2FFF _H	-	CPU3.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	4030 3000 _H - 4FFF FFFF _H	-	Reserved	SRIBE	SRIBE
5	5000 0000 _H - 5003 BFFF _H	240 Kbyte	CPU2.DSPR	Access	Access
	5003 C000 _H - 5003 FFFF _H	16 Kbyte	CPU2.DSPR (extension) or CPU2.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	5004 0000 _H - 500F FFFF _H	-	Reserved	SRIBE	SRIBE
	5010 0000 _H - 5010 FFFF _H	64 Kbyte	CPU2.PSPR	Access	Access
	5011 0000 _H - 5011 7FFF _H	32 Kbyte	CPU2.PSPR (extension) or CPU2.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	5011 8000 _H - 5011 FFFF _H	-	Reserved	SRIBE	SRIBE
	5012 0000 _H - 5012 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	5013 0000 _H - 5013 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	5013 8000 _H - 5013 FFFF _H	-	Reserved	SRIBE	SRIBE
	5014 0000 _H - 5014 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	5015 0000 _H - 5015 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	5015 8000 _H - 5015 FFFF _H	-	Reserved	SRIBE	SRIBE
	5016 0000 _H - 5016 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	5017 0000 _H - 5017 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	5017 8000 _H - 5017 FFFF _H	-	Reserved	SRIBE	SRIBE
	5018 0000 _H - 5018 FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	5019 0000 _H - 5019 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	5019 8000 _H - 5019 FFFF _H	-	Reserved	SRIBE	SRIBE
	501A 0000 _H - 501A FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	501B 0000 _H - 501B 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	501B 8000 _H - 501B FFFF _H	-	Reserved	SRIBE	SRIBE

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	501C 0000 _H - 501C FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	501D 0000 _H - 501D 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	501D 8000 _H - 501D FFFF _H	-	Reserved	SRIBE	SRIBE
	501E 0000 _H - 501E FFFF _H	64 Kbyte	Alias for CPU2.PSPR	Access	Access
	501F 0000 _H - 501F 7FFF _H	32 Kbyte	Alias for CPU2.PSPR (extension)	Access	Access
	501F 8000 _H - 501F FFFF _H	-	Reserved	SRIBE	SRIBE
	5020 0000 _H - 5020 17FF _H	-	CPU2.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	5020 1800 _H - 502F FFFF _H	-	Reserved	SRIBE	SRIBE
	5030 0000 _H - 5030 2FFF _H	-	CPU2.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	5030 3000 _H - 5FFF FFFF _H	-	Reserved	SRIBE	SRIBE
6	6000 0000 _H - 6003 BFFF _H	240 Kbyte	CPU1.DSPR	Access	Access
	6003 C000 _H - 6003 FFFF _H	16 Kbyte	CPU1.DSPR (extension) or CPU1.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	6004 0000 _H - 600F FFFF _H	-	Reserved	SRIBE	SRIBE
	6010 0000 _H - 6010 FFFF _H	64 Kbyte	CPU1.PSPR	Access	Access
	6011 0000 _H - 6011 7FFF _H	32 Kbyte	CPU1.PSPR (extension) or CPU1.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	6011 8000 _H - 6011 FFFF _H	-	Reserved	SRIBE	SRIBE
	6012 0000 _H - 6012 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	6013 0000 _H - 6013 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
	6013 8000 _H - 6013 FFFF _H	-	Reserved	SRIBE	SRIBE
	6014 0000 _H - 6014 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	6015 0000 _H - 6015 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
	6015 8000 _H - 6015 FFFF _H	-	Reserved	SRIBE	SRIBE
	6016 0000 _H - 6016 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	6017 0000 _H - 6017 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
	6017 8000 _H - 6017 FFFF _H	-	Reserved	SRIBE	SRIBE
	6018 0000 _H - 6018 FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	6019 0000 _H - 6019 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
	6019 8000 _H - 6019 FFFF _H	-	Reserved	SRIBE	SRIBE
	601A 0000 _H - 601A FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	601B 0000 _H - 601B 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	601B 8000 _H - 601B FFFF _H	-	Reserved	SRIBE	SRIBE
	601C 0000 _H - 601C FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	601D 0000 _H - 601D 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
	601D 8000 _H - 601D FFFF _H	-	Reserved	SRIBE	SRIBE
	601E 0000 _H - 601E FFFF _H	64 Kbyte	Alias for CPU1.PSPR	Access	Access
	601F 0000 _H - 601F 7FFF _H	32 Kbyte	Alias for CPU1.PSPR (extension)	Access	Access
	601F 8000 _H - 601F FFFF _H	-	Reserved	SRIBE	SRIBE
	6020 0000 _H - 6020 17FF _H	-	CPU1.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	6020 1800 _H - 602F FFFF _H	-	Reserved	SRIBE	SRIBE
	6030 0000 _H - 6030 2FFF _H	-	CPU1.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	6030 3000 _H - 6FFF FFFF _H	-	Reserved	SRIBE	SRIBE
7	7000 0000 _H - 7003 BFFF _H	240 Kbyte	CPU0.DSPR	Access	Access
	7003 C000 _H - 7003 FFFF _H	16 Kbyte	CPU0.DSPR (extension) or CPU0.DCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	7004 0000 _H - 700F FFFF _H	-	Reserved	SRIBE	SRIBE
	7010 0000 _H - 7010 FFFF _H	64 Kbyte	CPU0.PSPR	Access	Access
	7011 0000 _H - 7011 7FFF _H	32 Kbyte	CPU0.PSPR (extension) or CPU0.PCACHE (RAM)	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	7011 8000 _H - 7011 FFFF _H	-	Reserved	SRIBE	SRIBE
	7012 0000 _H - 7012 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
	7013 0000 _H - 7013 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	7013 8000 _H - 7013 FFFF _H	-	Reserved	SRIBE	SRIBE
	7014 0000 _H - 7014 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
	7015 0000 _H - 7015 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	7015 8000 _H - 7015 FFFF _H	-	Reserved	SRIBE	SRIBE
	7016 0000 _H - 7016 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
	7017 0000 _H - 7017 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	7017 8000 _H - 7017 FFFF _H	-	Reserved	SRIBE	SRIBE
	7018 0000 _H - 7018 FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
	7019 0000 _H - 7019 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	7019 8000 _H - 7019 FFFF _H	-	Reserved	SRIBE	SRIBE
	701A 0000 _H - 701A FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access

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Table 7 Address map of segments 0 to 7 (continued)

Segment	Address range	Size	Description	Access type	
				Read	Write
	701B 0000 _H - 701B 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	701B 8000 _H - 701B FFFF _H	-	Reserved	SRIBE	SRIBE
	701C 0000 _H - 701C FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
	701D 0000 _H - 701D 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	701D 8000 _H - 701D FFFF _H	-	Reserved	SRIBE	SRIBE
	701E 0000 _H - 701E FFFF _H	64 Kbyte	Alias for CPU0.PSPR	Access	Access
	701F 0000 _H - 701F 7FFF _H	32 Kbyte	Alias for CPU0.PSPR (extension)	Access	Access
	701F 8000 _H - 701F FFFF _H	-	Reserved	SRIBE	SRIBE
	7020 0000 _H - 7020 17FF _H	-	CPU0.DTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	7020 1800 _H - 702F FFFF _H	-	Reserved	SRIBE	SRIBE
	7030 0000 _H - 7030 2FFF _H	-	CPU0.PTAG	Access ⁽²⁾ / SRIBE	Access ⁽²⁾ / SRIBE
	7030 3000 _H - 7FFF FFFF _H	-	Reserved	SRIBE	SRIBE
8	8000 0000 _H - 8FFF FFFF _H	256 Mbyte	See Segments 8 and 10		
9	9000 0000 _H - 9FFF FFFF _H	256 Mbyte	See Segments 9 and 11		
10	A000 0000 _H - AFFF FFFF _H	256 Mbyte	See Segments 8 and 10		
11	B000 0000 _H - BFFF FFFF _H	256 Mbyte	See Segments 9 and 11		
12	C000 0000 _H - CFFF FFFF _H	256 Mbyte	See Segment 12		
13	D000 0000 _H - DFFF FFFF _H	256 Mbyte	See Segment 13		
14	E000 0000 _H - EFFF FFFF _H	256 Mbyte	See Segment 14		
15	F000 0000 _H - FFFF FFFF _H	256 Mbyte	See Segment 15		

(1) Any CPU load or store operation accessing 0000 0000_H will trap

(2) PCACHE/PTAG and DCACHE/DTAG can be only accessed when mapped into the address space

2 System address map

2.2.2 Segments 8 and 10

The read address of the program flash and erase counters varies for the following modes:

- Linear address map
- SOTA address map A
- SOTA address map B

2.2.2.1 TC49x linear address map of segment 8

Table 8 TC49x linear address map of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
8020 0000 _H - 803F FFFF _H	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
8040 0000 _H - 805F FFFF _H	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
8060 0000 _H - 807F FFFF _H	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
8080 0000 _H - 809F FFFF _H	2 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
80A0 0000 _H - 80BF FFFF _H	2 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
80C0 0000 _H - 80DF FFFF _H	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
80E0 0000 _H - 80FF FFFF _H	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
8100 0000 _H - 811F FFFF _H	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
8120 0000 _H - 813F FFFF _H	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
8140 0000 _H - 815F FFFF _H	2 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
8160 0000 _H - 817F FFFF _H	2 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
8180 0000 _H - 83FF FFFF _H	-	Reserved	SRIBE	SRIBE
8400 0000 _H - 840F FFFF _H	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
8410 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVM.BROM	Access	SRIBE

2 System address map

2.2.2.2 TC49x linear address map of segment 10

Table 9 TC49x linear address map of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
A020 0000 _H - A03F FFFF _H	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
A040 0000 _H - A05F FFFF _H	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
A060 0000 _H - A07F FFFF _H	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
A080 0000 _H - A09F FFFF _H	2 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
A0A0 0000 _H - A0BF FFFF _H	2 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
A0C0 0000 _H - A0DF FFFF _H	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
A0E0 0000 _H - A0FF FFFF _H	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
A100 0000 _H - A11F FFFF _H	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
A120 0000 _H - A13F FFFF _H	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
A140 0000 _H - A15F FFFF _H	2 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
A160 0000 _H - A17F FFFF _H	2 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
A180 0000 _H - A3FF FFFF _H	-	Reserved	SRIBE	SRIBE
A400 0000 _H - A40F FFFF _H	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
A410 0000 _H - A7FF FFFF _H	-	Reserved	SRIBE	SRIBE
A800 0000 _H - A800 3FFF _H	16 Kbyte	NVM.PFLASH00 (Erase Counter)	Access	SRIBE
A800 4000 _H - A81F FFFF _H	-	Reserved	SRIBE	SRIBE
A820 0000 _H - A820 3FFF _H	16 Kbyte	NVM.PFLASH01 (Erase Counter)	Access	SRIBE
A820 4000 _H - A83F FFFF _H	-	Reserved	SRIBE	SRIBE
A840 0000 _H - A840 3FFF _H	16 Kbyte	NVM.PFLASH10 (Erase Counter)	Access	SRIBE
A840 4000 _H - A85F FFFF _H	-	Reserved	SRIBE	SRIBE
A860 0000 _H - A860 3FFF _H	16 Kbyte	NVM.PFLASH11 (Erase Counter)	Access	SRIBE
A860 4000 _H - A87F FFFF _H	-	Reserved	SRIBE	SRIBE
A880 0000 _H - A880 3FFF _H	16 Kbyte	NVM.PFLASH20 (Erase Counter)	Access	SRIBE
A880 4000 _H - A89F FFFF _H	-	Reserved	SRIBE	SRIBE
A8A0 0000 _H - A8A0 3FFF _H	16 Kbyte	NVM.PFLASH21 (Erase Counter)	Access	SRIBE
A8A0 4000 _H - A8BF FFFF _H	-	Reserved	SRIBE	SRIBE
A8C0 0000 _H - A8C0 3FFF _H	16 Kbyte	NVM.PFLASH30 (Erase Counter)	Access	SRIBE
A8C0 4000 _H - A8DF FFFF _H	-	Reserved	SRIBE	SRIBE
A8E0 0000 _H - A8E0 3FFF _H	16 Kbyte	NVM.PFLASH31 (Erase Counter)	Access	SRIBE
A8E0 4000 _H - A8FF FFFF _H	-	Reserved	SRIBE	SRIBE
A900 0000 _H - A900 3FFF _H	16 Kbyte	NVM.PFLASH40 (Erase Counter)	Access	SRIBE

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Table 9 TC49x linear address map of segment 10 (continued)

Address range	Size	Description	Access type	
			Read	Write
A900 4000 _H - A91F FFFF _H	-	Reserved	SRIBE	SRIBE
A920 0000 _H - A920 3FFF _H	16 Kbyte	NVM.PFLASH41 (Erase Counter)	Access	SRIBE
A920 4000 _H - A93F FFFF _H	-	Reserved	SRIBE	SRIBE
A940 0000 _H - A940 3FFF _H	16 Kbyte	NVM.PFLASH50 (Erase Counter)	Access	SRIBE
A940 4000 _H - A95F FFFF _H	-	Reserved	SRIBE	SRIBE
A960 0000 _H - A960 3FFF _H	16 Kbyte	NVM.PFLASH51 (Erase Counter)	Access	SRIBE
A960 4000 _H - ABFF FFFF _H	-	Reserved	SRIBE	SRIBE
AC00 0000 _H - AC00 3FFF _H	16 Kbyte	NVM.PFLASHcs (Erase Counter)	Access	SRIBE
AC00 4000 _H - ADFF FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE0F FFFF _H	1024 Kbyte	NVM.EEPROM0	Access	SRIBE
AE10 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE41 3FFF _H	80 Kbyte	NVM.UCB0	Access	SRIBE
AE41 4000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE81 FFFF _H	128 Kbyte	NVM.EEPROM1	Access	SRIBE
AE82 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 CFFF _H	52 Kbyte	NVM.UCB1	Access	SRIBE
AEC0 D000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVM.BROM	Access	SRIBE

2 System address map

2.2.2.3 TC49x SOTA address map A of segment 8

Table 10 TC49x SOTA address map A of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
8020 0000 _H - 803F FFFF _H	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
8040 0000 _H - 805F FFFF _H	2 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
8060 0000 _H - 807F FFFF _H	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
8080 0000 _H - 809F FFFF _H	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
80A0 0000 _H - 80BF FFFF _H	2 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
80C0 0000 _H - 81FF FFFF _H	-	Reserved	SRIBE	SRIBE
8200 0000 _H - 821F FFFF _H	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
8220 0000 _H - 823F FFFF _H	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
8240 0000 _H - 825F FFFF _H	2 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
8260 0000 _H - 827F FFFF _H	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
8280 0000 _H - 829F FFFF _H	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
82A0 0000 _H - 82BF FFFF _H	2 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
82C0 0000 _H - 83FF FFFF _H	-	Reserved	SRIBE	SRIBE
8400 0000 _H - 840F FFFF _H	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
8410 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVM.BROM	Access	SRIBE

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2.2.2.4 TC49x SOTA address map A of segment 10

Table 11 TC49x SOTA address map A of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
A020 0000 _H - A03F FFFF _H	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
A040 0000 _H - A05F FFFF _H	2 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
A060 0000 _H - A07F FFFF _H	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
A080 0000 _H - A09F FFFF _H	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
A0A0 0000 _H - A0BF FFFF _H	2 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
A0C0 0000 _H - A1FF FFFF _H	-	Reserved	SRIBE	SRIBE
A200 0000 _H - A21F FFFF _H	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
A220 0000 _H - A23F FFFF _H	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
A240 0000 _H - A25F FFFF _H	2 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
A260 0000 _H - A27F FFFF _H	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
A280 0000 _H - A29F FFFF _H	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
A2A0 0000 _H - A2BF FFFF _H	2 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
A2C0 0000 _H - A3FF FFFF _H	-	Reserved	SRIBE	SRIBE
A400 0000 _H - A40F FFFF _H	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
A410 0000 _H - A7FF FFFF _H	-	Reserved	SRIBE	SRIBE
A800 0000 _H - A800 3FFF _H	16 Kbyte	NVM.PFLASH00 (Erase Counter)	Access	SRIBE
A800 4000 _H - A81F FFFF _H	-	Reserved	SRIBE	SRIBE
A820 0000 _H - A820 3FFF _H	16 Kbyte	NVM.PFLASH10 (Erase Counter)	Access	SRIBE
A820 4000 _H - A83F FFFF _H	-	Reserved	SRIBE	SRIBE
A840 0000 _H - A840 3FFF _H	16 Kbyte	NVM.PFLASH20 (Erase Counter)	Access	SRIBE
A840 4000 _H - A85F FFFF _H	-	Reserved	SRIBE	SRIBE
A860 0000 _H - A860 3FFF _H	16 Kbyte	NVM.PFLASH30 (Erase Counter)	Access	SRIBE
A860 4000 _H - A87F FFFF _H	-	Reserved	SRIBE	SRIBE
A880 0000 _H - A880 3FFF _H	16 Kbyte	NVM.PFLASH40 (Erase Counter)	Access	SRIBE
A880 4000 _H - A89F FFFF _H	-	Reserved	SRIBE	SRIBE
A8A0 0000 _H - A8A0 3FFF _H	16 Kbyte	NVM.PFLASH50 (Erase Counter)	Access	SRIBE
A8A0 4000 _H - A9FF FFFF _H	-	Reserved	SRIBE	SRIBE
AA00 0000 _H - AA00 3FFF _H	16 Kbyte	NVM.PFLASH01 (Erase Counter)	Access	SRIBE
AA00 4000 _H - AA1F FFFF _H	-	Reserved	SRIBE	SRIBE
AA20 0000 _H - AA20 3FFF _H	16 Kbyte	NVM.PFLASH11 (Erase Counter)	Access	SRIBE
AA20 4000 _H - AA3F FFFF _H	-	Reserved	SRIBE	SRIBE

2 System address map

Table 11 TC49x SOTA address map A of segment 10 (continued)

Address range	Size	Description	Access type	
			Read	Write
AA40 0000 _H - AA40 3FFF _H	16 Kbyte	NVM.PFLASH21 (Erase Counter)	Access	SRIBE
AA40 4000 _H - AA5F FFFF _H	-	Reserved	SRIBE	SRIBE
AA60 0000 _H - AA60 3FFF _H	16 Kbyte	NVM.PFLASH31 (Erase Counter)	Access	SRIBE
AA60 4000 _H - AA7F FFFF _H	-	Reserved	SRIBE	SRIBE
AA80 0000 _H - AA80 3FFF _H	16 Kbyte	NVM.PFLASH41 (Erase Counter)	Access	SRIBE
AA80 4000 _H - AA9F FFFF _H	-	Reserved	SRIBE	SRIBE
AAA0 0000 _H - AAA0 3FFF _H	16 Kbyte	NVM.PFLASH51 (Erase Counter)	Access	SRIBE
AAA0 4000 _H - ABFF FFFF _H	-	Reserved	SRIBE	SRIBE
AC00 0000 _H - AC00 3FFF _H	16 Kbyte	NVM.PFLASHcs (Erase Counter)	Access	SRIBE
AC00 4000 _H - ADFE FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE0F FFFF _H	1024 Kbyte	NVM.EEPROM0	Access	SRIBE
AE10 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE41 3FFF _H	80 Kbyte	NVM.UCB0	Access	SRIBE
AE41 4000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE81 FFFF _H	128 Kbyte	NVM.EEPROM1	Access	SRIBE
AE82 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 CFFF _H	52 Kbyte	NVM.UCB1	Access	SRIBE
AEC0 D000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVM.BROM	Access	SRIBE

2 System address map

2.2.2.5 TC49x SOTA address map B of segment 8

Table 12 TC49x SOTA address map B of segment 8

Address range	Size	Description	Access type	
			Read	Write
8000 0000 _H - 801F FFFF _H	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
8020 0000 _H - 803F FFFF _H	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
8040 0000 _H - 805F FFFF _H	2 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
8060 0000 _H - 807F FFFF _H	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
8080 0000 _H - 809F FFFF _H	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
80A0 0000 _H - 80BF FFFF _H	2 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
80C0 0000 _H - 81FF FFFF _H	-	Reserved	SRIBE	SRIBE
8200 0000 _H - 821F FFFF _H	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
8220 0000 _H - 823F FFFF _H	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
8240 0000 _H - 825F FFFF _H	2 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
8260 0000 _H - 827F FFFF _H	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
8280 0000 _H - 829F FFFF _H	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
82A0 0000 _H - 82BF FFFF _H	2 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
82C0 0000 _H - 83FF FFFF _H	-	Reserved	SRIBE	SRIBE
8400 0000 _H - 840F FFFF _H	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
8410 0000 _H - 8FDF FFFF _H	-	Reserved	SRIBE	SRIBE
8FE0 0000 _H - 8FE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
8FE8 0000 _H - 8FFD FFFF _H	-	Reserved	SRIBE	SRIBE
8FFE 0000 _H - 8FFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
8FFF 0000 _H - 8FFF FFFF _H	64 Kbyte	NVM.BROM	Access	SRIBE

2 System address map

2.2.2.6 TC49x SOTA address map B of segment 10

Table 13 TC49x SOTA address map B of segment 10

Address range	Size	Description	Access type	
			Read	Write
A000 0000 _H - A01F FFFF _H	2 Mbyte	NVM.PFLASH01 (Program Flash)	Access	SRIBE
A020 0000 _H - A03F FFFF _H	2 Mbyte	NVM.PFLASH11 (Program Flash)	Access	SRIBE
A040 0000 _H - A05F FFFF _H	2 Mbyte	NVM.PFLASH21 (Program Flash)	Access	SRIBE
A060 0000 _H - A07F FFFF _H	2 Mbyte	NVM.PFLASH31 (Program Flash)	Access	SRIBE
A080 0000 _H - A09F FFFF _H	2 Mbyte	NVM.PFLASH41 (Program Flash)	Access	SRIBE
A0A0 0000 _H - A0BF FFFF _H	2 Mbyte	NVM.PFLASH51 (Program Flash)	Access	SRIBE
A0C0 0000 _H - A1FF FFFF _H	-	Reserved	SRIBE	SRIBE
A200 0000 _H - A21F FFFF _H	2 Mbyte	NVM.PFLASH00 (Program Flash)	Access	SRIBE
A220 0000 _H - A23F FFFF _H	2 Mbyte	NVM.PFLASH10 (Program Flash)	Access	SRIBE
A240 0000 _H - A25F FFFF _H	2 Mbyte	NVM.PFLASH20 (Program Flash)	Access	SRIBE
A260 0000 _H - A27F FFFF _H	2 Mbyte	NVM.PFLASH30 (Program Flash)	Access	SRIBE
A280 0000 _H - A29F FFFF _H	2 Mbyte	NVM.PFLASH40 (Program Flash)	Access	SRIBE
A2A0 0000 _H - A2BF FFFF _H	2 Mbyte	NVM.PFLASH50 (Program Flash)	Access	SRIBE
A2C0 0000 _H - A3FF FFFF _H	-	Reserved	SRIBE	SRIBE
A400 0000 _H - A40F FFFF _H	1 Mbyte	NVM.PFLASHcs (Program Flash)	Access	SRIBE
A410 0000 _H - A7FF FFFF _H	-	Reserved	SRIBE	SRIBE
A800 0000 _H - A800 3FFF _H	16 Kbyte	NVM.PFLASH01 (Erase Counter)	Access	SRIBE
A800 4000 _H - A81F FFFF _H	-	Reserved	SRIBE	SRIBE
A820 0000 _H - A820 3FFF _H	16 Kbyte	NVM.PFLASH11 (Erase Counter)	Access	SRIBE
A820 4000 _H - A83F FFFF _H	-	Reserved	SRIBE	SRIBE
A840 0000 _H - A840 3FFF _H	16 Kbyte	NVM.PFLASH21 (Erase Counter)	Access	SRIBE
A840 4000 _H - A85F FFFF _H	-	Reserved	SRIBE	SRIBE
A860 0000 _H - A860 3FFF _H	16 Kbyte	NVM.PFLASH31 (Erase Counter)	Access	SRIBE
A860 4000 _H - A87F FFFF _H	-	Reserved	SRIBE	SRIBE
A880 0000 _H - A880 3FFF _H	16 Kbyte	NVM.PFLASH41 (Erase Counter)	Access	SRIBE
A880 4000 _H - A89F FFFF _H	-	Reserved	SRIBE	SRIBE
A8A0 0000 _H - A8A0 3FFF _H	16 Kbyte	NVM.PFLASH51 (Erase Counter)	Access	SRIBE
A8A0 4000 _H - A9FF FFFF _H	-	Reserved	SRIBE	SRIBE
AA00 0000 _H - AA00 3FFF _H	16 Kbyte	NVM.PFLASH00 (Erase Counter)	Access	SRIBE
AA00 4000 _H - AA1F FFFF _H	-	Reserved	SRIBE	SRIBE
AA20 0000 _H - AA20 3FFF _H	16 Kbyte	NVM.PFLASH10 (Erase Counter)	Access	SRIBE
AA20 4000 _H - AA3F FFFF _H	-	Reserved	SRIBE	SRIBE

2 System address map

Table 13 TC49x SOTA address map B of segment 10 (continued)

Address range	Size	Description	Access type	
			Read	Write
AA40 0000 _H - AA40 3FFF _H	16 Kbyte	NVM.PFLASH20 (Erase Counter)	Access	SRIBE
AA40 4000 _H - AA5F FFFF _H	-	Reserved	SRIBE	SRIBE
AA60 0000 _H - AA60 3FFF _H	16 Kbyte	NVM.PFLASH30 (Erase Counter)	Access	SRIBE
AA60 4000 _H - AA7F FFFF _H	-	Reserved	SRIBE	SRIBE
AA80 0000 _H - AA80 3FFF _H	16 Kbyte	NVM.PFLASH40 (Erase Counter)	Access	SRIBE
AA80 4000 _H - AA9F FFFF _H	-	Reserved	SRIBE	SRIBE
AAA0 0000 _H - AAA0 3FFF _H	16 Kbyte	NVM.PFLASH50 (Erase Counter)	Access	SRIBE
AAA0 4000 _H - ABFF FFFF _H	-	Reserved	SRIBE	SRIBE
AC00 0000 _H - AC00 3FFF _H	16 Kbyte	NVM.PFLASHcs (Erase Counter)	Access	SRIBE
AC00 4000 _H - ADFE FFFF _H	-	Reserved	SRIBE	SRIBE
AE00 0000 _H - AE0F FFFF _H	1024 Kbyte	NVM.EEPROM0	Access	SRIBE
AE10 0000 _H - AE3F FFFF _H	-	Reserved	SRIBE	SRIBE
AE40 0000 _H - AE41 3FFF _H	80 Kbyte	NVM.UCB0	Access	SRIBE
AE41 4000 _H - AE7F FFFF _H	-	Reserved	SRIBE	SRIBE
AE80 0000 _H - AE81 FFFF _H	128 Kbyte	NVM.EEPROM1	Access	SRIBE
AE82 0000 _H - AEBF FFFF _H	-	Reserved	SRIBE	SRIBE
AEC0 0000 _H - AEC0 CFFF _H	52 Kbyte	NVM.UCB1	Access	SRIBE
AEC0 D000 _H - AFDF FFFF _H	-	Reserved	SRIBE	SRIBE
AFE0 0000 _H - AFE7 FFFF _H	512 Kbyte	OLDA	SRIBE	Access / SRIBE
AFE8 0000 _H - AFFD FFFF _H	-	Reserved	SRIBE	SRIBE
AFFE 0000 _H - AFFE FFFF _H	64 Kbyte	CSROM	Access	SRIBE
AFFF 0000 _H - AFFF FFFF _H	64 Kbyte	NVM.BROM	Access	SRIBE

2 System address map

2.2.3 Segments 9 and 11

2.2.3.1 TC49x address map of segment 9

Table 14 TC49x address map of segment 9

Address range	Size	Description	Access type	
			Read	Write
9000 0000 _H - 9007 FFFF _H	512 Kbyte	CPU0.DLMU	Access	Access
9008 0000 _H - 900F FFFF _H	512 Kbyte	CPU1.DLMU	Access	Access
9010 0000 _H - 9015 FFFF _H	384 Kbyte	CPU2.DLMU	Access	Access
9016 0000 _H - 9019 FFFF _H	256 Kbyte	CPU3.DLMU	Access	Access
901A 0000 _H - 901D FFFF _H	256 Kbyte	CPU4.DLMU	Access	Access
901E 0000 _H - 9021 FFFF _H	256 Kbyte	CPU5.DLMU	Access	Access
9022 0000 _H - 902F FFFF _H	-	Reserved	SRIBE	SRIBE
9030 0000 _H - 9037 FFFF _H	512 Kbyte	LMU0	Access	Access
9038 0000 _H - 903F FFFF _H	512 Kbyte	LMU1	Access	Access
9040 0000 _H - 9047 FFFF _H	512 Kbyte	LMU2	Access	Access
9048 0000 _H - 904F FFFF _H	512 Kbyte	LMU3	Access	Access
9050 0000 _H - 9057 FFFF _H	512 Kbyte	LMU4	Access	Access
9058 0000 _H - 905F FFFF _H	512 Kbyte	LMU5	Access	Access
9060 0000 _H - 9067 FFFF _H	512 Kbyte	LMU6	Access	Access
9068 0000 _H - 906F FFFF _H	512 Kbyte	LMU7	Access	Access
9070 0000 _H - 91FF FFFF _H	-	Reserved	SRIBE	SRIBE
9200 0000 _H - 9201 FFFF _H	128 Kbyte	CPUs.DLMU	Access	Access
9202 0000 _H - 9207 FFFF _H	-	Reserved	SRIBE	SRIBE
9208 0000 _H - 920B FFFF _H	256 Kbyte	PPU.CSM	Access	Access
920C 0000 _H - 97FF FFFF _H	-	Reserved	SRIBE	SRIBE
9800 0000 _H - 9FFF FFFF _H	128 Mbyte	xSPI	Access	Access

2 System address map

2.2.3.2 TC49x address map of segment 11

Table 15 TC49x address map of segment 11

Address range	Size	Description	Access type	
			Read	Write
B000 0000 _H - B007 FFFF _H	512 Kbyte	CPU0.DLMU	Access	Access
B008 0000 _H - B00F FFFF _H	512 Kbyte	CPU1.DLMU	Access	Access
B010 0000 _H - B015 FFFF _H	384 Kbyte	CPU2.DLMU	Access	Access
B016 0000 _H - B019 FFFF _H	256 Kbyte	CPU3.DLMU	Access	Access
B01A 0000 _H - B01D FFFF _H	256 Kbyte	CPU4.DLMU	Access	Access
B01E 0000 _H - B021 FFFF _H	256 Kbyte	CPU5.DLMU	Access	Access
B022 0000 _H - B02F FFFF _H	-	Reserved	SRIBE	SRIBE
B030 0000 _H - B037 FFFF _H	512 Kbyte	LMU0	Access	Access
B038 0000 _H - B03F FFFF _H	512 Kbyte	LMU1	Access	Access
B040 0000 _H - B047 FFFF _H	512 Kbyte	LMU2	Access	Access
B048 0000 _H - B04F FFFF _H	512 Kbyte	LMU3	Access	Access
B050 0000 _H - B057 FFFF _H	512 Kbyte	LMU4	Access	Access
B058 0000 _H - B05F FFFF _H	512 Kbyte	LMU5	Access	Access
B060 0000 _H - B067 FFFF _H	512 Kbyte	LMU6	Access	Access
B068 0000 _H - B06F FFFF _H	512 Kbyte	LMU7	Access	Access
B070 0000 _H - B1FF FFFF _H	-	Reserved	SRIBE	SRIBE
B200 0000 _H - B201 FFFF _H	128 Kbyte	CPUCs.DLMU	Access	Access
B202 0000 _H - B207 FFFF _H	-	Reserved	SRIBE	SRIBE
B208 0000 _H - B20B FFFF _H	256 Kbyte	PPU.CSM	Access	Access
B20C 0000 _H - B7FF FFFF _H	-	Reserved	SRIBE	SRIBE
B800 0000 _H - BFFF FFFF _H	128 Mbyte	xSPI	Access	Access

2.2.4 Segment 12

Table 16 Address map of segment 12

Address range	Size	Description	Access type	
			Read	Write
C000 0000 _H - CFFF FFFF _H	-	Reserved ⁽¹⁾ ⁽²⁾	SRIBE	SRIBE

(1) See the CPU chapter Local and global addressing sub-chapter for details

(2) See the PPU chapter for details

2 System address map

2.2.5 Segment 13

Table 17 Address map of segment 13

Address range	Size	Description	Access type	
			Read	Write
D000 0000 _H - DFFF FFFF _H	-	Reserved ⁽¹⁾	SRIBE	SRIBE

(1) See the CPU chapter Local and global addressing sub-chapter for details

2.2.6 Segment 14

Table 18 Address map of segment 14

Address range	Size	Unit	Access type	
			Read	Write
E000 0000 _H - E501 FFFF _H	-	Reserved	LLIBE	LLIBE
E502 0000 _H - E502 07FF _H	2 Kbyte	TMADC0 (Read Only)	Access	LLIBE
E502 0800 _H - E502 0FFF _H	2 Kbyte	TMADC1 (Read Only)	Access	LLIBE
E502 1000 _H - E502 17FF _H	2 Kbyte	TMADC2 (Read Only)	Access	LLIBE
E502 1800 _H - E502 1FFF _H	2 Kbyte	TMADC3 (Read Only)	Access	LLIBE
E502 2000 _H - E502 27FF _H	2 Kbyte	TMADC4 (Read Only)	Access	LLIBE
E502 2800 _H - E502 2FFF _H	2 Kbyte	TMADC5 (Read Only)	Access	LLIBE
E502 3000 _H - E502 37FF _H	2 Kbyte	TMADC6 (Read Only)	Access	LLIBE
E502 3800 _H - E502 3FFF _H	2 Kbyte	TMADC7 (Read Only)	Access	LLIBE
E502 4000 _H - E907 FFFF _H	-	Reserved	LLIBE	LLIBE
E908 0000 _H - E909 FFFF _H	128 Kbyte	eGTM.CLUSTER0	Access	Access
E90A 0000 _H - E90B FFFF _H	128 Kbyte	eGTM.CLUSTER1	Access	Access
E90C 0000 _H - E90D FFFF _H	128 Kbyte	eGTM.CLUSTER2	Access	Access
E90E 0000 _H - E90E 1FFF _H	8 Kbyte	eGTM.SFR	Access	Access
E90E 2000 _H - E90E 23FF _H	1 Kbyte	HRPWM.SFR	Access	Access
E90E 2400 _H - E90E 27FF _H	1 Kbyte	HRPWM0	Access	Access
E90E 2800 _H - E90E 2BFF _H	1 Kbyte	HRPWM1	Access	Access
E90E 2C00 _H - E90E 2FFF _H	1 Kbyte	HRPWM2	Access	Access
E90E 3000 _H - E97F FFFF _H	-	Reserved	LLIBE	LLIBE
E980 0000 _H - E980 FFFF _H	64 Kbyte	PPU.SFR	Access	Access
E981 0000 _H - EFFF FFFF _H	-	Reserved	LLIBE	LLIBE

2 System address map

2.2.7 Segment 15

The address map of segment 15 includes the following module address ranges:

- Absolute Addressing Range
 - If a module is addressed in the first 16 Kbyte of segment 15, the CPU can access the module with absolute addressing mode.
- Others
 - If a module is addressed above the first 16 Kbyte of segment 15, the CPU can access the module with base + offset.

Table 19 Address map of segment 15

Address range	Size	Unit	Access type	
			Read	Write
F000 0000 _H - F000 07FF _H	2 Kbyte	WTU	Access	Access
F000 0800 _H - F000 09FF _H	512 Byte	MSC0	Access	Access
F000 0A00 _H - F000 0BFF _H	512 Byte	MSC1	Access	Access
F000 0C00 _H - F000 0DFF _H	512 Byte	MSC2	Access	Access
F000 0E00 _H - F000 0FFF _H	512 Byte	MSC3	Access	Access
F000 1000 _H - F000 11FF _H	512 Byte	QSPI0	Access	Access
F000 1200 _H - F000 13FF _H	512 Byte	QSPI1	Access	Access
F000 1400 _H - F000 15FF _H	512 Byte	QSPI2	Access	Access
F000 1600 _H - F000 17FF _H	512 Byte	QSPI3	Access	Access
F000 1800 _H - F000 19FF _H	512 Byte	QSPI4	Access	Access
F000 1A00 _H - F000 1BFF _H	512 Byte	QSPI5	Access	Access
F000 1C00 _H - F000 1DFF _H	512 Byte	QSPI6	Access	Access
F000 1E00 _H - F000 1FFF _H	512 Byte	QSPI7	Access	Access
F000 2000 _H - F000 2DFF _H	–	Reserved	SPBBE	SPBBE
F000 2E00 _H - F000 2FFF _H	512 Byte	FCE0	Access	Access
F000 3000 _H - F000 3BFF _H	6 x 512 Byte	SENT0	Access	Access
F000 3C00 _H - F000 3FFF _H	–	Reserved	SPBBE	SPBBE
F000 4000 _H - F000 4BFF _H	6 x 512 Byte	SENT1	Access	Access
F000 4C00 _H - F000 4FFF _H	–	Reserved	SPBBE	SPBBE
F000 5000 _H - F000 5BFF _H	6 x 512 Byte	PSI5	Access	Access
F000 5C00 _H - F000 6FFF _H	–	Reserved	SPBBE	SPBBE
F000 7000 _H - F000 7FFF _H	4 Kbyte	PSI5S0	Access	Access
F000 8000 _H - F000 8FFF _H	4 Kbyte	PSI5S1	Access	Access
F000 9000 _H - F000 FFFF _H	–	Reserved	SPBBE	SPBBE
F001 0000 _H - F001 3FFF _H	16 Kbyte	DMA0	Access	Access
F001 4000 _H - F001 7FFF _H	16 Kbyte	DMA1	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F001 8000 _H - F001 BFFF _H	–	Reserved	SPBBE	SPBBE
F001 C000 _H - F001 CFFF _H	4 Kbyte	ERAY0	Access	Access
F001 D000 _H - F001 DFFF _H	4 Kbyte	ERAY1	Access	Access
F001 E000 _H - F001 FFFF _H	–	Reserved	SPBBE	SPBBE
F002 0000 _H - F002 01FF _H	512 Byte	GPT120	Access	Access
F002 0200 _H - F002 03FF _H	512 Byte	GPT121	Access	Access
F002 0400 _H - F002 05FF _H	512 Byte	GPT122	Access	Access
F002 0600 _H - F002 07FF _H	512 Byte	GPT123	Access	Access
F002 0800 _H - F002 3FFF _H	–	Reserved	SPBBE	SPBBE
F002 4000 _H - F002 43FF _H	1 Kbyte	SCU	Access	Access
F002 4400 _H - F002 7FFF _H	–	Reserved	SPBBE	SPBBE
F002 8000 _H - F002 9FFF _H	8 Kbyte	SMU	Access	Access
F002 A000 _H - F002 FFFF _H	–	Reserved	SPBBE	SPBBE
F003 0000 _H - F003 1FFF _H	8 Kbyte	IR.SFR	Access	Access
F003 2000 _H - F003 5FFF _H	16 Kbyte	IR.SRC	Access	Access
F003 6000 _H - F003 9FFF _H	–	Reserved	SPBBE	SPBBE
F003 A000 _H - F003 A3FF _H	1 Kbyte	P00	Access	Access
F003 A400 _H - F003 A7FF _H	1 Kbyte	P01	Access	Access
F003 A800 _H - F003 ABFF _H	1 Kbyte	P02	Access	Access
F003 AC00 _H - F003 AFFF _H	1 Kbyte	P03	Access	Access
F003 B000 _H - F003 B3FF _H	1 Kbyte	P04	Access	Access
F003 B400 _H - F003 C7FF _H	–	Reserved	SPBBE	SPBBE
F003 C800 _H - F003 CBFF _H	1 Kbyte	P10	Access	Access
F003 CC00 _H - F003 CFFF _H	1 Kbyte	P11	Access	Access
F003 D000 _H - F003 D3FF _H	1 Kbyte	P12	Access	Access
F003 D400 _H - F003 D7FF _H	1 Kbyte	P13	Access	Access
F003 D800 _H - F003 DBFF _H	1 Kbyte	P14	Access	Access
F003 DC00 _H - F003 DFFF _H	1 Kbyte	P15	Access	Access
F003 E000 _H - F003 E3FF _H	1 Kbyte	P16	Access	Access
F003 E400 _H - F003 E7FF _H	1 Kbyte	P17	Access	Access
F003 E800 _H - F003 EFFF _H	–	Reserved	SPBBE	SPBBE
F003 F000 _H - F003 F3FF _H	1 Kbyte	P20	Access	Access
F003 F400 _H - F003 F7FF _H	1 Kbyte	P21	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F003 F800 _H - F003 FBFF _H	1 Kbyte	P22	Access	Access
F003 FC00 _H - F003 FFFF _H	1 Kbyte	P23	Access	Access
F004 0000 _H - F004 03FF _H	1 Kbyte	P24	Access	Access
F004 0400 _H - F004 07FF _H	1 Kbyte	P25	Access	Access
F004 0800 _H - F004 0BFF _H	1 Kbyte	P26	Access	Access
F004 0C00 _H - F004 17FF _H	–	Reserved	SPBBE	SPBBE
F004 1800 _H - F004 1BFF _H	1 Kbyte	P30	Access	Access
F004 1C00 _H - F004 1FFF _H	1 Kbyte	P31	Access	Access
F004 2000 _H - F004 23FF _H	1 Kbyte	P32	Access	Access
F004 2400 _H - F004 27FF _H	1 Kbyte	P33	Access	Access
F004 2800 _H - F004 2BFF _H	1 Kbyte	P34	Access	Access
F004 2C00 _H - F004 3FFF _H	–	Reserved	SPBBE	SPBBE
F004 4000 _H - F004 43FF _H	1 Kbyte	P40	Access	Access
F004 4400 _H - F004 47FF _H	1 Kbyte	P41	Access	Access
F004 4800 _H - F005 FFFF _H	–	Reserved	SPBBE	SPBBE
F006 0000 _H - F006 3FFF _H	16 Kbyte	SMM	Access	Access
F006 4000 _H - F006 7FFF _H	16 Kbyte	CCU	Access	Access
F006 8000 _H - F006 BFFF _H	16 Kbyte	TRI	Access	Access
F006 C000 _H - F006 FFFF _H	16 Kbyte	VTMON	Access	Access
F007 0000 _H - F007 FFFF _H	–	Reserved	SPBBE	SPBBE
F008 0000 _H - F008 03FF _H	4 x 256 Byte	HSSL0	Access	Access
F008 0400 _H - F008 FFFF _H	–	Reserved	SPBBE	SPBBE
F009 0000 _H - F009 FFFF _H	64 Kbyte	HSCT0	Access	Access
F00A 0000 _H - F00A 03FF _H	4 x 256 Byte	HSSL1	Access	Access
F00A 0400 _H - F00A FFFF _H	–	Reserved	SPBBE	SPBBE
F00B 0000 _H - F00B FFFF _H	64 Kbyte	HSCT1	Access	Access
F00C 0000 _H - F00D FFFF _H	128 Kbyte	I2C0	Access	Access
F00E 0000 _H - F00F FFFF _H	128 Kbyte	I2C1	Access	Access
F010 0000 _H - F023 FFFF _H	–	Reserved	SPBBE	SPBBE
F024 0000 _H - F024 7FFF _H	32 Kbyte	SCR.XRAM	Access	Access
F024 8000 _H - F024 8FFF _H	4 Kbyte	SCR.PER.SFR	Access	Access
F024 9000 _H - F024 9FFF _H	4 Kbyte	PMS	Access	Access
F024 A000 _H - F02A 7FFF _H	–	Reserved	SPBBE	SPBBE

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F02A 8000 _H - F02A FFFF _H	32 Kbyte	PCIe.SFR	Access	Access
F02B 0000 _H - F02B 0FFF _H	4 Kbyte	SDMMC0	Access	Access
F02B 1000 _H - F02B FFFF _H	–	Reserved	SPBBE	SPBBE
F02C 0000 _H - F02C 01FF _H	512 Byte	ASCLIN0	Access	Access
F02C 0200 _H - F02C 03FF _H	512 Byte	ASCLIN1	Access	Access
F02C 0400 _H - F02C 05FF _H	512 Byte	ASCLIN2	Access	Access
F02C 0600 _H - F02C 07FF _H	512 Byte	ASCLIN3	Access	Access
F02C 0800 _H - F02C 09FF _H	512 Byte	ASCLIN4	Access	Access
F02C 0A00 _H - F02C 0BFF _H	512 Byte	ASCLIN5	Access	Access
F02C 0C00 _H - F02C 0DFF _H	512 Byte	ASCLIN6	Access	Access
F02C 0E00 _H - F02C 0FFF _H	512 Byte	ASCLIN7	Access	Access
F02C 1000 _H - F02C 11FF _H	512 Byte	ASCLIN8	Access	Access
F02C 1200 _H - F02C 13FF _H	512 Byte	ASCLIN9	Access	Access
F02C 1400 _H - F02C 15FF _H	512 Byte	ASCLIN10	Access	Access
F02C 1600 _H - F02C 17FF _H	512 Byte	ASCLIN11	Access	Access
F02C 1800 _H - F02C 19FF _H	512 Byte	ASCLIN12	Access	Access
F02C 1A00 _H - F02C 1BFF _H	512 Byte	ASCLIN13	Access	Access
F02C 1C00 _H - F02C 1DFF _H	512 Byte	ASCLIN14	Access	Access
F02C 1E00 _H - F02C 1FFF _H	512 Byte	ASCLIN15	Access	Access
F02C 2000 _H - F02C 21FF _H	512 Byte	ASCLIN16	Access	Access
F02C 2200 _H - F02C 23FF _H	512 Byte	ASCLIN17	Access	Access
F02C 2400 _H - F02C 25FF _H	512 Byte	ASCLIN18	Access	Access
F02C 2600 _H - F02C 27FF _H	512 Byte	ASCLIN19	Access	Access
F02C 2800 _H - F02C 29FF _H	512 Byte	ASCLIN20	Access	Access
F02C 2A00 _H - F02C 2BFF _H	512 Byte	ASCLIN21	Access	Access
F02C 2C00 _H - F02C 2DFF _H	512 Byte	ASCLIN22	Access	Access
F02C 2E00 _H - F02C 2FFF _H	512 Byte	ASCLIN23	Access	Access
F02C 3000 _H - F02C 31FF _H	512 Byte	ASCLIN24	Access	Access
F02C 3200 _H - F02C 33FF _H	512 Byte	ASCLIN25	Access	Access
F02C 3400 _H - F02C 35FF _H	512 Byte	ASCLIN26	Access	Access
F02C 3600 _H - F02C 37FF _H	512 Byte	ASCLIN27	Access	Access
F02C 3800 _H - F02F FFFF _H	–	Reserved	SPBBE	SPBBE
F030 0000 _H - F030 8FFF _H	36 Kbyte	CAN0.SRAM	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F030 9000 _H - F030 FFFF _H	–	Reserved	SPBBE	SPBBE
F031 0000 _H - F031 10FF _H	4352 Byte	CAN0.SFR	Access	Access
F031 1100 _H - F031 FFFF _H	–	Reserved	SPBBE	SPBBE
F032 0000 _H - F032 4FFF _H	20 Kbyte	CAN1.SRAM	Access	Access
F032 5000 _H - F032 FFFF _H	–	Reserved	SPBBE	SPBBE
F033 0000 _H - F033 10FF _H	4352 Byte	CAN1.SFR	Access	Access
F033 1100 _H - F033 FFFF _H	–	Reserved	SPBBE	SPBBE
F034 0000 _H - F034 4FFF _H	20 Kbyte	CAN2.SRAM	Access	Access
F034 5000 _H - F034 FFFF _H	–	Reserved	SPBBE	SPBBE
F035 0000 _H - F035 10FF _H	4352 Byte	CAN2.SFR	Access	Access
F035 1100 _H - F035 FFFF _H	–	Reserved	SPBBE	SPBBE
F036 0000 _H - F036 4FFF _H	20 Kbyte	CAN3.SRAM	Access	Access
F036 5000 _H - F036 FFFF _H	–	Reserved	SPBBE	SPBBE
F037 0000 _H - F037 10FF _H	4352 Byte	CAN3.SFR	Access	Access
F037 1100 _H - F037 FFFF _H	–	Reserved	SPBBE	SPBBE
F038 0000 _H - F038 4FFF _H	20 Kbyte	CAN4.SRAM	Access	Access
F038 5000 _H - F038 FFFF _H	–	Reserved	SPBBE	SPBBE
F039 0000 _H - F039 10FF _H	4352 Byte	CAN4.SFR	Access	Access
F039 1100 _H - F03F FFFF _H	–	Reserved	SPBBE	SPBBE
F040 0000 _H - F041 FFFF _H	128 Kbyte	DMTU0	Access	Access
F042 0000 _H - F043 FFFF _H	128 Kbyte	DMTU1	Access	Access
F044 0000 _H - F045 FFFF _H	128 Kbyte	DMTU2	Access	Access
F046 0000 _H - F047 FFFF _H	128 Kbyte	DMTU3	Access	Access
F048 0000 _H - F049 FFFF _H	128 Kbyte	DMTU4	Access	Access
F04A 0000 _H - F04B FFFF _H	128 Kbyte	DMTU5	Access	Access
F04C 0000 _H - F04D FFFF _H	128 Kbyte	DMTU6	Access	Access
F04E 0000 _H - F04F FFFF _H	128 Kbyte	DMTU7	Access	Access
F050 0000 _H - F1FF FDFH	–	Reserved	SPBBE	SPBBE
F1FF FE00 _H - F1FF FFFF _H	512 Byte	SBCU	Access	Access
F200 0000 _H - F3FF FFFF _H	32 Mbyte	HSPHY	Access	Access
F400 0000 _H - F41F FFFF _H	2 Mbyte	GTM	Access	Access
F420 0000 _H - F4FF FFFF _H	–	Reserved	CPBBE	CPBBE
F500 0000 _H - F500 1FFF _H	8 Kbyte	ADC.SFR	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F500 2000 _H - F501 F7FF _H	–	Reserved	CPBBE	CPBBE
F501 F800 _H - F501 FFFF _H	2 Kbyte	TMADC.SFR	Access	Access
F502 0000 _H - F502 07FF _H	2 Kbyte	TMADC0 (Read Write)	Access	Access
F502 0800 _H - F502 0FFF _H	2 Kbyte	TMADC1 (Read Write)	Access	Access
F502 1000 _H - F502 17FF _H	2 Kbyte	TMADC2 (Read Write)	Access	Access
F502 1800 _H - F502 1FFF _H	2 Kbyte	TMADC3 (Read Write)	Access	Access
F502 2000 _H - F502 27FF _H	2 Kbyte	TMADC4 (Read Write)	Access	Access
F502 2800 _H - F502 2FFF _H	2 Kbyte	TMADC5 (Read Write)	Access	Access
F502 3000 _H - F502 37FF _H	2 Kbyte	TMADC6 (Read Write)	Access	Access
F502 3800 _H - F502 3FFF _H	2 Kbyte	TMADC7 (Read Write)	Access	Access
F502 4000 _H - F503 F7FF _H	–	Reserved	CPBBE	CPBBE
F503 F800 _H - F503 FFFF _H	2 Kbyte	FCC.SFR	Access	Access
F504 0000 _H - F504 03FF _H	1 Kbyte	FCC0	Access	Access
F504 0400 _H - F504 07FF _H	1 Kbyte	FCC1	Access	Access
F504 0800 _H - F504 0BFF _H	1 Kbyte	FCC2	Access	Access
F504 0C00 _H - F504 0FFF _H	1 Kbyte	FCC3	Access	Access
F504 1000 _H - F504 13FF _H	1 Kbyte	FCC4	Access	Access
F504 1400 _H - F504 17FF _H	1 Kbyte	FCC5	Access	Access
F504 1800 _H - F504 1BFF _H	1 Kbyte	FCC6	Access	Access
F504 1C00 _H - F504 1FFF _H	1 Kbyte	FCC7	Access	Access
F504 2000 _H - F504 23FF _H	1 Kbyte	FCC8	Access	Access
F504 2400 _H - F504 27FF _H	1 Kbyte	FCC9	Access	Access
F504 2800 _H - F505 F7FF _H	–	Reserved	CPBBE	CPBBE
F505 F800 _H - F505 FFFF _H	2 Kbyte	DSADC.SFR	Access	Access
F506 0000 _H - F506 03FF _H	1 Kbyte	DSADC0	Access	Access
F506 0400 _H - F506 07FF _H	1 Kbyte	DSADC1	Access	Access
F506 0800 _H - F506 0BFF _H	1 Kbyte	DSADC2	Access	Access
F506 0C00 _H - F506 0FFF _H	1 Kbyte	DSADC3	Access	Access
F506 1000 _H - F506 13FF _H	1 Kbyte	DSADC4	Access	Access
F506 1400 _H - F506 17FF _H	1 Kbyte	DSADC5	Access	Access
F506 1800 _H - F506 1BFF _H	1 Kbyte	DSADC6	Access	Access
F506 1C00 _H - F506 1FFF _H	1 Kbyte	DSADC7	Access	Access
F506 2000 _H - F506 23FF _H	1 Kbyte	DSADC8	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F506 2400 _H - F506 27FF _H	1 Kbyte	DSADC9	Access	Access
F506 2800 _H - F506 2BFF _H	1 Kbyte	DSADC10	Access	Access
F506 2C00 _H - F506 2FFF _H	1 Kbyte	DSADC11	Access	Access
F506 3000 _H - F506 33FF _H	1 Kbyte	DSADC12	Access	Access
F506 3400 _H - F506 37FF _H	1 Kbyte	DSADC13	Access	Access
F506 3800 _H - F506 3BFF _H	1 Kbyte	EXMOD0	Access	Access
F506 3C00 _H - F506 3FFF _H	1 Kbyte	EXMOD1	Access	Access
F506 4000 _H - F506 43FF _H	1 Kbyte	EXMOD2	Access	Access
F506 4400 _H - F506 47FF _H	1 Kbyte	EXMOD3	Access	Access
F506 4800 _H - F50B F7FF _H	–	Reserved	CPBBE	CPBBE
F50B F800 _H - F50B FFFF _H	2 Kbyte	CDSP.SFR	Access	Access
F50C 0000 _H - F50C 1FFF _H	8 Kbyte	CDSP0	Access	Access
F50C 2000 _H - F50C 3FFF _H	8 Kbyte	CDSP1	Access	Access
F50C 4000 _H - F50C 5FFF _H	8 Kbyte	CDSP2	Access	Access
F50C 6000 _H - F50C 7FFF _H	8 Kbyte	CDSP3	Access	Access
F50C 8000 _H - F50C 9FFF _H	8 Kbyte	CDSP4	Access	Access
F50C A000 _H - F50C BFFF _H	8 Kbyte	CDSP5	Access	Access
F50C C000 _H - F50C DFFF _H	8 Kbyte	CDSP6	Access	Access
F50C E000 _H - F50C FFFF _H	8 Kbyte	CDSP7	Access	Access
F50D 0000 _H - F50D 1FFF _H	8 Kbyte	CDSP8	Access	Access
F50D 2000 _H - F50D 3FFF _H	8 Kbyte	CDSP9	Access	Access
F50D 4000 _H - F50D 5FFF _H	8 Kbyte	CDSP10	Access	Access
F50D 6000 _H - F50D 7FFF _H	8 Kbyte	CDSP11	Access	Access
F50D 8000 _H - F50D 9FFF _H	8 Kbyte	CDSP12	Access	Access
F50D A000 _H - F50D BFFF _H	8 Kbyte	CDSP13	Access	Access
F50D C000 _H - F50D DFFF _H	8 Kbyte	CDSP14	Access	Access
F50D E000 _H - F50D FFFF _H	8 Kbyte	CDSP15	Access	Access
F50E 0000 _H - F50E 1FFF _H	8 Kbyte	CDSP16	Access	Access
F50E 2000 _H - F50E 3FFF _H	8 Kbyte	CDSP17	Access	Access
F50E 4000 _H - F5FF FDFH _H	–	Reserved	CPBBE	CPBBE
F5FF FE00 _H - F5FF FFFF _H	512 Byte	CBCU	Access	Access
F600 0000 _H - F600 01FF _H	512 Byte	CSCU	Access	Access
F600 0200 _H - F600 03FF _H	512 Byte	TRNG	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F600 0400 _H - F600 FFFF _H	–	Reserved	CSPBBE	CSPBBE
F601 0000 _H - F601 7FFF _H	32 Kbyte	PKC	Access	Access
F601 8000 _H - F6FF FDFD _H	–	Reserved	CSPBBE	CSPBBE
F6FF FE00 _H - F6FF FFFF _H	512 Byte	CSBCU	Access	Access
F700 0000 _H - F7FF FFFF _H	–	Reserved	CSPBBE	CSPBBE
F800 0000 _H - F803 FFFF _H	–	Reserved	SRIBE	SRIBE
F804 0000 _H - F807 FFFF _H	256 Kbyte	NVM.DMU (incl. SFR)	Access	Access
F808 0000 _H - F80B FFFF _H	256 Kbyte	NVM.DMU (Host Cmd. Seq. Interpreter)	SRIBE	Access
F80C 0000 _H - F80F FFFF _H	256 Kbyte	NVM.DMU (CS Cmd. Seq. Interpreter)	SRIBE	Access
F810 0000 _H - F83F FFFF _H	–	Reserved	SRIBE	SRIBE
F840 0000 _H - F840 0FFF _H	4 Kbyte	CPU0.FSFR	Access	Access
F840 1000 _H - F840 FFFF _H	–	Reserved	SRIBE	SRIBE
F841 0000 _H - F841 3FFF _H	16 Kbyte	NVM.PFRWB00.UR	Access	Access
F841 4000 _H - F841 7FFF _H	–	Reserved	SRIBE	SRIBE
F841 8000 _H - F841 BFFF _H	16 Kbyte	NVM.PFRWB01.UR	Access	Access
F841 C000 _H - F843 FFFF _H	–	Reserved	SRIBE	SRIBE
F844 0000 _H - F844 0FFF _H	4 Kbyte	CPU1.FSFR	Access	Access
F844 1000 _H - F844 FFFF _H	–	Reserved	SRIBE	SRIBE
F845 0000 _H - F845 3FFF _H	16 Kbyte	NVM.PFRWB10.UR	Access	Access
F845 4000 _H - F845 7FFF _H	–	Reserved	SRIBE	SRIBE
F845 8000 _H - F845 BFFF _H	16 Kbyte	NVM.PFRWB11.UR	Access	Access
F845 C000 _H - F847 FFFF _H	–	Reserved	SRIBE	SRIBE
F848 0000 _H - F848 0FFF _H	4 Kbyte	CPU2.FSFR	Access	Access
F848 1000 _H - F848 FFFF _H	–	Reserved	SRIBE	SRIBE
F849 0000 _H - F849 3FFF _H	16 Kbyte	NVM.PFRWB20.UR	Access	Access
F849 4000 _H - F849 7FFF _H	–	Reserved	SRIBE	SRIBE
F849 8000 _H - F849 BFFF _H	16 Kbyte	NVM.PFRWB21.UR	Access	Access
F849 C000 _H - F84B FFFF _H	–	Reserved	SRIBE	SRIBE
F84C 0000 _H - F84C 0FFF _H	4 Kbyte	CPU3.FSFR	Access	Access
F84C 1000 _H - F84C FFFF _H	–	Reserved	SRIBE	SRIBE
F84D 0000 _H - F84D 3FFF _H	16 Kbyte	NVM.PFRWB30.UR	Access	Access
F84D 4000 _H - F84D 7FFF _H	–	Reserved	SRIBE	SRIBE
F84D 8000 _H - F84D BFFF _H	16 Kbyte	NVM.PFRWB31.UR	Access	Access

2 System address map

Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F84D C000 _H - F84F FFFF _H	–	Reserved	SRIBE	SRIBE
F850 0000 _H - F850 0FFF _H	4 Kbyte	CPU4.FSFR	Access	Access
F850 1000 _H - F850 FFFF _H	–	Reserved	SRIBE	SRIBE
F851 0000 _H - F851 3FFF _H	16 Kbyte	NVM.PFRWB40.UR	Access	Access
F851 4000 _H - F851 7FFF _H	–	Reserved	SRIBE	SRIBE
F851 8000 _H - F851 BFFF _H	16 Kbyte	NVM.PFRWB41.UR	Access	Access
F851 C000 _H - F853 FFFF _H	–	Reserved	SRIBE	SRIBE
F854 0000 _H - F854 0FFF _H	4 Kbyte	CPU5.FSFR	Access	Access
F854 1000 _H - F854 FFFF _H	–	Reserved	SRIBE	SRIBE
F855 0000 _H - F855 3FFF _H	16 Kbyte	NVM.PFRWB50.UR	Access	Access
F855 4000 _H - F855 7FFF _H	–	Reserved	SRIBE	SRIBE
F855 8000 _H - F855 BFFF _H	16 Kbyte	NVM.PFRWB51.UR	Access	Access
F855 C000 _H - F857 FFFF _H	–	Reserved	SRIBE	SRIBE
F858 0000 _H - F858 0FFF _H	4 Kbyte	CPUcs.FSFR	Access	Access
F858 1000 _H - F858 FFFF _H	–	Reserved	SRIBE	SRIBE
F859 0000 _H - F859 3FFF _H	16 Kbyte	NVM.PFRWBcs.UR	Access	Access
F859 4000 _H - F87F FFFF _H	–	Reserved	SRIBE	SRIBE
F880 0000 _H - F880 FFFF _H	64 Kbyte	CPU0.SFR (incl. STM0 and OVL)	Access	Access
F881 0000 _H - F881 FFFF _H	64 Kbyte	CPU0.HR1.CSFR	Access	Access
F882 0000 _H - F882 FFFF _H	64 Kbyte	CPU0.HR2.CSFR	Access	Access
F883 0000 _H - F883 FFFF _H	64 Kbyte	CPU0.HR0.CSFR	Access	Access
F884 0000 _H - F884 FFFF _H	64 Kbyte	CPU1.SFR (incl. STM1 and OVL)	Access	Access
F885 0000 _H - F885 FFFF _H	64 Kbyte	CPU1.HR1.CSFR	Access	Access
F886 0000 _H - F886 FFFF _H	64 Kbyte	CPU1.HR2.CSFR	Access	Access
F887 0000 _H - F887 FFFF _H	64 Kbyte	CPU1.HR0.CSFR	Access	Access
F888 0000 _H - F888 FFFF _H	64 Kbyte	CPU2.SFR (incl. STM2 and OVL)	Access	Access
F889 0000 _H - F889 FFFF _H	64 Kbyte	CPU2.HR1.CSFR	Access	Access
F88A 0000 _H - F88A FFFF _H	64 Kbyte	CPU2.HR2.CSFR	Access	Access
F88B 0000 _H - F88B FFFF _H	64 Kbyte	CPU2.HR0.CSFR	Access	Access
F88C 0000 _H - F88C FFFF _H	64 Kbyte	CPU3.SFR (incl. STM3 and OVL)	Access	Access
F88D 0000 _H - F88D FFFF _H	64 Kbyte	CPU3.HR1.CSFR	Access	Access
F88E 0000 _H - F88E FFFF _H	64 Kbyte	CPU3.HR2.CSFR	Access	Access
F88F 0000 _H - F88F FFFF _H	64 Kbyte	CPU3.HR0.CSFR	Access	Access

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Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
F890 0000 _H - F890 FFFF _H	64 Kbyte	CPU4.SFR (incl. STM4 and OVL)	Access	Access
F891 0000 _H - F891 FFFF _H	64 Kbyte	CPU4.HR1.CSFR	Access	Access
F892 0000 _H - F892 FFFF _H	64 Kbyte	CPU4.HR2.CSFR	Access	Access
F893 0000 _H - F893 FFFF _H	64 Kbyte	CPU4.HR0.CSFR	Access	Access
F894 0000 _H - F894 FFFF _H	64 Kbyte	CPU5.SFR (incl. STM5 and OVL)	Access	Access
F895 0000 _H - F895 FFFF _H	64 Kbyte	CPU5.HR1.CSFR	Access	Access
F896 0000 _H - F896 FFFF _H	64 Kbyte	CPU5.HR2.CSFR	Access	Access
F897 0000 _H - F897 FFFF _H	64 Kbyte	CPU5.HR0.CSFR	Access	Access
F898 0000 _H - F898 FFFF _H	64 Kbyte	CPUcs.SFR (incl. STMcs and OVL)	Access	Access
F899 0000 _H - F899 FFFF _H	64 Kbyte	CPUcs.HR1.CSFR	Access	Access
F89A 0000 _H - F89A FFFF _H	64 Kbyte	CPUcs.HR2.CSFR	Access	Access
F89B 0000 _H - F89B FFFF _H	64 Kbyte	CPUcs.HR0.CSFR	Access	Access
F89C 0000 _H - F8CF FFFF _H	–	Reserved	SRIBE	SRIBE
F8D0 0000 _H - F8D4 FFFF _H	320 Kbyte	CSS	Access	Access
F8D5 0000 _H - F8EF FFFF _H	–	Reserved	SRIBE	SRIBE
F8F0 0000 _H - F8F0 FFFF _H	64 Kbyte	SRI0.SFR	Access	Access
F8F1 0000 _H - F8F1 FFFF _H	64 Kbyte	SRI1.SFR	Access	Access
F8F2 0000 _H - F8F2 FFFF _H	64 Kbyte	SRI2.SFR	Access	Access
F8F3 0000 _H - F8F3 FFFF _H	64 Kbyte	SRI3.SFR	Access	Access
F8F4 0000 _H - F8F4 FFFF _H	64 Kbyte	SRI4.SFR	Access	Access
F8F5 0000 _H - F8FF FFFF _H	–	Reserved	SRIBE	SRIBE
F900 0000 _H - F901 FFFF _H	128 Kbyte	XGETH.SFR	Access	Access
F902 0000 _H - F902 FFFF _H	64 Kbyte	XGETH.RAM	Access	Access
F903 0000 _H - F903 7FFF _H	32 Kbyte	DRE.SFR	Access	Access
F903 8000 _H - F903 FFFF _H	32 Kbyte	DRE.RAM	Access	Access
F904 0000 _H - F904 FFFF _H	64 Kbyte	xSPI.SFR	Access	Access
F905 0000 _H - F980 FFFF _H	–	Reserved	SRIBE	SRIBE
F981 0000 _H - F981 FFFF _H	64 Kbyte	PPU.STUDMI	Access	Access
F982 0000 _H - F982 FFFF _H	64 Kbyte	PPU.DEBUG	Access	Access
F983 0000 _H - F983 FFFF _H	64 Kbyte	PPU.SAFETY	Access	Access
F984 0000 _H - F984 FFFF _H	64 Kbyte	PPU.AP	Access	Access
F985 0000 _H - F985 FFFF _H	64 Kbyte	PPU.CSM.AP	Access	Access
F986 0000 _H - F9FF FFFF _H	–	Reserved	SRIBE	SRIBE

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Table 19 Address map of segment 15 (continued)

Address range	Size	Unit	Access type	
			Read	Write
FA00 0000 _H - FA01 FFFF _H	128 Kbyte	MCDS4P (incl. TBUF32k)	Access	Access
FA02 0000 _H - FA03 FFFF _H	–	Reserved	TPBBE	TPBBE
FA04 0000 _H - FA05 FFFF _H	128 Kbyte	MCDS2P (incl. TBUF16k)	Access	Access
FA06 0000 _H - FA0F FFFF _H	–	Reserved	TPBBE	TPBBE
FA10 0000 _H - FA10 1FFF _H	8 Kbyte	TRIF	Access	Access
FA10 2000 _H - FA17 FFFF _H	–	Reserved	TPBBE	TPBBE
FA18 0000 _H - FA18 03FF _H	1 Kbyte	CBS	Access	Access
FA18 0400 _H - FAFF FDFD _H	–	Reserved	TPBBE	TPBBE
FAFF FE00 _H - FAFF FFFF _H	512 Byte	TBCU	Access	Access
FB00 0000 _H - FB00 FFFF _H	64 Kbyte	LMU0.SFR	Access	Access
FB01 0000 _H - FB01 FFFF _H	64 Kbyte	LMU1.SFR	Access	Access
FB02 0000 _H - FB02 FFFF _H	64 Kbyte	LMU2.SFR	Access	Access
FB03 0000 _H - FB03 FFFF _H	64 Kbyte	LMU3.SFR	Access	Access
FB04 0000 _H - FB04 FFFF _H	64 Kbyte	LMU4.SFR	Access	Access
FB05 0000 _H - FB05 FFFF _H	64 Kbyte	LMU5.SFR	Access	Access
FB06 0000 _H - FB06 FFFF _H	64 Kbyte	LMU6.SFR	Access	Access
FB07 0000 _H - FB07 FFFF _H	64 Kbyte	LMU7.SFR	Access	Access
FB08 0000 _H - FBFF FFFF _H	–	Reserved	SRIBE	SRIBE
FC00 0000 _H - FFFF FFFF _H	64 Mbyte	PCIe.DATA	Access	Access